Hard Disk Drive Specification

Ultrastar 15K300

3.5 inch SCSI Hard Disk Drive

Models: HUS153030VL3800
HUS153014VL3800
HUS153073VL3800

Version 1.0 09 April 2007

Warning: Printed copies of this document are considered current only on the date of print. Replacement and disposal of down-level versions is the responsibility of the document holder.

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1st Edition (Rev. 1.0) (09 April 2007)

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1.0 General

1.1 Introduction

This document describes the specifications of the following Hitachi 3.5 inch SCSI drives.

Table 1: Product ID table

Product ID	Description
HUS153030VL3800	300 GB, SCSI
HUS153014VL3800	147 GB, SCSI
HUS153073VL3800	73 GB, SCSI

Note: The specifications in this document are subject to change without notice.

For technical and ordering information, please visit our website at http://www.hitachigst.com.

1.2 Glossary

Word	**Meaning**
BMS	Background Media Scan
Kb	Kilobit = 1000 bits
Mb	Megabit = 1,000,000 bits
GB	Gigabyte = 1,000,000,000 bits
HDD	Hard Disk Drive
MB	Megabyte = 1,000,000 bytes
KB	Kilobyte = 1000 bytes
PFA	Predictive Failure Analysis
SAS	Serial Attached SCSI
S.M.A.R.T.	Self-Monitoring and Reporting Technology
LVD	Low Voltage Differential SCSI
FC-AL	Fibre Channel - Arbitrated Loop

1.3 Caution

This drive can be damaged by electrostatic discharge (ESD). Any damages incurred to the drive after its removal from the shipping package and the ESD protective bag are the responsibility of the user.

2.0 Outline of the drive

- Storage capacities of 300 GB, 147 GB, and 73 GB
- Ultra 320
- Variable Sector Size (512-528 bytes/sector in multiples of 2)
- Tagged Command Queuing support
- Automatic read/write data transfer
- 3.6 ms seek time in read operation for 300 GB
- 3.4 ms seek time in read operation for 147 GB
- 3.4 ms seek time in read operation for 73 GB
- Adaptive read ahead algorithm
- Write Cache
- Back to back write
- ECC on the fly
- Automatic defect reallocation
- Self diagnostics at power on
- Closed loop actuator servo
- Non head disk contact start stop
- Spindle rotation of 15,000 RPM
- Automatic actuator lock
- PFA (SMART)
- ANSI T10 Protection Information (End-to-End)

3.0 Fixed-disk Subsystem Description

3.1 Control Electronics

The drive is electronically controlled by a microprocessor, logic modules, digital/analog modules and various drivers and receivers. The control electronics perform the following major functions:

- Perform self-checkout (diagnostics)
- Conduct a power-up sequence and calibrate the servo.
- Monitor various timers for head settling, servo failure, etc.
- Analyze servo signals to provide closed-loop control. These include position error signal and estimated velocity.
- Control of the voice coil motor driver to align the actuator onto a desired position
- Monitor the actuator position and determine the target track for a seek operation.
- Constantly monitor error conditions of the servo and take corresponding action if an error occurs.
- Control starting, stopping, and rotating speed of the spindle.
- Control and interpretation of all interface signals between the host controller and the drive
- Control of read/write accessing of the disk media, including defect management and error recovery

3.2 Head Disk Assembly

The head/disk assembly (HDA) is assembled in a clean room environment and contains disks, a spindle motor, actuator assembly, and voice coil motor. Air is constantly circulated and filtered when the drive is operational. Venting of the HDA is accomplished via a breather filter.

The spindle is driven directly by a brushless, sensorless DC drive motor. Dynamic braking is used to stop the spindle quickly.

3.3 Actuator

The read/write heads are mounted in the actuator. The actuator is a swing-arm assembly driven by a voice coil motor. A closed-loop positioning servo controls the movement of the actuator. An embedded servo data pattern supplies feedback to the positioning servo to keep the read/write heads centered over the desired track.

The actuator assembly is balanced to allow vertical or horizontal mounting without adjustment.

Heads are moved out from the disks (unloaded) to protect the disk data during shipping, moving, or storage. At power down, the heads are automatically unloaded from over the disk area and the head actuator locking mechanism will secure the heads in the unload position.

4.0 Drive characteristics

4.1 Formatted capacity

Table 2: Formatted capacity

Description	HUS153030VL3800	HUS153014VL3800	HUS153073VL3800
Label capacity	300 GB	147 GB	73 GB
Number of heads	8	4	2
Number of disks	4	2	1
Total data bytes (512 bytes/sector)	300,000,739,328	147,015,821,824	73,407,900,160
Total logical data blocks	585,937,500 (22ECB25C)	287,140,277 (111D69B5h)	143,374,805 (88BB9D5h)

4.2 Data sheet

Table 3: Data sheet

Buffer to/from media	864 - 1441 Mb/sec
Host to/from buffer (interface transfer rate)	320 [MB/sec]
Data buffer size	16MB
Number of buffer segments	1 - 254
Rotational speed	15,000 RPM
Recording density	824.5 [Kbpi] (Max)
Track density	137,000 [TPI] (average)
Areal density	113 [Gb/sq. in]
Data zone	20

4.3 Inquiry information

4.3.1 Product ID

Product ID in section 17.5.1.1, "Inquiry Data format - CmdDt = 0, EVPD = 0, Page code = 0" on page 76. is as follows:

Table 4: Product ID in INQUIRY Command

Product ID	Description
HUS153030VL3800	300 GB, 80-pin
HUS153014VL3800	147 GB, 80-pin
HUS153073VL3800	73 GB, 80-pin

4.3.2 Worldwide ID - Block assignment

Block assignment of Worldwide ID is as follows:

Table 5: Block assignment of worldwide ID in INQUIRY Command

Manufacturing site	Product Name and Associtated Models	Block assignment
Singapore	HUS153030VL3800 HUS153014VL3800 HUS153073VL3800	001h(1)

Note (1) - Additional block assignment will be issued as needed based on actual production volume

4.4 Cylinder allocation

Table 6: Cylinder allocation

Cylinder Allocation (all models)				
Zone	Sectors/Track	Cyln/zone	Start Cyln	End Cyln
0	1080	14817	1	14818
1	1041	2503	14819	17321
2	1026	4806	17322	22127
3	1012	3904	22128	26032
4	990	5106	26033	31138
5	972	2303	31139	33441
6	918	7109	33442	40550
7	900	6608	40551	47158
8	877	3304	47159	50462
9	864	1501	50464	51964
10	855	601	51965	52565
11	810	10012	52566	62578
12	765	2803	62579	65381
13	756	1702	65382	67083
14	742	2203	67084	69286
15	720	4004	69287	73291
16	702	2103	73292	75394
17	675	4704	75395	80099
18	648	1401	80101	81501
19	630	1802	81502	83303

Note: Values shown are nominal. Actual values will vary based on manufacturing optimization. Mode Page 03 (Format Device Parameters), page 117 and SectionMode Page 0C (Notch Parameters), page 125 provide methods to determine actual medium format and zone parameters for specific drives.

4.5 Performance characteristics

Drive performance is characterized by the following parameters:

- •Command overhead
- •Mechanical head positioning
  - Seek time
  - Latency
- •Data transfer speed
- •Buffering operation (read ahead/write cache)

Note: All the above parameters contribute to drive performance. There are other parameters that contribute to the performance of the actual system. This specification tries to define the bare drive characteristics, not system throughput, which depends on the system and the application.

4.5.1 Mechanical positioning

4.5.1.1 Average seek time (including settling)

Table 7: Mechanical positioning performance

Model	Command	**Typical (ms)**	**Max**
300 GB	Read	3.6	4.7
	Write	4.1	5.1
147 GB	Read	3.4	4.6
	Write	3.8	5.1
73 GB	Read	3.4	4.6
	Write	3.9	5.0

“Typical” and “Max” are used throughout this document and are defined as follows:

Typical Average of the drive population tested at nominal environmental and voltage conditions.

Max Maximum value measured on any one drive over the full range of the environmental and voltage conditions. (See Section 8.0, “Environment” on page 25 and Section 9.0, “DC power requirements” on page 29 for ranges.)

Seek time is measured from the start of the actuator’s motion to the start of a read or write operation. Average seek time is measured as the weighted average of all possible seek combinations.

Weighted average =

$$\frac{\sum_{n=1}^{Max} = (max + 1 - n) \cdot (Tnin + Tnout)}{(max + 1) \cdot (max)}$$

Where:

max = Maximum seek length
n = Seek length (1 to max)
Tn.in = Inward measured seek time for an n track seek
Tn.out = Outward measured seek time for an n track seek

4.5.1.2 Full stroke seek time

Table 8: Full stroke seek time

Model	Command	Typical (ms)	Max
300 GB	Read	6.6	11.1
	Write	7.1	11.5
147 GB	Read	6.5	11.0
	Write	6.8	11.6
73 GB	Read	6.5	11.0
	Write	6.9	11.4

Full stroke seek is measured as the average of 1,000 full stroke seeks with a random head switch from both directions (inward and outward).

4.5.1.3 Average latency

Table 9: Latency time

Rotation	Time for a revolution (ms)	Average latency (ms)
15,000 RPM	4.0	2.0

4.5.2 Drive ready time

Table 10: Drive ready time

Model	**Typical (sec)**	**Maximum (sec)**
300 GB Model	21.0	29.9
147 GB Model	11.5	29.9
73 GB Model	7.0	29.9

4.5.3 Spindle stop time

Table 11: Spindle stop time

Model	**Typical (sec)**	**Maximum (sec)**
300 GB Model	11.0	20
147 GB Model	7.0	20
73 GB Model	4.0	20

The period from power off to the complete stop of the rotating spindle is categorized as 'operating'. The Operating shock criteria apply during this period. Refer to section 12.3, "Operating shock" on page 54.

4.5.4 Data transfer speed

Table 12: Data transfer speed (sector size 512 Byte case)

Description			Typical (MB / Sec)	
Disk-buffer transfer	Zone	Model	Read	Write
Instantaneous	0	All	138.2	138.2
Measured typical values for sustained disk-buffer transfer rate	0	300 GB	123.0	120.0
	0	147 GB	123.0	120.0
	0	73 GB	123.0	120.0
Instantaneous	19	All	80.9	80.9
Measured typical values for sustained disk-buffer transfer rate	19	300 GB	71.7	69.9
	19	147 GB	71.7	69.9
	19	73 GB	71.7	69.9

Notes:

- Instantaneous disk-buffer transfer rate is derived by: (Number of sectors on a track) x 512 x (revolutions/sec)
- For this table, '1 MB / Sec' should be interpreted as 1,000,000 bytes per Second.
- The number of sectors per track will vary by zone because of the linear density recording.
- Sustained disk-buffer transfer rate is the average rate measured while transferring multiple cylinders of data. It differs from the instantaneous transfer rate because of the time required to change tracks (Cylinder skew and Head skew). In addition, time is added for the occasional missed transfer.

4.5.5 Buffering operation (read ahead/write cache)

This hard disk drive has a buffer for read ahead (see 20.8, “Segmented Caching” on page 254).

5.0 Data Integrity

The drive retains recorded information under all non-write operations.

No more than one sector can be lost by power down during a write operation while write cache is disabled. If power down occurs before completion of a data transfer from write cache to disk while write cache is enabled, the data remaining in the write cache will be lost. To prevent this data loss at power off, the following action is recommended:

- Confirm successful completion of a SYNCHRONIZE CACHE (35h) command

5.1 Equipment Status

Equipment status is available to the host system any time the drive is not ready to READ, WRITE or SEEK. This status normally exists at power-on time and will be maintained until the following conditions are satisfied:

- Access recalibration/tuning is complete
- Spindle speed meets requirements for reliable operations
- Self-check of drive is complete

Appropriate error status is made available to the host system if any of the following conditions occur after the drive has become ready:

- Spindle speed goes outside of requirements for reliable operation
- “Write fault” is detected

5.2 Error Recovery Procedure

Errors occurring with the drive are handled by the error recovery procedure.

Errors that are uncorrectable after application of the error recovery procedures are reported to the host system as non-recoverable errors.

6.0 Physical format

Media defects are remapped to the next available sector during the Format Process in manufacturing. The mapping from Logical Block Address (LBA) to the physical Block location is calculated using internally maintained tables.

6.1 Shipped format (Plist)

- Data areas are optimally used.
- All pushes generated by defects are absorbed by available tracks of the inner notch.

Plist physical format

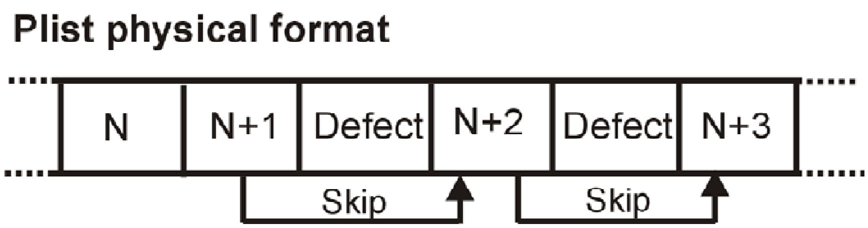


Figure 1 : Plist physical format

Note: Defects are skipped without any constraint, such as track or cylinder boundary. The calculation from LBA to physical is done automatically by internal table.

6.2 Reassigned format (Glist)

- G-List has a capacity of 5000 Customer LBAs.
- Multiple reassignments of the same Customer LBA do not increase the number of G-List entries.
- A track for spare sectors is inserted after every 800 nominal customer tracks.

7.0 Electrical interface

7.1 SCA connector

The drive uses the standard 80 pin SCA-2 connector which conforms to the mechanical requirements of SFF 8451.

The connector is expected to be used in an environment which uses a common connector structure for racking disk drives in a cabinet. The connector allows for plugging a drive directly into a backplane by providing the necessary electrical connection. Mechanical stability and device retention must be provided by a mechanism outside the drive. The drive also conforms to the electrical specification of SPI-4 document, Annex C “Single Attachment for Small SCSI Disk Drives”.

7.1.1 SCSI signal connector (80-pin SCA-2 model)

Table 13: Table of signals (80-pin)

Connector contact number	Signal name	Connector contact number	Signal name
01	12 Volt Charge	41	12V Ground
02	12 volt	42	12V Ground
03	12 volt	43	12V Ground
04	12 volt	44	Mated 1
05	Opt 3.3 V/NC	45	Opt 3.3 V charge/NC
06	Opt 3.3 V/NC	46	DIFFSENS(*1)
07	-DB(11)	47	+DB(11)
08	-DB(10)	48	+DB(10)
09	-DB(9)	49	+DB(9)
10	-DB(8)	50	+DB(8)
11	-I/O	51	+I/O
12	-REQ	52	+REQ
13	-C/D	53	+C/D
14	-SEL	54	+SEL
15	-MSG	55	+MSG
16	-RST	56	+RST
17	-ACK	57	+ACK
18	-BSY	58	+BSY
19	-ATN	59	+ATN
20	-P_CRCA	60	+P_CRCA
21	-DB(7)	61	+DB(7)
22	-DB(6)	62	+DB(6)
23	-DB(5)	63	+DB(5)
24	-DB(4)	64	+DB(4)
25	-DB(3)	65	+DB(3)
26	-DB(2)	66	+DB(2)
27	-DB(1)	67	+DB(1)
28	-DB(0)	68	+DB(0)
29	-DB(P1)	69	+DB(P1)
30	-DB(15)	70	+DB(15)
31	-DB(14)	71	+DB(14)
32	-DB(13)	72	+DB(13)
33	-DB(12)	73	+DB(12)
34	5 Volt	74	Mated 2
35	5 Volt	75	5V Ground
36	5 Volt Charge	76	5V Ground
37	Spindle Sync/NC	77	ACTIVE LED OUT
38	RMT START	78	DELAYED START
39	SCSI ID (0)	79	SCSI ID (1)
40	SCSI ID (2)	80	SCSI ID (3)

Note: *1 HVD is not supported

Eight-bit devices that connect to the SCA-2 connector should have the following signals inactive (high): -DB(8), -DB(9), -DB(10), -DB(11), -DB(12), -DB(13), -DB(14), -DB(15), -DB(P1). All other signals shall be connected as defined.

7.2 SCSI cable

Refer to ANSI SPI-4.

7.3 SCSI bus terminator

Onboard SCSI active termination feature is not supported. The using system is responsible for making sure that all required signals are terminated at both ends of the bus cable.

Terminator power

The 80-pin SCA-2 models do not support SCSI termination power.

7.4 Hot plug/unplug

The term “hot plug” refers to the action of mechanically engaging a device to the power and/or bus when other devices may be active on the same bus. A comprehensive classification of the state of the SCSI bus during this event is located in the SCSI-3 Parallel Interface Standard.

While every effort was made to design the drive not to influence the SCSI bus during these events, it is the responsibility of the system to ensure voltage regulation and conformance to operational and non-operational shock limits. During hot plug events the non-operational shock levels should not be exceeded. The operational shock levels of adjacent drives should also not be exceeded. The recommended procedure is to prohibit write operations to adjacent drives during hot plug and unplug actions.

During hot unplug the operational shock limit specifications should not be exceeded. If this cannot be guaranteed, the drive should be issued a SCSI STOP UNIT command that is allowed to complete before unplugging. The basic requirement is that while the drive is operational or spinning down the operational shock limits are in effect. When the drive has completely stopped, the non-operational shock limits are in effect. The recommended procedure is to allow the unplugged drive to rest in the drive bay for a minimum of 15 seconds and then complete the removal. During hot plug or unplug events the power supply ripple on adjacent operational drives should not be outside the ±5% regulation tolerance. It is recommended that the system have current limiter for in-rush current as described in ANSI SPI-4.

7.5 SCSI bus electrical characteristics

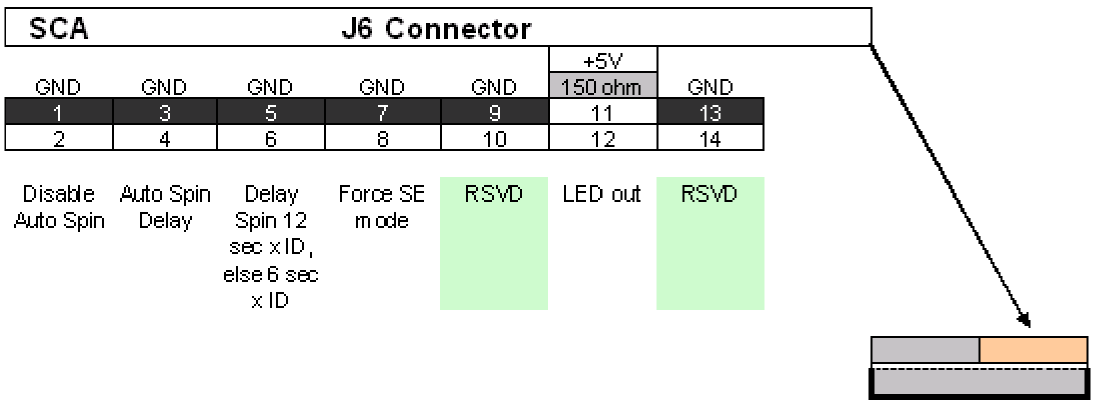
Refer to ANSI SPI-4 for bus electrical characteristics.

Note: If the drive is connected to an HVD bus, the drive I/O will be permanently damaged.

7.6 Option jumper block

As shown in the figure below, the 80 pin SCA-2 card has a single jumper block (J6).

As described in Section 7.2.2, “SCSI signal connector (80-pin SCA-2 model)” on page 31, some of the jumper pins on J6 of the 80-pin models can also be controlled through the 80-pin SCA-2 connector. These controls work as a logical OR between the Option Jumper Block and the SCA-2 connector.



7.7 Jumper signal Description on 80 pin Jumper Block

7.7.1 Disable Auto Spin (position 1-2)

If a jumper is not installed, the drive will spin up automatically after the power on reset. If a jumper is installed, the drive will not spin up unless a START UNIT command is received.

7.7.2 Auto Spin Delay and Delay Spin 12 / 6 (position 3-4 and position 5-6)

These pins control when and how the drive spins up with the combination of Position #1-2 on J6. When both Auto Spin up and Auto Start Delay are enabled, the drive start will be delayed by a period of time multiplied by its own SCSI address. If Auto Spin up is disabled, these jumpers will be ignored. Placing a jumper on delay start 12 / 6 results in a start up delay of 12 seconds time the SCSI ID.

Note: In the table below, “on” means a jumper is installed and “off” means that a jumper is not installed.

Table 14: Auto start delay & delay start 6/12 drive behavior

Model	Jumper Settings			Response	
SCA	Disable Auto Spin	Auto Spin Delay	Dly 12 / 6	Auto Start ?	Delay Multiplier
	OFF	OFF	OFF	YES	0
	OFF	OFF	ON	YES	0
	OFF	ON	OFF	YES	6
	OFF	ON	ON	YES	12
	ON	OFF	OFF	NO	-
	ON	OFF	ON	NO	-
	ON	ON	OFF	NO	-
	ON	ON	ON	NO	-

7.7.3 Force Single Ended Mode (position 7-8)

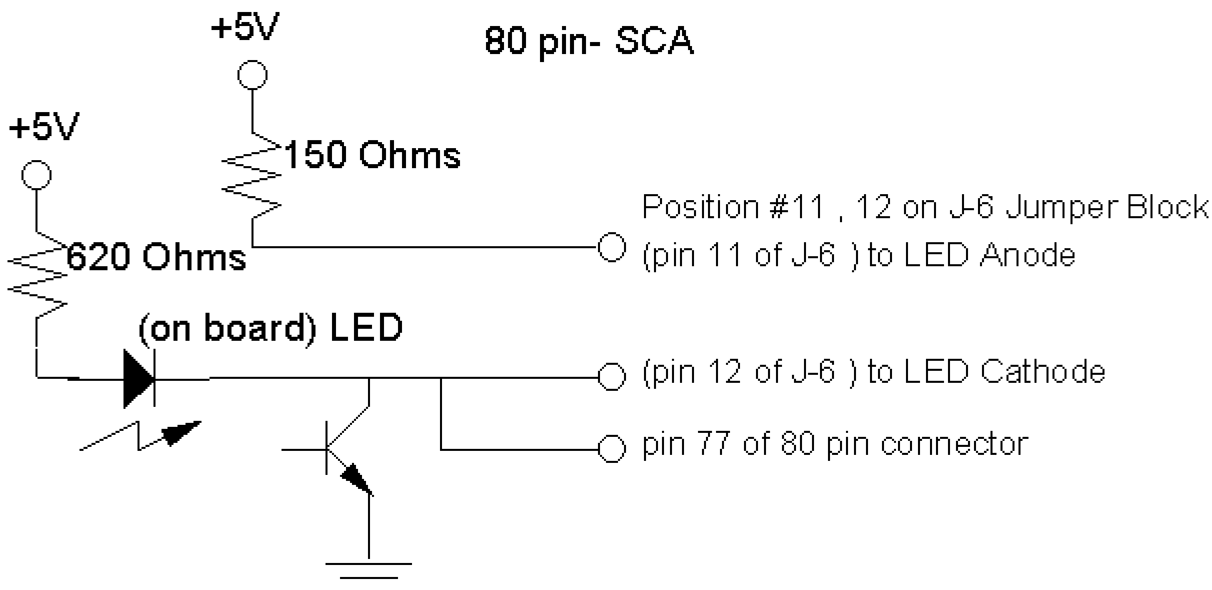
If a jumper is installed, the drive functions as a single-ended mode drive.

7.7.4 Reserved (position 9-10)

No connection should be made.

7.7.5 LED Driver Out (position 11-12)

The LED pins are used to drive an external Light Emitting Diode. Up to 30 mA of sink current capability is provided. The LED Anode must be tied to the current limited +5V source provided at the Location #11 on J6 jumper block.
The LED Cathode is then connected to pin #12 on J6 jumper block to complete the circuit.

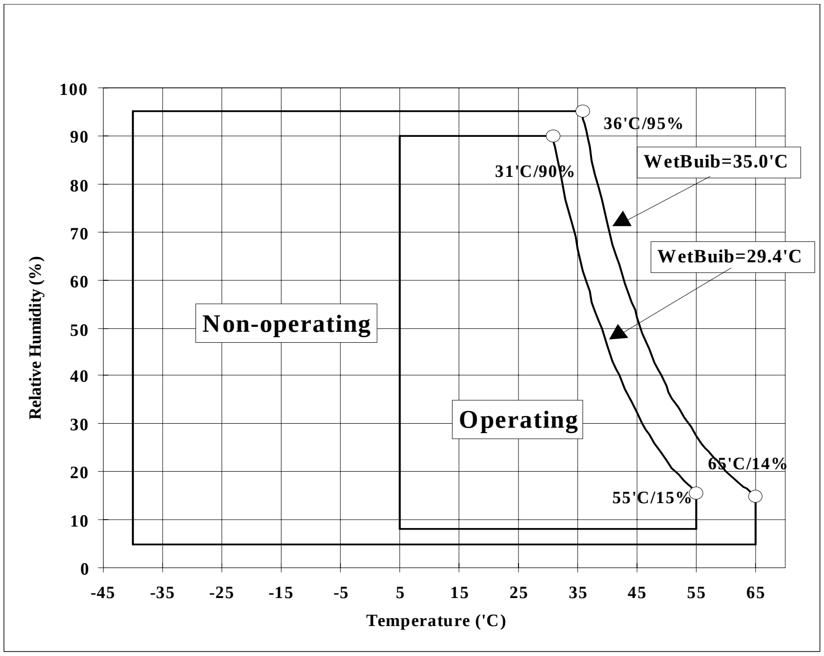


8.0 Environment

8.1 Temperature and humidity

Table 15: Operating and non-operating conditions

Operating conditions	
Ambient Temperature	5°C to 55ºC (see note)
Relative humidity	5 to 90%, non-condensing
Maximum wet bulb temperature	29.4ºC, non-condensing
Maximum temperature gradient	20ºC/hour
Altitude	-305 to 3,048 m
Shipping conditions	
Ambient Temperature	-40°C to70ºC
Relative humidity	5 to 95%, non-condensing
Maximum wet bulb temperature	35ºC, non-condensing
Maximum temperature gradient	30ºC/hour
Altitude	-305 to 12,192 m
Storage conditions	
Ambient Temperature	0°C to 65ºC
Relative humidity	5 to 90%, non-condensing
Maximum wet bulb temperature	35ºC, non-condensing
Altitude	-305 to 12,192 m



8.2 Storage requirements

8.2.1 Packaging

The drive or option kit must be heat-sealed in a moisture barrier bag with dessiccant inside the bag supplied by Hitachi Global Storage Technologies.

8.2.2 Storage time

Cumulative storage time in the package must not exceed one year. If a longer storage time is required, the drive must be repackaged with new desiccant or moved to a climatically controlled environment.

After the drive is unpackaged, it must not remain inoperative for longer than six months.

8.3 Corrosion test

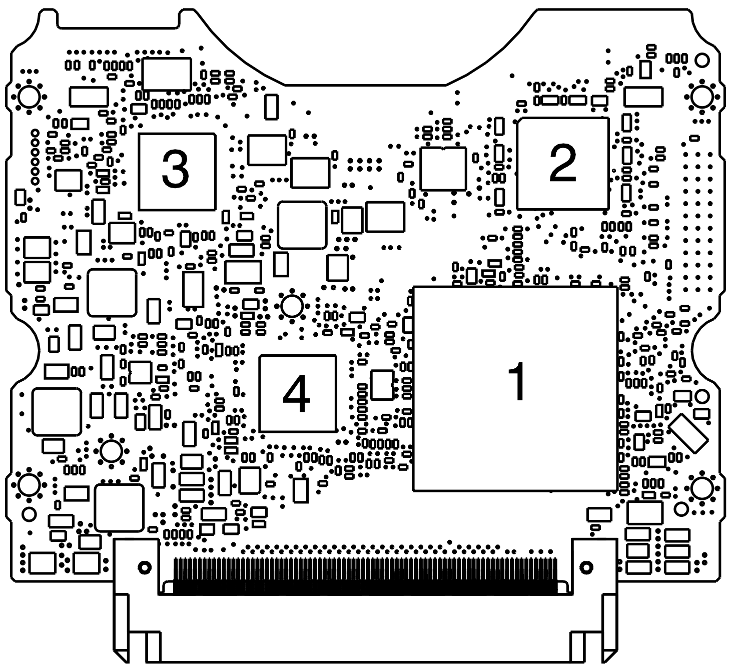
The hard disk drive shows no signs of corrosion inside or outside of the hard disk assembly and remains functional after being exposed to a temperature of 50ºC and relative humidity of 90% for seven days.

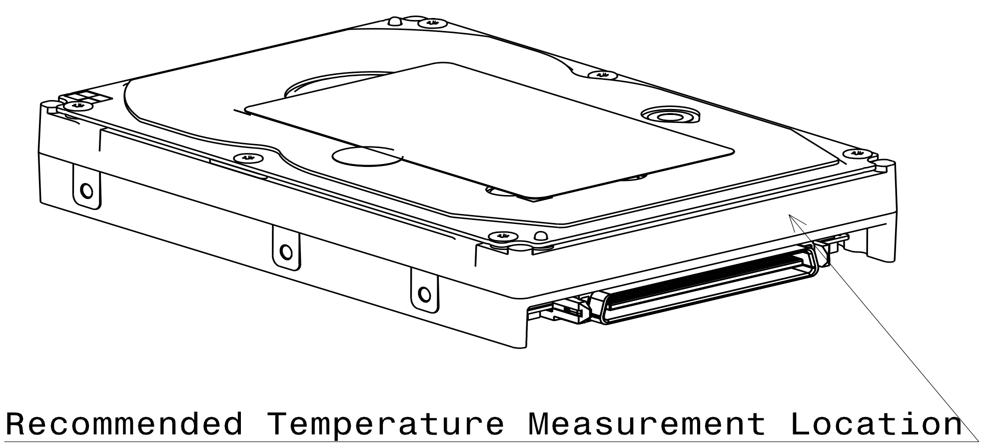
8.4 Cooling requirements

Drive component surface temperatures must remain within the limits specified in the following table. The drive may require forced air cooling to meet specified operating temperatures.

Table 16: Maximum allowable surface temperatures

Module name	Location	Maximum allowable surface temperature
MPU/HDC Integration module	1	108ºC
DRAM	2	85ºC
VCM & spindle driver	3	108ºC
Channel	4	115ºC
HDD base casting	as noted in picture	60ºC





9.0 DC power requirements

The following voltage specification applies at the drive power connector. Connections to the drive should be made in a safety extra low voltage (SELV) circuit. There is no power on or power off sequencing requirement.

Adequate secondary over-current protection is the responsibility of the system. A 10 A limit is required for safety purposes.

Table 17: Input voltage

	Tolerance	**Absolute max spike voltage**	**Supply rise time**
+5 Volts supply	+/- 5%	5.5 V	0-200 ms
+12 Volts supply	+/- 5%, [1]	15 V	0-400 ms

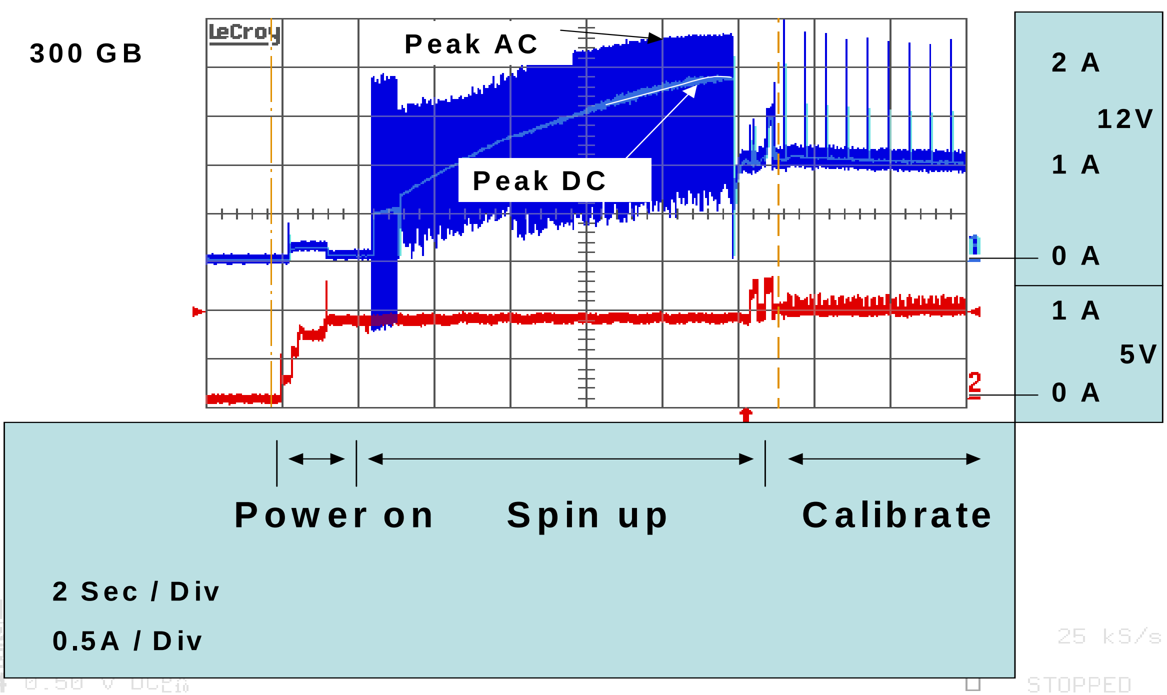
1. -8% of 12V voltage margin is acceptable during spin-up, but the drive ready time as specified in section 4.5.2 is not guaranteed.

***CAUTION**: To avoid damage to the drive electronics, power supply voltage spikes must not exceed 5.5V.*

9.1 Power supply current, average, and peak

The following current and power requirements are typical when operating under the following conditions: Nominal 5 and 12V, Background Media Scan (BMS) disabled for Idle, Write Caching disabled and the drive reporting a temperature of 45C.

Model pSCSI U320	300GB IO /Sec	Current +5V	Current +12V	Power Watts	Note
Start Peak DC		---	1.85	---	
Start Peak AC		1.19	2.50	---	
Idle		0.65	0.82	13.1	Average
Idle Ripple		0.10	0.25	---	Peak to Peak
Random W/R Peak	217	1.36	2.80	---	2KB Qd=4
Random W/R Average	217	0.75	1.19	18.1	2KB Qd=4
Random W/R Average	161	0.73	1.13	17.2	2KB Qd=1
	150	0.73	1.11		
	120	0.71	1.05		
	90	0.71	0.99		
	60	0.70	0.94		
	30	0.70	0.88		
Sequential Read Peak		1.85	---	---	Maximum Transfer Rate
Sequential Read Average		1.51	0.84	17.6	Maximum Transfer Rate
Sequential Write Peak		1.61	---	---	Maximum Transfer Rate
Sequential Write Average		1.35	0.84	16.8	Maximum Transfer Rate
BMS Average		0.84	0.84	14.3	
BMS Peak		1.47			



Model pSCSI U320	**147GB** IO /Sec	**Current** +5V	**Current** +12V	**Power** Watts	**Note**
Start Peak DC		---	1.85	---	
Start Peak AC		1.19	2.50	---	
Idle		0.65	0.54	9.7	Average
Idle Ripple		0.10	0.25	---	Peak to Peak
Random W/R Peak	217	1.33	2.80	---	2KB Qd=4
Random W/R Average	217	0.74	0.93	14.9	2KB Qd=4
Random W/R Average	161	0.73	0.83	13.6	2KB Qd=1
	150	0.73	0.81		
	120	0.71	0.76		
	90	0.71	0.70		
	60	0.70	0.65		
	30	0.70	0.59		
Sequential Read Peak		1.78	---	---	Maximum Transfer Rate
Sequential Read Average		1.49	0.56	14.2	Maximum Transfer Rate
Sequential Write Peak		1.59	---	---	Maximum Transfer Rate
Sequential Write Average		1.38	0.56	13.6	Maximum Transfer Rate
BMS Average		0.83	0.56	10.9	
BMS Peak		1.41			

Model pSCSI U320	**73GB** IO /Sec	**Current** +5V	**Current** +12V	**Power** Watts	**Note**
Start Peak DC		---	1.85	---	
Start Peak AC		1.19	2.50	---	
Idle		0.65	0.41	8.2	Average
Idle Ripple		0.10	0.19	---	Peak to Peak
Random W/R Peak	217	1.33	2.80	---	2KB Qd=4
Random W/R Average	217	0.74	0.79	13.2	2KB Qd=4
Random W/R Average	161	0.73	0.71	12.1	2KB Qd=1
	150	0.73	0.69		
	120	0.71	0.63		
	90	0.71	0.58		
	60	0.70	0.52		
	30	0.70	0.47		
Sequential Read Peak		1.82	---	---	Maximum Transfer Rate
Sequential Read Average		1.48	0.43	12.6	Maximum Transfer Rate
Sequential Write Peak		1.61	---	---	Maximum Transfer Rate
Sequential Write Average		1.36	0.43	12.0	Maximum Transfer Rate
BMS Average		0.83	0.43	9.3	
BMS Peak		1.46			

9.2 Ripple voltage

Table 18: Power supply generated ripple at drive power connector

	Maximum (mV pp)	MHz
+5 V DC	250	0-10
+12 V DC	250	0-10

During drive start up and seek, 12 volt ripple is generated by the drive (referred to as dynamic loading). If the power of several drives is daisy chained, the power supply ripple plus other drive dynamic loading must remain within the regulation tolerance of +5%. A common supply with separate power leads to each drive is a more desirable method of power distribution.

To prevent external electrical noise from interfering with the drive's performance, the drive must be held by four screws in a user system frame that has no electrical level difference at the four screw positions. The drive enclosure must not be used in the current return path of the drive power supply. The maximum common-mode noise current passing through the drive must not exceed 20 mA.

9.3 Power consumption efficiency index

Table 19: Power consumption efficiency index

Model	**300 GB**	**147GB**	**73 GB**
Power consumption efficiency index (W/GB)	0.044	0.066	0.112

10.0 Reliability

10.1 Start/stop cycles

The drive is designed to withstand a minimum of 50,000 start/stop cycles at ambient environment.

The drive is designed to withstand a minimum of 10,000 start/stop cycles at the operating environment conditions specified in "Enviornment" on page 36.

10.2 Data reliability

The probability of an uncorrectable data error is 1 in 10^{16} bits read.

The following ECC On-The-Fly (OTF) correction is implemented:

- 1 symbol = 10 bits
- 1 interleave
- 20 symbols, On-The-Fly correction
- Up to 38 symbols (total) Off-Line correction with burst error information
- LBA seeded 32 bit CRC for ECC miscorrect detection

10.3 Seek errors

A non-recoverable seek/ID miscompare error is defined as a seek operation that cannot be recovered by the error recovery procedure of the drive. The drive reports sense key 04 and sense code 02 for this error.

No drive has more than one non-recoverable seek/ID miscompare error per 100 million seek operations (1 in 1x 10^8) when operated at the full range of voltage and environmental conditions.

10.4 Failure prediction (PFA/S.M.A.R.T)

A recoverable equipment error is an error other than a seek/ID miscompare error or read error that is detected and corrected by the drive error recovery procedure. Examples are *Write Fault, Drive Not Ready* and internal drive errors.

No drive has more than one recoverable equipment error per 10^8 READ, 10^8 WRITE, or 10^6 SEEK operations when operated at the full range of voltage and environmental conditions.

Non-recoverable equipment errors indicate a defective drive.

10.5 Preventive maintenance

None.

10.6 Temperature warning

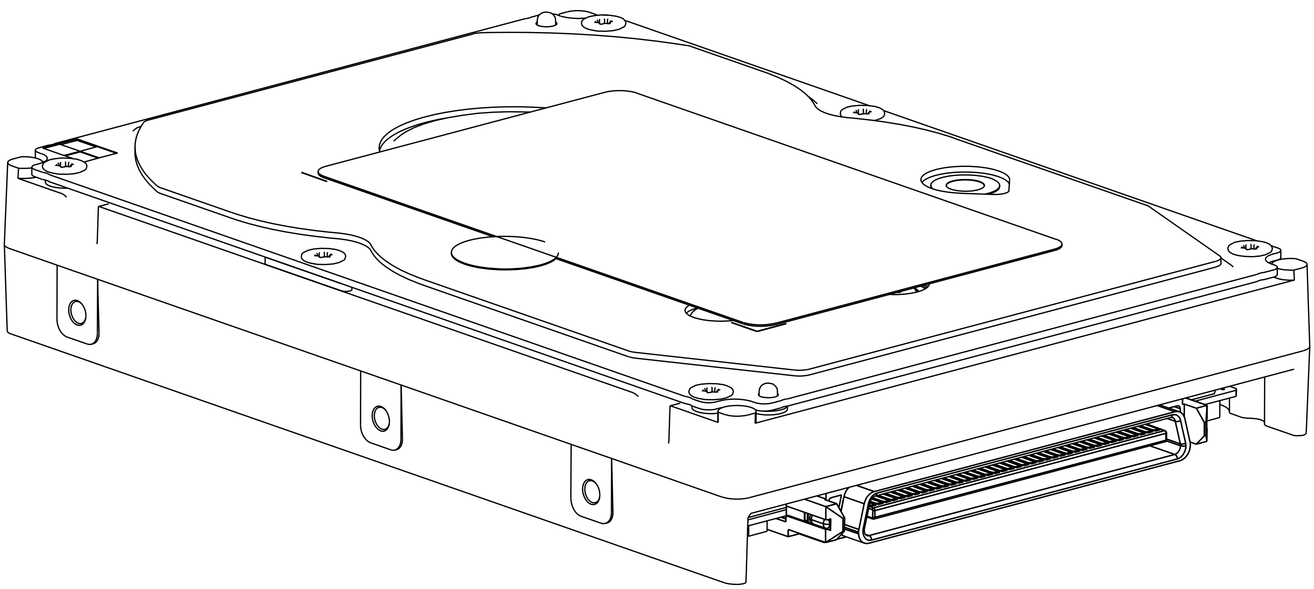
Temperature warning is enabled by setting the EWASC (*Enable Warning* additional sense code) bit to 1 and setting DEXCPT (Disable Exception Control) bit to 0 in Mode Page 1C. For mode page settings, refer to Section 17.11.13, “Mode page 1C (Informational exceptions control)” on page 152. The warning is issued as sense data (Sense Key 01h, Code 0Bh, Qual 01h).

The drive temperature is reported in Log Sense page 2F. Refer to Section 17.7.11, “Log Sense page 2F” on page 111.

11.0 Mechanical specifications

11.1 Outline

11.1.0.1 80-pin connector model

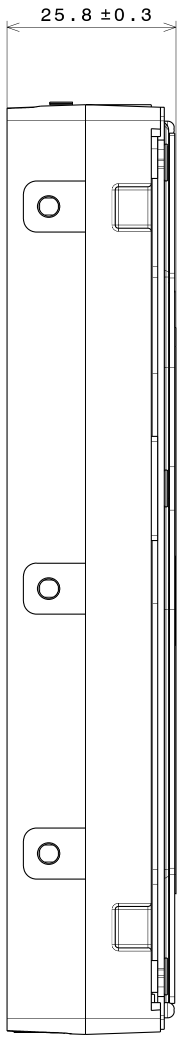


11.2 Mechanical dimensions

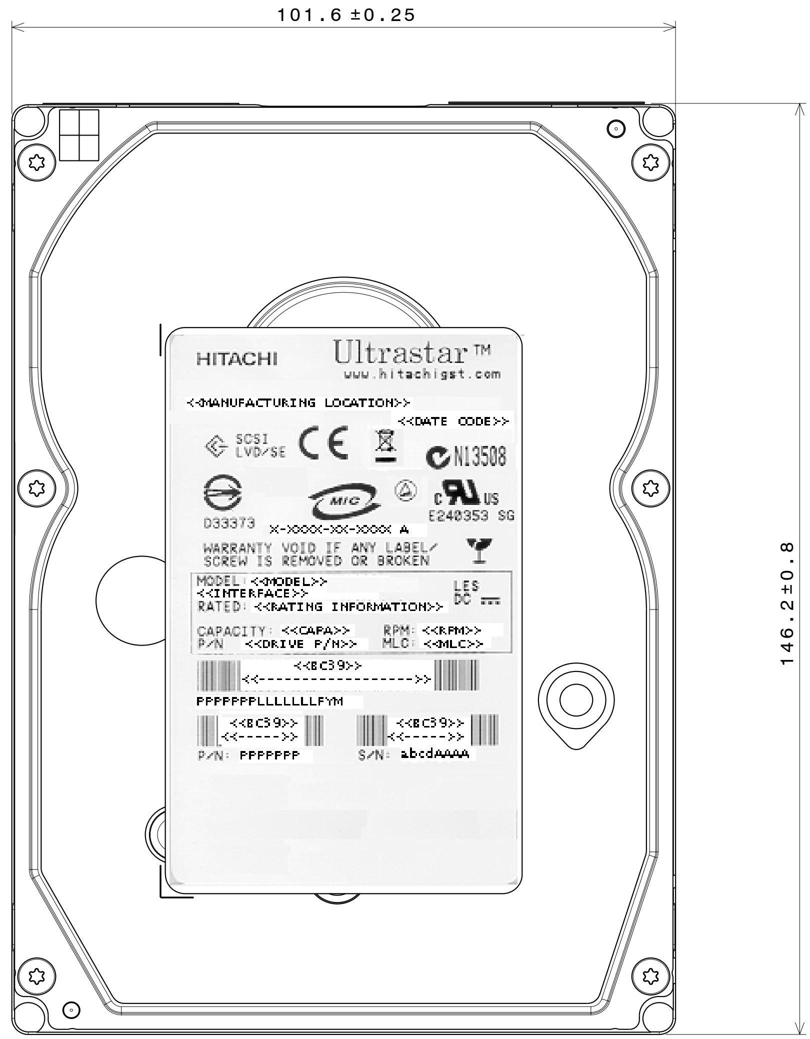
The drive complies with SFF-8301.

Table 20: Physical dimensions

Height [mm]	25.8 ± 0.3	
Width [mm]	101.6 ± 0.25	
Length [mm]	146.2 ± 0.8	
Weight [grams - maximum]	300 GB Model	745 grams
	147 GB Model	714 grams
	73 GB Model	699 grams


25.8 ±0.3

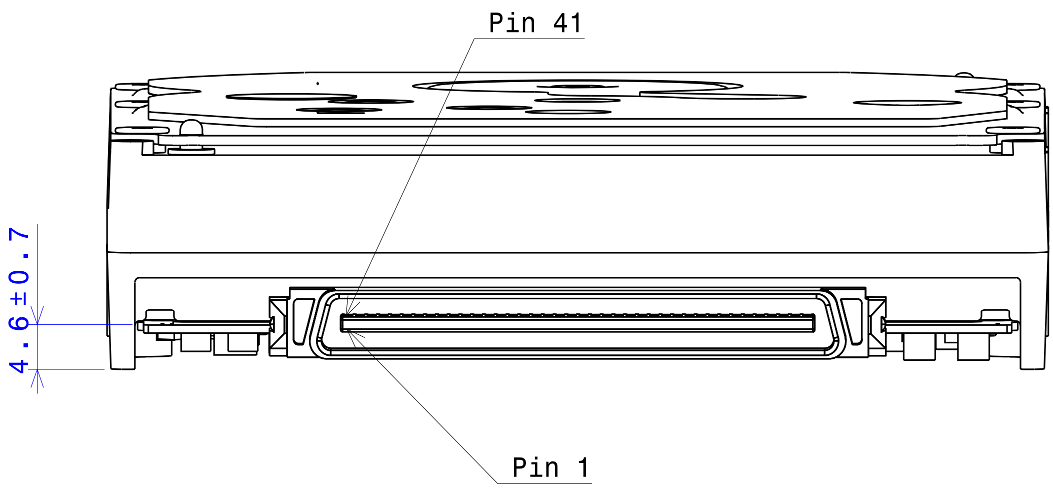
LEFT


101.6 ±0.25
146.2 ±0.8
HITACHI
Ultrastar™
www.hitachigst.com
<<MANUFACTURING LOCATION>>
<<DATE CODE>>
SCSI
LVD/SE
N13508
D33373
E240353 SG
WARRANTY VOID IF ANY LABEL/
SCREW IS REMOVED OR BROKEN
MODEL: <<MODEL>>
<<INTERFACE>>
RATED: <<RATING INFORMATION>>
CAPACITY: <<CAPA>>
RPM: <<RPM>>
P/N <<DRIVE P/N>>
MLC: <<MLC>>
<<BC39>>
PPPPPPPLLLLLLLFYM
P/N: PPPPPPP
S/N: abcdAAAA

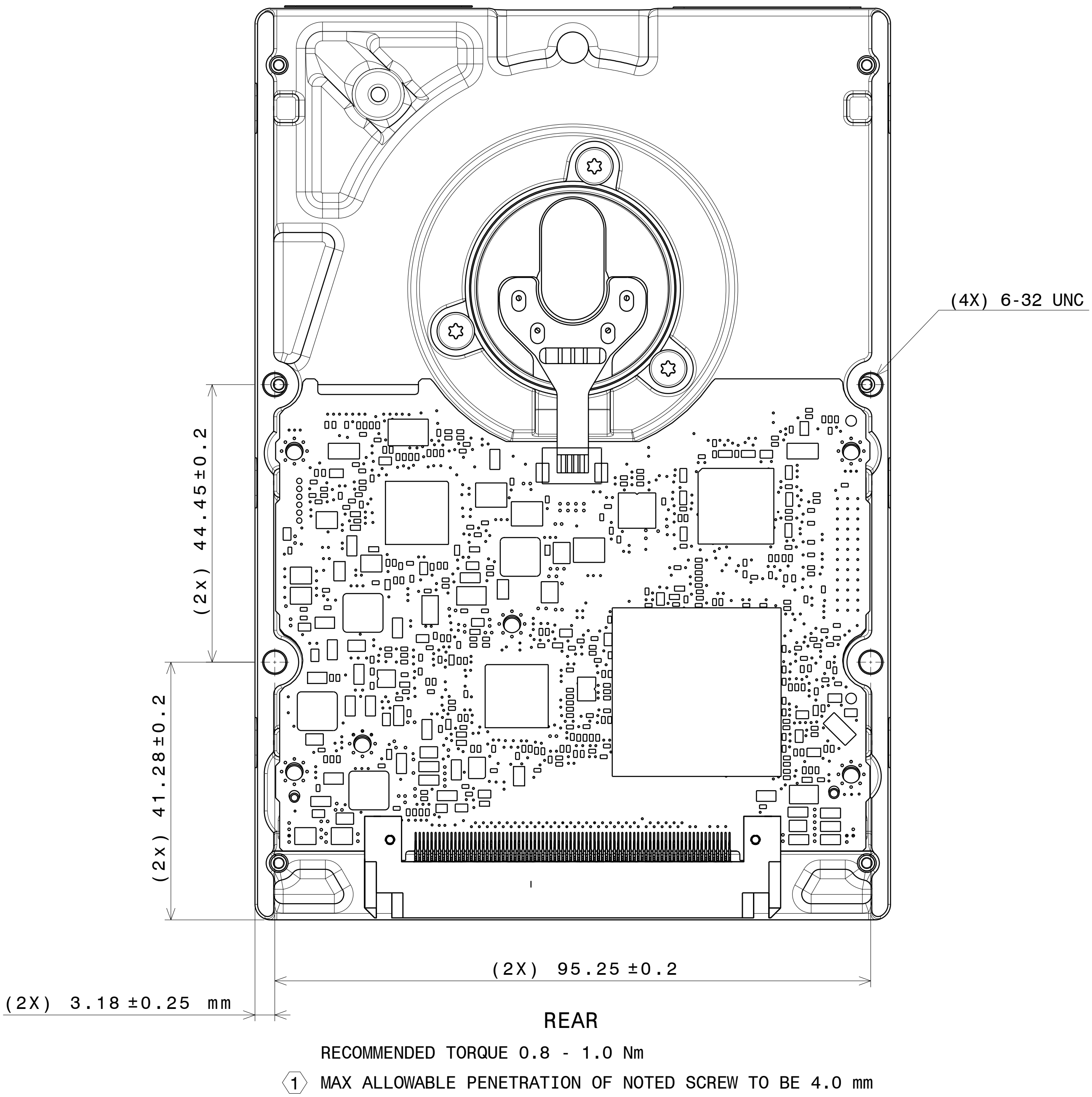
FRONT

11.3 Interface connector - 80 pin

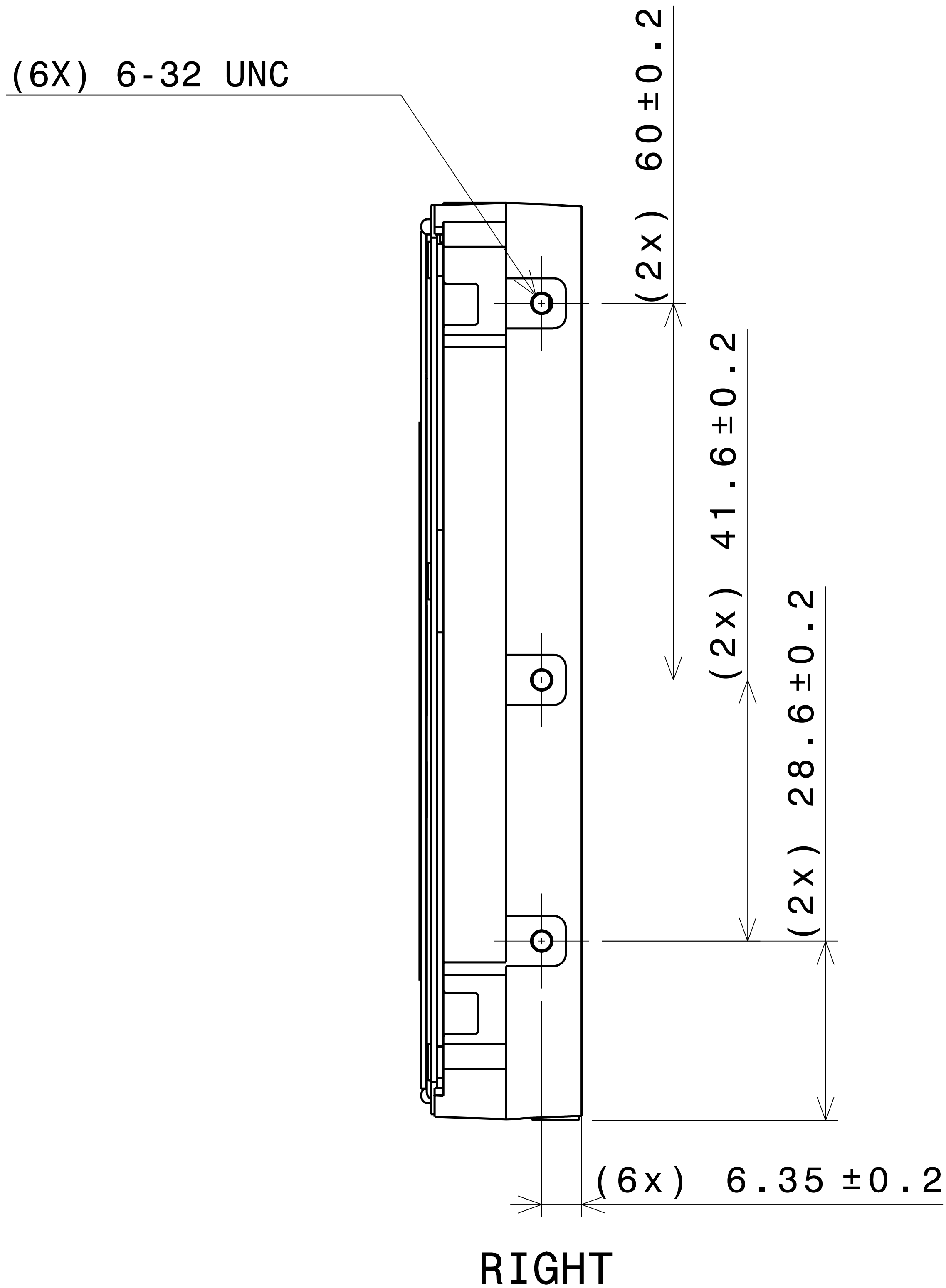
Table 21: Interface connector - 80 pin



11.4 Mounting positions and tappings



11.4.0.1 Mounting positions and tappings



11.5 Drive mounting

The drive will operate in all axes (six directions). Performance and error rate will stay within specification limits if the drive is operated in the other orientations from which it was formatted.

The recommended mounting screw torque is 0.6 - 1.0 Nm (6 - 10 kgf-cm). The recommended mounting screw depth is 4 mm maximum for bottom and 4.5 mm maximum for horizontal mounting.

To avoid performance degradation, mount the drive in the system securely enough to prevent excessive motion or vibration of the drive at seek operation or spindle rotation, using appropriate screws or equivalent mounting hardware. Consult with the issuer of this specification for actual application if necessary.

Drive level vibration tests and shock tests are to be conducted with the drive mounted to a table using the bottom four screws.

11.6 Heads unload and actuator lock

Heads are moved out from the disks (unload) to protect the disk data during shipping, moving, or storage. At power down, the heads are automatically unloaded from over the disk area and the head actuator locking mechanism will secure the heads in the unload position.

12.0 Vibration and shock

All vibration and shock measurements in this section are made with a bare drive. The input for the measurements are applied to the normal drive mounting points unless noted otherwise.

12.1 Operating vibration

12.1.1 Random vibration

The drive is designed to operate without unrecoverable errors while being subjected to the vibration levels defined below.

The assessments are carried out during 30 minutes of random vibration using the power spectral density (PSD) levels as follows.

No Errors: 0.4 G RMS, 5-500 Hz, flat PSD profile for each of the three mutually perpendicular axes.

No Data Loss: 1.2 G RMS, 5-500 Hz, flat PSD profile for each of the three mutually perpendicular axes.

Note: The specified levels are measured at the mounting points.

12.1.2 Swept sine vibration

The drive will meet the criterion while operating in the respective conditions as described below.

No errors: 0.5 G 0-peak, 5-400-5 Hz sine wave, 0.5 octave/minute sweep rate

No data loss: 1.5 G 0-peak, 5-500-5 Hz sine wave, 0.5 octave/minute sweep rate

12.2 Non-operating vibration

The drive will not sustain permanent damage or loss of recorded data after being subjected to the environments as described below.

12.2.1 Random vibration

The test consists of a random vibration applied for each of the three mutually perpendicular axes at a time duration of ten minutes per axis:

1.04 G RMS, 5-500 Hz, flat PSD profile

12.2.2 Swept sine vibration

The test consists of a swept sine vibration applied for each of the three mutually perpendicular axes.

2.0G 0-peak, 5-500-5 Hz sine wave, 0.5 octave/minute sweep rate

12.3 Operating shock

The drive will meet the criterion while operating in the respective conditions as described below.

No data loss: 15G, 11 ms duration, half sinewave shock pulse

No data loss: 30G, 2 ms duration, half sinewave shock pulse

The shock pulses of each level are applied to the drive, ten pulses for each direction and for all three mutually perpendicular axes. There must be a minimum of 30 seconds delay between shock pulses. The input level is applied to a base plate where the drive is attached using four mounting screws.

12.4 Non-operating shock

The drive will not sustain permanent damage or loss of recorded data after being subjected to the environments as described below.

12.4.1 Half sinewave shock pulse

80 G, 11 ms duration, half sinewave pulse

250 G, 2 ms duration, half sinewave pulse

The shocks are applied in each direction of the drive for the three mutually perpendicular axes, one axis at a time. The input level is applied to a base plate where the drive is attached using four mounting screws.

12.4.2 Rotational shock

30,000 radians/second^2, 1 ms duration

20,000 radians/second^2, 2 ms duration

The shock input is applied around the axis of the actuator pivot. The shock input does not displace the heads from the actuator latched position.

13.0 Acoustics

13.1 Sound power levels

The upper limit criteria of A-weighted sound power levels are given in Bel, relative to one pico watt, and are shown in the following table. The measurement method is in accordance with ISO-7779.

Table 22: A-weighted sound power levels

Model	Mode	A-weighted sound power level (Bel)	
		Typical	Maximum
300 GB	Idle	3.5	3.8
	Operating	4.3	4.7
147 GB	Idle	3.5	3.8
	Operating	4.3	4.7
73 GB	Idle	3.5	3.8
	Operating	4.3	4.7

Background power levels of the acoustic test chamber for each octave band are to be recorded. Sound power levels are measured with the drive supported by spacers so that the lower surface of the drive is located at a height of 25 cm from the chamber floor.

No sound-absorbing material shall be used. The acoustical characteristics of the drive subsystem are measured under the following conditions.

Idle Mode:

Powered on, disks spinning, track following, unit ready to receive and respond to host commands.

Operating Mode:

Continuous random cylinder selection and seek operation of the actuator with dwell time at each cylinder. Seek rate for the drive is calculated per the formula below:

Ns =average seek rate in seeks/sec where:

Ns = 0.4 / (Tt + Tl)

Tt = published random seek time

Tl = time for the drive to rotate by half a revolution

14.0 Identification

14.1 Labels

The following labels are affixed to every hard disk drive shipped from the drive manufacturing location in accordance with appropriate hard disk drive assembly drawing:

- A label containing Hitachi logo, Hitachi part number, and the statement “Made by Hitachi”, or Hitachi approved equivalent.
- A label containing drive model number, manufacturing date, formatted capacity, country of origin or Hitachi approved equivalent and UL, C-UL, TUV, CE, MIC, BSMI,CTICK, RoHS and Recycle logos.
- A bar code label containing the drive serial number.
- Jumper setting label.
- A user designed label, per agreement.
- Interface definition mark, SCSI LVD/SE multimode.

SCSI LVD/SE

The labels may be integrated with other labels.

15.0 Electromagnetic Compatibility

The drive, when installed in a suitable enclosure and exercised with a random accessing routine at a maximum data rate will comply with the worldwide EMC requirements listed below.

The drive is designed for system integration and installation into a suitable enclosure for use. As such, the drive is supplied as a subassembly and is not subject to Subpart B of Part 15 of the FCC Rules and Regulations.

The design of the drive serves to minimize radiated emissions when installed in an enclosure that provides reasonable shielding. As such, the drive is capable of meeting FCC Class B limits. However, it is the users responsibility to assure that the drive meets the appropriate EMC requirements in their system. Shielded I/O cables may be required if the enclosure does not provide adequate shielding, with the shields grounded to the enclosure and to the host computer.

Radiated and Conducted EMI

CISPR22	Class B
AS/NZS CISPR22	Class B
CNS13438 (Taiwan)	Class B
EN55022 (EU)	Class B
FCC Title47 Part 15 (USA)	Class B
GB9254 (China)	Class B
ICES-003, Issue 4	Class B
VCCI (Japan)	Class B

ITE Immunity

EN55024

Power Line Harmonics

EN61000-3-2 (EU)

Voltage Fluctuations and Flicker

EN61000-3-3 (EU)

15.1 Class B Regulatory Notices

European Union

This product is in conformity with the protection requirements of EU Council Directive 89/336/EEC, as amended by Council Directive 93/68/EEC on the approximation of the laws of the Member States relating to electromagnetic compatibility. Hitachi cannot accept responsibility for any failure to satisfy the protection requirements resulting from a non-recommended modification of the product, including the fitting of non-Hitachi option cards.

This product has been tested and found to comply with the limits for Class B Information Technology Equipment according to European Standard EN 55022. The limits for Class B equipment were derived for typical residential environments to provide reasonable protection against interference with licensed communication devices.

Canada

This Class B digital apparatus complies with Canadian ICES-003.
Cet appareil numérique de la classe B est conforme à la norme NMB-003 du Canada.

Germany

Deutschsprachiger EU Hinweis:
Hinweis für Geräte der Klasse B EU-Richtlinie zur Elektromagnetischen Verträglichkeit Dieses Produkt entspricht den Schutzanforderungen der EU-Richtlinie 89/336/EWG zur Angleichung der Rechtsvorschriften über die elektromagnetische Verträglichkeit in den EU-Mitgliedsstaaten. und hält die Grenzwerte der EN 55022 Klasse B ein. Um dieses sicherzustellen, sind die Geräte wie in den Handbüchern beschrieben zu installieren und zu betreiben. Des Weiteren dürfen auch nur von der HITACHI empfohlene Kabel angeschlossen werden. HITACHI übernimmt keine Verantwortung für die Einhaltung der Schutzanforderungen, wenn das Produkt ohne Zustimmung der HITACHI verändert bzw. wenn Erweiterungskomponenten von Fremdherstellern ohne Empfehlung der HITACHI gesteckt/eingebaut werden.

Deutschland: Einhaltung des Gesetzes über die elektromagnetische Verträglichkeit von Geräten
Dieses Produkt entspricht dem "Gesetz über die elektromagnetische Verträglichkeit von Geräten (EMVG)". Dies ist die Umsetzung der EU-Richtlinie 89/336/EWG in der Bundesrepublik Deutschland.

Zulassungsbescheinigung laut dem Deutschen Gesetz über die elektromagneti-sche Verträglichkeit von Geräten (EMVG) vom 18. September 1998 (bzw. der EMC EG Richtlinie 89/336) für Geräte der Klasse B Dieses Gerät ist berechtigt, in Übereinstimmung mit dem Deutschen EMVG das EG-Konformitätszeichen - CE - zu führen. Verantwortlich für die Konformitätserklärung nach Paragraf 5 des EMVG ist die Hitachi Global Storage Technologies , 5600 Cottle road, San Jose, California 95193. Informationen in Hinsicht EMVG Paragraf 4 Abs. (1) 4:

Das Gerät erfüllt die Schutzanforderungen nach EN 55024 und EN 55022 Klasse B.

Korea (MIC)

이 기기는 가정용으로 전자파적합등록을 한 기기로서 주거 지역에서는 물론 모든 지역에서 사용할 수 있습니다.

Taiwan (BSMI)

新加坡商日立環球儲存科技股份有限公司台灣分公司
台北市敦化北路 167 號 5 樓 (宏國大樓)

16.0 Standards

The following shows the safety standards for different countries.

16.1 UL and CSA standard conformity

The drive is qualified per ULIEC 60950-1: 2001, First Edition for use in Information Technology Equipment, including Electric Business Equipment. The UL recognition, or the CSA certification, is maintained for the product life. The UL and C-UL recognition mark, or CSA monogram for CSA certification, appears on the drive.

16.2 European standards compliance

The product is certified to EN60950.

16.3 German safety mark

The product is approved by the TUV and will carry the applicable Bauart mark on the label.

16.4 Flammability

The printed circuit boards used in this drive are made of material with a UL recognized flammability rating of V-1 or better. The flammability rating is marked or etched on the board. All other parts not considered electrical components are made of material with a UL recognized flammability rating of V-1 or better.

16.5 Corporate Standards Compliance

This product has been designed to meet the following Corporate Standards:

- NB 3-0501-201 Product Safety, National Requirements-All Countries.
- CS 3-0501-070 Electrical, Mechanical and Flammability
- NB 3-0501-033 Product Safety National Certification Conformity Requirement
- CS 1-9700-020 Eco-Product Design Requirement

Hitachi GST encourages owners of information technology (IT) equipment to responsibly recycle their equipment when it is no longer needed. Hitachi GST offers a variety of programs and services to assist equipment owners in recycling their IT products.

17.0 SCSI Command Set

Summaries of the SCSI commands supported by the drive are listed below. O = optional, M = mandatory

Table 23: SCSI Commands Supported

Type	Code	Description
M	04h	FORMAT UNIT (04), page 54
M	12h	INQUIRY (12), page 61
O	4Ch	LOG SELECT (4C), page 72
O	4Dh	LOG SENSE (4D), page 75
O	15h	MODE SELECT (15), page 100
O	55h	MODE SELECT (55), page 101
O	1Ah	MODE SENSE (1A), page 102
O	5Ah	MODE SENSE (5A), page 137
O	5Eh	PERSISTENT RESERVE IN (5E), page 139
O	5Fh	PERSISTENT RESERVE OUT (5F), page 142
O	34h	PRE-FETCH (34), page 147
M	08h	READ (6) - (08), page 148
M	28h	READ (10) - (28), page 149
O	A8h	READ (12) - (A8), page 151
O	88h	READ (16) - (88), page 152
O	3Ch	READ BUFFER (3C), page 153
M	25h	READ CAPACITY (10) - (25), page 157
O	9Eh/10h	READ CAPACITY (16) (9E/10), page 159
O	37h	READ DEFECT DATA (37), page 160
O	B7h	READ DEFECT DATA (B7), page 164
O	3Eh	READ LONG (3E), page 166
O	07h	REASSIGN BLOCKS (07), page 167
O	1Ch	RECEIVE DIAGNOSTICS RESULTS (1C), page 169
M	17h	RELEASE (17), page 172
O	57h	RELEASE (57), page 173
O	A3h/05h	REPORT DEVICE IDENTIFIER (A3/05), page 174
O	A0h	REPORT LUNS (A0), page 176
O	A3h/0Ch	REPORT SUPPORTED OPERATION CODES (A3/0C), page 177
O	A3h/0Dh	REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS (A3/0D), page 180
M	03h	REQUEST SENSE (03), page 182
M	16h	RESERVE (16), page 183
O	56h	RESERVE (56), page 184
O	01h	REZERO UNIT (01), page 185
O	0Bh	SEEK (6) - (0B), page 186
O	2Bh	SEEK (10) - (2B), page 186
M	1Dh	SEND DIAGNOSTIC (1D), page 187
O	A4h/06h	SET DEVICE IDENTIFIER (A4/06), page 191
O	1Bh	START STOP UNIT (1B), page 192
O	35h	SYNCHRONIZE CACHE (10) - (35), page 193

O	**91h**	**SYNCHRONIZE CACHE (16) - (91), page 194**
M	**00h**	**TEST UNIT READY (00), page 195**
O	**2Fh**	**VERIFY (2F), page 196**
O	**AFh**	**VERIFY (12) - (AF), page 199**
O	**AFh**	**VERIFY (16) - (8F), page 200**
M	**0Ah**	**WRITE (6) - (0A), page 201**
M	**2Ah**	**WRITE (10) - (2A), page 202**
O	**AAh**	**WRITE (12) - (AA), page 205**
O	**8Ah**	**WRITE (16) - (8A), page 206**
O	**2Eh**	**WRITE AND VERIFY (10) - (2E), page 207**
O	**AEh**	**WRITE AND VERIFY (12) - (AE), page 208**
O	**8Eh**	**WRITE AND VERIFY (16) - (8E), page 209**
O	**3Bh**	**WRITE BUFFER (3B), page 210**
O	**3Fh**	**WRITE LONG (3F), page 214**
O	**41h**	**WRITE SAME (41), page 215**
O	**93h**	**WRITE SAME (16) - (93), page 216**

17.1 SCSI Control Byte

The Control Byte is the last byte of every CDB. The format of this byte is shown below.

Table 24: SCSI Control Byte

BIT							
7	**6**	**5**	**4**	**3**	**2**	**1**	**0**
VU = 0		**Reserved = 0**				**FLAG**	**LINK**

VU

VU stands for Vendor Unique.

FLAG**

If Link is zero, Flag must also be zero. If Link is one, Flag may also be one. Typically this bit is used to cause an interrupt in the Initiator between linked commands.

LINK**

This bit is set to one to indicate that the Initiator desires an automatic link to the next command upon successful completion of the current command.

Note: * - The drive ignores the link bit and flag bit in the CDB.

17.2 Abbreviations

These abbreviations are used throughout the following sections:

LUN Logical Unit Number. An encoded three bit identifier for the logical unit.

VU Vendor Unique bits

LBA Logical Block Address

RSVD Reserved

MSB Most Significant Byte

LSB Least Significant Byte

17.3 Byte ordering conventions

In this specification, where it is not explicitly stated, all multi-byte values are stored with the most significant byte first. For example, in a 4 byte field, byte 0 will contain the MSB and byte 3 the LSB.

17.4 FORMAT UNIT (04)

Table 25: FORMAT UNIT (04)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Command Code = 04h							
1	FMTPINFO	RTO_REQ	LONG LIST=0	FMT-DATA	CMPLIST	Defect List Format		
2	VU = 0							
3-4	Obsolete = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

- **FMTPINFO (Format Protection Information)** set to zero specifies that the drive shall disable the use of protection information and format to the block size specified. FMTPINFO set to one specifies that the drive shall enable the use of protection information and format to the block size specified + 8 (e.g., if the block length is 512, then the formatted block length is 520). Following a successful format, the PROT_EN bit in the READ CAPACITY (16) parameter data indicates whether protection information is enabled. When protection information is written during a FORMAT UNIT command (i.e., the FMTPINFO bit is set to one), protection information shall be written with a default value of all FF's.
- **RTO_REQ (Reference Tag Own Request)** specifies whether the initiator or drive has ownership of the Logical Block Reference Tag field in protection information. If the FMTPINFO bit is set to zero and the RTO_REQ bit is set to one, Check Condition status will be returned, with the sense key set to Illegal Request and the additional sense code set to Invalid Field in CDB.

  If the FMTPINFO bit is set to one and the RTO_REQ bit is set to one, application client ownership of the Logical Block Reference Tag field is enabled, (i.e. the initiator owns the Logical Block Reference Tag field). If the FMTPINFO bit is set to one the the RRTO_REQ bit is set to zero, application client ownership of the Logical Block Reference Tag field is disabled (i.e. the drive owns the Logical Block Reference Tag field). Following a successful format, the RTO_EN bit in the READ CAPACITY (16) parameter data indicates whether application client ownership of the Logical Block Reference Tag field is enabled.
- **FmtData** set to one specifies that a Data Out phase follows the Command phase. The Data Out phase consists of a Parameter List header, optionally followed by an Initialization Pattern Descriptor, optionally followed by a Defect List. If FmtData=0, the following defaults are assumed: DPRY=0, DCRT=1, STPF=1, IP=0, DSP=0, Immed=0.
- **CmpLst**
  - - set to one specifies that the Grown Defect List (GList) existing prior to the issuance of the Format Unit command be discarded. If provided, the DList then becomes the GList. Following these operations, the Drive will be formatted with the PList and GList.
  - - set to zero specifies that the GList existing prior to the issuance of the Format Unit command is retained. If provided, the DList is combined with the GList to become the new GList. Following these operations, the Drive will be formatted with the PList and GList.

Note: The drive manages two internal defect lists and one external. The Plist is created at time of manufacture. The Glist is built after manufacture by the Initiators' use of the REASSIGN BLOCK command and the Automatic Reallocate functions. The Dlist is an external list. It is supplied by the Initiator in the Data Out phase of the FORMAT UNIT command.

- **Defect List Format** specifies the format of the defect descriptor transferred to the Target when FmtData bit is set to one. The Target supports the following three defect descriptor formats for the FORMAT UNIT command:

Format Description

000b	Block format
100b	Bytes From Index format
101b	Physical Sector format

If the FmtData bit is set to zero, this field must also be zero. Otherwise the command will complete with a CHECK CONDITION with a sense key of *Illegal Request* and an additional sense code of *Invalid Field in CDB.*

•**Notes:**It is recommended that the MODE SELECT command be issued prior to the FORMAT UNIT command to specify parameters that affect the formatting process.

The Block Length parameter of the Mode Select Parameter List's Block Descriptor is used during formatting and is saved following a successful format operation. If a MODE SELECT command has not been issued since the last reset or start-up (bring-up) sequence, then the Block Length from the previous format operation is used.

Subsequent to receiving a FORMAT UNIT command, the Target responds to commands as follows:

- All commands except REQUEST SENSE and INQUIRY return *Check Condition* status, while the format operation is an active I/O process.
- When tagged queuing is enabled (DQue = 0), all commands except REQUEST SENSE and INQUIRY return *Queue Full* status, while the FORMAT UNIT command is a queued I/O process.
- When tagged queuing is disabled (DQue = 1), all commands except REQUEST SENSE and INQUIRY return *Busy* status, while the FORMAT UNIT command is a queued I/O process
- If a REQUEST SENSE command is received while a format operation is an active I/O process, the Target returns *Good* status. The sense key is set to *Not ready* and the additional sense code and qualifier is set to *Format In Progress*.
- If an INQUIRY command is received while a format operation is an active I/O process, the Target returns *Good* status and Inquiry data as requested.

The format operation must complete successfully for the Drive to be usable. If the command is interrupted by a reset, power down, or an unrecoverable error, the Drive enters a degraded mode of operation in which reading and writing are prohibited. To exit the degraded mode, another FORMAT UNIT command must be sent by the Initiator and completed successfully by the Target.

The FORMAT UNIT command sets the *Unit Attention Condition* for all Initiators except the one that issued the FORMAT UNIT command.

17.4.1 Parameter List Header

Following is the format of the Paramter List Header sent during the data out phase when FmtData is set to one.

Table 26: Format of the Parameter List Header

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Reserved = 0							
1	FOV	DPRY	DCRT	STPF=1	IP	DSP	Immed	Ignored
2 3	(MSB) Defect List Length (LSB)							
4-n	Initialization Pattern Descriptor							
(n+1) - m	Defect Descriptor							

- **FOV** (Format Options Valid) bit set to zero indicates that the Target should use its default settings for the DPRY (0), DCRT (1), STPF (1), IP (0), and DSP (1) bits. These bits must all be set to zero in the Parameter List Header when FOV=0, or the command will be terminated with Check Condition status, sense key of Illegal Request, and additional sense code of Invalid Field in Parameter List.. FOV=1 indicates that the values set in DPRY, DCRT, STPF, IP, and DSP will be defined as specified below.
- **DPRY** (Disable Primary) bit set to zero indicates that the Target does not use portions of the medium identified as defective in the primary defect Plist for Initiator addressable logical blocks. If the Target cannot locate the Plist or it cannot determine whether a Plist exists, theTarget terminates the FORMAT UNIT command as described for STPF=1. A DPRY bit set to one indicates that the Target does not use the Plist to identify defective areas of the medium. The Plist is not deleted. DPRY must be set to 0 when DCRT is set to 0.
- **DCRT** (Disable Certification) bit set to zero indicates that the Target performs a medium certification operation and generates a Certification List (Clist), and adds the Clist to the Glist. DPRY must be set to 0 when DCRT is set to 0. A DCRT bit of one indicates that the Target does not generate a Clist or perform a certification process.

  Note: Since the DCRT bit is part of the Data Out phase that follows the FORMAT command, the FCERT bit in Mode Page 0 is provided to control certification when the FORMAT command is issued with no Data Out phase. If a FORMAT command is issued with a Data Out phase then FCERT is ignored.
- **STPF** (Stop Format) bit must be set to one. If one or both of the following conditions occurs, the Target terminates the FORMAT UNIT command with *Check Condition* status. The sense key is set to *Medium Error* and the additional sense code is set to *Defect List Not Found* if the first condition occurred or to *Defect List Error* if the second condition occurred.
  - The Target cannot locate a required Dlist nor determine that the list exists.
  - The Target encounters an unrecoverable error while accessing a required Dlist.
  - **IP** (Initialization Pattern) bit set to zero specifies that an initialization pattern descriptor is not included and all customer data will be initialized to zeroes. An IP bit of one specifies that an Initialization Pattern Descriptor is included in the FORMAT UNIT parameter list following the parameter list header.

Table 27: Initialization Pattern Descriptor:

Byte	BIT							
	7	6	5	4	3	2	1	0
0	IP Modifier = 0		SI	Reserved = 0				
1	Initialization Pattern Type = 1							
2 - 3	Initialization Pattern Length (n-3)							
4	Initialization Pattern							
...	...							
n	Initialization Pattern							

- **IP Modifier** must be set to 0, indicating that the drive will not modify the initialization pattern.
- **SI** (Security Initialize) bit set to one specifies that all customer data sectors, including those that have been previously reassigned, will be initialized. SI set to zero specifies that only the current customer accessible sectors will be formatted.
- **Initialization Pattern Type** must be set to one, specifying that the Initialization Pattern specified shall be repeated as required to fill each logical block.
- **Initialization Pattern Length** specifies the number of bytes that follow in the Initialization Pattern field, and must be less than or equal to the current block size, and non-zero.
- **Initialization Pattern** contains the data pattern to be written to the media.
- **DSP** (Disable Saving Parameters) bit when zero indicates the target is to save all the current MODE SELECT saveable parameters during the format operation. When the bit is one, the target is not to save the current MODE SELECT saveable parameters.
- **Immed** (Immediate) bit set to zero requests that status be returned at the end of the format operation. An immediate bit set to one requests that status be returned immediately following CDB validation and transfer of data in the Data Out phase. If the format operation, with the immediate bit set to one, terminates in error, DEFERRED ERROR SENSE data is generated.
- **Defect List Length** field specifies the total length in bytes of the defect descriptors that follow (not including the Initialization Pattern Descriptor, if any). Up to 1024 defect descriptors are allowed. The Defect List Length must be equal to four times the number of defect descriptors for BLOCK format, or eight times the number of defect descriptors for BYTES FROM INDEX and PHYSICAL SECTOR formats. Otherwise the command is terminated with Check Condition status with the sense key set to Illegal Request and the additional sense code set to Invalid Field in Parameter List.

17.4.2 Defect Descriptor

Three defect descriptor formats are supported. Entries are not required to be in ascending order. If an entry does not correspond to a valid user addressable media location, the command terminates with Check Condition status with the sense key set to Illegal Request and the additional sense code set to Invalide Field in Parameter List.

17.4.2.1 Block Format - 000b

Format of the Dlist sent during the data out phase when Dlist Format is Block format (000b) and FmtData is set to one.

Table 28: Defect Descriptor - Block Format (for n + 1 defects)

Byte	BIT							
	7	6	5	4	3	2	1	0
0 - 3	(MSB) Defective Logical Block Address (LSB)							
4n 4n+1 4n+2 4n+3	(MSB) Defective Logical Block Address n (LSB)							

The Block format of the Dlist is the LBA of each defective sector.

Note: If a Defective LBA entry, when converted to a physical sector, is equal to the physical sector of a Plist entry and DPRY = 1, then the entry is not added to the Glist.

17.4.2.2 Bytes From Index Format - 100b

Format of the Dlist sent during the data out phase when Dlist Format is Bytes From Index format (100b) and FmtData is set to one.

Table 29: Defect Descriptor - Bytes From Index Format (for n = 1 defects)

Byte	BIT							
	7	6	5	4	3	2	1	0
0 - 2	(MSB) Cylinder Number of Defect (LSB)							
3	Head Number of Defect							
4 - 7	(MSB) Defect Bytes from Index (LSB)							
8n 8n + 1 8n + 2	(MSB) Cylinder Number of Defect n (LSB)							
8n + 3	Head Number of Defect n							
8n + 4 8n + 5 8n + 6 8n + 7	(MSB) Defect (n) Bytes from Index (LSB)							

Each defect descriptor for the Bytes From Index format specifies that the sector containing this byte be marked defective. The defect descriptor is comprised of the cylinder number of the defect, the head number of the defect, and the number of the defect byte relative to index.

Note: If a Byte From Index entry, when converted to a physical sector, is equal to the physical sector of a Plist entry and DPRY = 1, then the entry is not added to the Glist.

17.4.2.3 Physical Sector Format - 101b

Format of the Dlist sent during the data out phase when Dlist Format is Physical Sector format (101b) and FmtData is set to one.

Table 30: Defect Descriptor - Physical Sector Format (for n + 1 defects)

Byte	BIT							
	7	6	5	4	3	2	1	0
0 1 2	(MSB) Cylinder Number of Defect (LSB)							
3	Head Number of Defect							
4 5 6 7	(MSB) Defect Sector Number (LSB)							
8n 8n + 1 8n + 2	(MSB) Cylinder Number of Defect n (LSB)							
8n + 3	Head Number of Defect n							
8n + 4 8n + 5 8n + 6 8n + 7	(MSB) Defect (n) Sector Number (LSB)							

Each defect descriptor for the Physical Sector format specifies a defective sector. The defect descriptor is comprised of the cylinder number of the defect, the head number of the defect, and the defect's sector number.

Note: If a Physical Sector entry, when converted to a physical sector, is equal to the physical sector of a Plist entry and DPRY = 1, then the entry is not added to the Glist.

17.5 INQUIRY (12)

Table 31: INQUIRY (12)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Command Code = 12h							
1	Reserved = 0			CmdDt=0		EVPD		
2	Page Code							
3 - 4	Allocation Length							
5	VU = 0		Reserved = 0				FLAG	LINK

The INQUIRY command requests the parameters of theTarget to be sent to the initiator.

An **EVPD** bit of one specifies that the Target return the vital product data page identified by the Page Code field in the CDB.

The **Page Code** specifies which page of vital product data information the drive shall return.

Table 32: Page Code descriptions

EVPD	PAGE CODE	Description
0	0	**The Target returns the standard INQUIRY data.**
0	Non Zero	**The drive returns *Check Condition* status with the sense key of *Illegal Request* and the additional sense code of *Invalid Field in CDB*.**
1	Non Zero	**The drive returns the vital product data of page code requested.**

Allocation Length specifies the number of bytes that the Initiator has allocated for INQUIRY data to be returned. An allocation length of zero implies that no data is to be returned. The Target will terminate the DATA IN phase when all available INQUIRY data has been transferred or when allocation length bytes have been transferred, whichever is less.

Note: If an INQUIRY command is received from an Initiator with a pending unit attention condition (before the target reports *Check Condition* status), the Target processes the INQUIRY command. The unit attention condition is not cleared by this action.

Note: The INQUIRY command is a Priority command and is not queued.

Note: The inquiry data is set at the time of manufacture and will not change (without a FRU change), with the following exceptions:

- Product Revision Level (EVPD=0) can be changed when microcode is downloaded with the Write Buffer command.
- The information returned for EVPD=1, Page Code = 3 is not fixed.

Note: The inquiry data returned when media is not available will not be complete.

Byte 0 of the returned data on an INQUIRY command is the same no matter which page(s) is(are) returned. This description is to be used for all the following page definitions.

The Peripheral Qualifier field of zero (0) indicates that the peripheral device is currently connected to this logical unit. A Peripheral Device Type field of zero (0) indicates that this device is a Direct Access Storage Device (DASD).

17.5.1 Inquiry Data

Fields with a value shown inside quotes (e.g. Value ='xyz') are character fields. A value not in quotes is a numeric value. Character fields are alphanumeric and represented in either ASCII.

17.5.1.1 Inquiry Data Format - EVPD = 0, Page Code = 0

Table 33: Inquiry Data- EVPD = 0

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	RMB = 0	Reserved=0						
2	Version = 3							
3	Obsolete	Obsolete	Norm ACA=0	HiSup = 1	Response Data Format = 2			
4	Additional Length = 159 (9Fh)							
5	SCCS=0	ACC=0	ALUA=00b		3PC=0	Reserved = 0		Protect=1
6	BQue = 0	EncSer = 0	Rsvd=0	MultiP=0	MChngr= 0	Obsolete		Addr16 = 1
7	Obsolete	Obsolete	Wb_16 =1	Sync = 1	Link = 0	Obsolete	CmdQue= 1	RSVD = 0
8-15	Vendor ID = "HITACHI " (ASCII)							
16-31	Product ID (ASCII)							
32-35	Product Revision Level (ASCII)							
36-43	Unit Serial Number (ASCII)							
44-55	Reserved = 0							
56	Reserved = 0				Clock=11b		QAS=1	IUS=1
57-95	Reserved=0							
96-145	Copyright Notice (ASCII)							
146-163	Reserved=0							

- **Qualifier** is set to zero to indicate that the LUN specified is currently supported. Qualifier is set to 011b when the LUN specified is not present [1]
- **Peripheral Device Type** is set to zero to indicate that the device is a Direct-Access Peripheral Device.
- **Removal Media Bit (RMB)** is always set to zero to indicate no removal media exists.
- **Version** indicates the level of the ANSI standard that the product supports. The drive supports ANSI SCSI version 3.
- **NormACA** (Normal ACA) field of 0 indicates the device server does not support setting the NACA bit to one in the Control Byte of the CDB as defined in the SAM.
- **HiSup** bit of 1 indicates that the drive uses the hierarchical addressing model to assign LUNs to logical units.

1.If an INVALID LUN is specified, a *Check Condition* status will be returned for all commands except INQUIRY and REQUEST SENSE.

- **Response Data Format** is set to two to indicate that the INQUIRY Data Format as specified in the ANSI SCSI version 2 is supported by the Target.
- **Additional Length** indicates the number of bytes of INQUIRY information that follows.
- **SCCS** bit of zero indicates that the device does not contain an embedded storage array controller component.
- **ACC** bit of zero indicates that no access controls coordinator may be addressed through this logical unit.
- **ALUA** bit of zero indicates that the device does not support asymmetric logical unit access.
- **3PC** bit of zero indicates that the device does not support thrid-party copy commands.
- **Protect** bit of one indicates that the drive supports protection information
- **BQue** bit shall be zero if the CmdQue bit is one.
- **EncSer** (Enclosure Services) bit of 0 indicates that the Target does not contain an embedded enclosure services component.
- **Port** bit of 0 indicates that the drive received the Inquiry command on port A, while a Port bit of 1 indicates that the drive received the Inquiry command on port B.
- **MultiP** (MultiPort) bit of 0 indicates that the Target has a single port and does not implement multi-port requirements.
- **MChngr** (Medium Changer) bit is always 0 to indicate MChngr is not supported.
- **Addr16** (Wide SCSI Address 16) bit of 1 indicates that the Target supports 16-bit wide SCSI Addresses.
- **Wb_16** is set to one to indicate that the Target supports 16-bit wide data transfers.
- **Sync** is set to one to indicate that the Target supports synchronous data transfer.
- **Link** is set to zero to indicate that the Target does not support linked commands.
- **CmdQue** is set to one to indicate that the drive supports command queuing.
- **Vendor ID** is HITACHI padded with ASCII blanks.
- **Product ID** is specified in table 3 of Section 4.3.1.
- **Product Revision Level** indicates the level of microcode.
- **Unit Serial Number** contains the drive serial number.

17.5.1.2 Inquiry Data Format - EVPD = 1 - Page Code = 00h

Table 34: Inquiry Data - EVPD = 1 (Page Code = 00h)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">BIT</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="3">Qualifier = 0</td><td colspan="5">Peripheral Device Type = 0</td></tr>
<tr><td>1</td><td colspan="8">Page Code = 00h</td></tr>
<tr><td>2</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>3</td><td colspan="8">Page Length = 08h</td></tr>
<tr><td>4</td><td colspan="8">Supported Page Code - 00h</td></tr>
<tr><td>5</td><td colspan="8">Supported Page Code - 03h</td></tr>
<tr><td>6</td><td colspan="8">Supported Page Code - 80h</td></tr>
<tr><td>7</td><td colspan="8">Supported Page Code - 83h</td></tr>
<tr><td>8</td><td colspan="8">Supported Page Code = 86h</td></tr>
<tr><td>9</td><td colspan="8">Supported Page Code = 87h</td></tr>
<tr><td>10</td><td colspan="8">Supported Page Code - D1h</td></tr>
<tr><td>11</td><td colspan="8">Supported Page Code - D2h</td></tr>
</table>

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to 0, and this field contains the same value as in the page code field of the INQUIRY command descriptor block.
- **Page length** specifies the length of the following page data.
- **Supported Page Code** field contains the Page Codes supported by the Target. The list is in ascending order.

17.5.1.3 Inquiry Data Format - EVPD = 1, Page Code - 03h

Table 35: Inquiry Data - EVPD = 1 (Page Code = 03h)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = 03h							
2	Reserved = 0							
3	Page Length = 184 (B8h)							
4	ASCII Fields Length = 00h							
5-23	Reserved = 0							
24-35	ASCII uCode Identifier							
36-39	ASCII Servo P/N							
40-41	Major Version							
42-43	Minor Version							
44-47	User Count							
48-51	Build Number							
52-79	Build Date String							
80-81	Code ID							
82-83	Compatibility ID							
84-91	Product ID							
92-99	Interface ID							
100-107	Code Type							
108-119	User Name							
120-135	Machine Name							
136-167	Directory Name							
168-171	Operating State							
172-175	Functional Mode							
176-179	Degraded Reason							
180-183	Broken Reason							
184-187	Code Mode							

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to the value of the page code field in the CDB.

- **Page Length** field specifies the length (in bytes) of the vendor unique VPD information (bytes 4 - 163). If the allocation length of the CDB is too small to transfer all the data, the Page Length field is not adjusted to reflect the truncation.
- **ASCII uCode Identifier** contains the drive's microcode identifier. The field is alphanumeric (ASCII), left aligned, and the unused bytes are ASCII spaces (20h).
- **ASCII Servo P/N** contains the part number of the Servo microcode installed on the drive. This field is hex numeric ASCII (i.e., the characters will be in the set 0...9, A...F).
- **Major Version** and **Minor Version** are version numbers of the code loaded on the drive.
- **User Count** is the number of times the code has been built since the master build.
- **Build Number** is the master build version number.
- **Build Date String** is the date the code on the drive was built, in an extended string format.
- **Code ID** is a binary value for firmware development tracking.
- **Compatibility ID** is a binary value for firmware development tracking.
- **Product ID** is the name of the product this code is for.
- **Interface ID** is the interface type and serial interface speed (e.g. SCSI or FCAL 4Gb) of the code.
- **Code Type** is the intended use of the this code. (e.g. local, released, test)
- **User Name** is the username of the person who built this version of the code.
- **Machine Name** is the workstation on which this version of the code was built.
- **Directory Name** is the last 32 characters of the directory from where this code was built.
- **Operating State** is the drive operating state. The least significant bit contains the following:

0 = OM_BROKEN	We have detected a hardware failure.
1 = OM_DEGRADED	We have a soft failure; i.e., incomplete format. Motor is still spinning.
2 = OM_INACCESSIBLE	Drive is good but motor is stopped.
3 = OM_STARTING	Motor is starting.
4 = OM_SPINNING	Motor is started but reserved area is not loaded yet.
5 = OM_NORMAL	Drive is spinning and ready to read/write.
6 = OM_SLEEP	Drive is ready but has entered power save mode.
7 = OM_STOPPED	Drive has come ready but now has been stopped.

- **Functional Mode** is the drive functional mode. The least significant byte (0x0000000n) contains the following:

0 = OM_NORMAL_MODE	Not in special or recovery mode.
1 = OM_SPECIAL_CMD	Special command mode on.
3 = OM_SPC_RSV_ACCESS	Special cmd mode and access to reserved area allowed.
5 = OM_SPC_SDWNLOAD	Special cmd mode and special download allowed.
7 = OM_SPC _ RACCESS_SDWNLD	Special cmd, access to reserved area, and special download allowed.

The second byte (0x000n0000) contains the following:
0 = Idle functions are not enabled.
1 = Idle functions are enabled.

- **Degraded Reason** (UECType) is why the file is in a degraded mode; i.e., how to exit this mode.
- **Broken Reason** (UECType) is why the drive believes the hardware is broken.
- **Code Mode** is the type of code the drive is running. The least significant bit contains the following:
  - 0 = OM_FLASH Drive is running flash code
  - 1 = OM_FLASH_OVERLAY Drive is running flash overlay code
  - 2 = OM_DISK Drive is running code that has been loaded from disk
  - 3 = OM_TRANSIENT Drive is running code that has been downloaded but not saved

17.5.1.4 Inquiry Data Format - EVPD = 1, Page Code - 80h

Table 36: Inquiry Data - EVPD = 1 (Page Code = 80h)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">BIT</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="3">Qualifier = 0</td><td colspan="5">Peripheral Device Type = 0</td></tr>
<tr><td>1</td><td colspan="8">Page Code = 80h</td></tr>
<tr><td>2</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>3</td><td colspan="8">Page Length = 16 (10h)</td></tr>
<tr><td>4-19</td><td colspan="8">Serial Number (ASCII)</td></tr>
</table>

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to the value of the page code field in the CDB.
- **Page Length** is set to 16, and this field specifies the length of the following page data.
- **Serial Number** gives the drive serial number, right aligned.

17.5.1.5 Inquiry Data - EVPD = 1 (Page Code = 83h)

Table 37: Inquiry Data - EVPD = 1 (Page Code = 83h)

Byte	BIT 7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = 83h							
2	Reserved = 0							
3	Page Length = 12 (0Ch)							
4	Reserved = 0				Code Set = 1			
5	Reserved = 0		Association = 0		Identifier Type = 3			
6	Reserved = 0							
7	Identifier Length = 8							
8-15	(MSB) World Wide ID (LSB)							

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to the value of the page code field in the CDB.
- **Page Length** is set to 12, and this field specifies the length of the following page data.
- **Code Set** field specifies the code set used for the identifier field. The Target supports binary.
- **Association** field is set to 0, indicating that the Identifier field is associated with the logical unit.
- **Identifier Type** field specifies the format and assignment authority for the identifier. The Target supports the value of 03h.
- **World Wide ID** is a 64-bit unique value for each drive. The format is: **5000CCA**h **xxx**h nb yyb where:

  xxx is the 12-bit Block Assignment defined for each model and manufacturing site

  n is the 22-bit drive unique serial number representation

  yy is the 2-bit Port Identifier.

17.5.1.6 Inquiry Data Format - EVPD = 1, Page Code - 86h

Byte	BIT 7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = 86h							
2	Reserved = 0							
3	Page Length = 60 (3Ch)							
4	Reserved = 0				RTO	GRD_CHK	APP_CHK	REF_CHK
5	Reserved = 0			Group_Sup	Prior_Sup	HEADSUP	ORDSUP	SIMPSUP
6	Reserved=0						NV_SUP	V_SUP
7-63	Reserved = 0							

- **RTO (Reference Tag Ownership)** is set to one to indicate that the drive supports application client ownership of the

Logical Block Reference Tag field.

- **GRD_CHK (Guard Check)** is set to one to indicate that the drive checks the Logical Block Guard Tag field in the protection information, if any.
- **APP_CHK (Application Tag Check)** bit is set to one to indicate that the drive checks the Logical Block Application Tag field in the protection information, if any.
- **REF_CHK (Reference Tag Check)** bit is set to one to indicate that the drive checks the Logical Block Reference Tag field in the protection information, if any.
- **GROUP_SUP (Group Supported)** bit is set to zero to indicate that the grouping function is not supported.
- **PRIOR_SUP (Priority Supported)** bit is set to zero to indicate that task priority is not supported.
- **HEADSUP (Head of Queue Supported),** ORDSUP (Ordered Supported), and SIMPSUP (Simple Supported) are set to one to indicate support for Head of Queue, Ordered and Simple task attributes.
- **NV_SUP (Non-volatile Supported)** is set to 0 to indicated that non-volatile cache features are not supported.
- **V_SUP (Volatile Supported)** is set to 1 to indicated support of a volatile cache.

17.5.1.7 Inquiry Data Format - EVPD = 1, Page Code - 87h

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = 87h							
2-3	Page Length = 0004h							
4	Reserved=0		Policy Page Code = 3Fh					
5	Policy Subpage Code = FFh							
6	MILUS=1	Reserved = 0					Mode PagePolicy = 0	
7	Reserved = 0							

- **Policy Page Code** set to 3Fh and Policy Subpage Code set to FFh indicate that the descriptor applies to all mode pages and subpages
- **MILUS (Multiple Logical Units Share)** set to one indicates the policy is shared by multiple logical units.
- **Mode Page Policy** set to 00b indicates that all mode pages and subpages are shared.

17.5.1.8 Inquiry Data Format - EVPD = 1, Page Code - D1h

Table 38: Inquiry Data - EVPD = 1 (Page Code = D1h)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = D1h							
2	Reserved = 0							
3	Page Length = 80 (50h)							
4-19	ASCII Media Disk Definition							
20-35	ASCII Motor Serial Number							
36-51	ASCII Flex Assembly Serial Number							
52-67	ASCII Actuator Serial Number							
68-83	ASCII Device Enclosure Serial Number							

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to the value of the page code field in the CDB.
- **Page Length** is set to 80, and this field specifies the length of the following page data.

Note: If the media is not available, bytes 0 through 3 are valid. All the other fields are ASCII blanks (20h).

Note: All ASCII fields are alphanumeric, left aligned, and padded on the right with ASCII blanks (20h).

17.5.1.9 Inquiry Data Format - EVPD = 1, Page Code - D2h

Table 39: Inquiry Data - EVPD = 1 (Page Code = D2h)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Qualifier = 0			Peripheral Device Type = 0				
1	Page Code = D2h							
2	Reserved = 0							
3	Page Length = 52 (34h)							
4	HDC Version Length = 16 (10h)							
5 - 20	ASCII HDC Version							
21	Card Serial Number Length = 16 (10h)							
22 - 37	ASCII Card Serial Number							
38	Card Assembly Part Number Length = 16 (10h)							
39 - 54	ASCII Card Assembly Part Number							
55	Reserved = 0							

- **Qualifier** is set to zero to indicate that the LUN specified in the Command Block is currently supported.
- **Peripheral Device Type** is set to zero to indicate that the device is Direct Access.
- **Page Code** is set to the value of the page code field in the CDB.
- **Page Length** is set to 52, and this field specifies the length of the following page data.

Note: If the media is not available, bytes 0 through 3 are valid. All the other fields are ASCII blanks (20h).

Note: All ASCII fields are alphanumeric, left aligned, and padded on the right with ASCII blanks (20h).

17.6 LOG SELECT (4C)

Table 40: Log Select (4C)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 4Ch</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="3">Reserved = 0</td><td>PCR</td><td>SP</td></tr>
<tr><td>2</td><td colspan="2">PC</td><td colspan="6">Reserved = 0</td></tr>
<tr><td>3
4
5
6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7
8</td><td colspan="8">(MSB) Parameter List Length = 0
(LSB)</td></tr>
<tr><td>9</td><td colspan="6">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The LOG SELECT command provides a means for the Initiator to clear statistical information maintained by the drive and reported via the LOG SENSE command.

- **PCR** The Parameter Code Reset determines whether the Log Sense parameters will be cleared and unit attention posted for all other Initiators. A value of 1 indicates that the parameters be cleared, while a value of zero (except when PC = 11b) indicates that the parameters not be cleared. Parameter list length must be zero when PCR is 1. The PC field is ignored for list parameters, i.e. when the Format and Linking (F&L) field contains 01b or 11b.
- **SP** The Save Parameters bit value of zero indicates that the page parameters not be saved. A value of 1 indicates that the page parameters that are savable be saved after they have been changed. SP bit MUST be 1 if parameter list length is greater than zero. Otherwise it will result in a *Check Condition* status being returned. The sense key shall be set to *Illegal Request* and additional sense code of *Invalid Field in CDB.*
- **PC** The Page Control field defines the type of parameters to be selected. The PC field set to 11b (and PCR is then a don't care) indicates that the Default Cumulative values are set to their default values of 0. If the PC field is set to 01b and PCR is set to 1, the Current Cumulative values are also set to their default values of 0.

  Parameter List Length MUST be zero when PC = 11b. Otherwise the command is terminated and a *Check Condition* status is returned. The sense key shall be set to *Illegal Request* and additional sense code of *Invalid Field in CDB.*
- **Parameter List Length** The Parameter List Length field specifies the length in bytes of the parameter list that shall be located in the DATA OUT buffer. A parameter list length zero indicates that no pages shall be transferred.

Note: A specified length greater than 0x00FF will result in a *Check Condition* status being returned. A length that results in log data being truncated will generate a *Check Condition* status.

Note: For page 0Fh, the maximum parameter list length supported is 4004h (4 bytes for the header and 100h bytes for each of the 40h parameters that are supported). The Parameter List Length must be an integral of the number of parameters plus the 4 byte header. (Ex: Parameter length =104h for one parameter, 204h for 2 parameters,... 4004h for all 40h parameters).

The drive allows updates to the current cumulative values only. A value of zero is acceptable and is not considered an error. The drive updates only pages 0Eh, the Start/Stop Cycle page and 0Fh, the Application Client page. For other pages the parameters are ignored. If the data out buffer contains multiple pages then the application client should send the pages in ascending order. If the data out buffer contains multiple log parameters within a page,all log parameters within the page should be sent and they should be sent in ascending order by parameter code value. The drive shall return Check Condition status if the application client sends pages out of order, parameter codes out of order or missing parameter code. The sense key shall be set to Illegal Request and additional sense code set to Invalid Field in Parameter List. If one or more fields of the CDB are not set correctly the command will be terminated with a *Check Condition* status. The sense key shall be set to *Illegal Request* and additional sense code of *Invalid Field in CDB*. To indicate that parameters have changed, the Target generates a unit attention condition for all Initiators except the one that issued the LOG SELECT command.

The following list contains all individual page parameters (counters) that are set to their default value of zero by the LOG SELECT command (when PCR=1).

- Page **02h** parameters: (Counters for write errors)
    - Write errors recovered without delay
    - LBAs with write fault error
    - LBAs with ID type error
    - Total errors recovered
    - Number of times recovery invoked
    - Total write byte count
    - LBAs with hard error
- Page **03h** parameters: (Counters for read errors)
    - Read errors recovered without delay
    - LBAs with ECC detected error
    - LBAs with ID type error
    - Total errors recovered
    - Number of times recovery invoked
    - Total read byte count
    - LBAs with hard error.
- Page **05h** parameters: (Counters for Verify Errors)
    - Errors recovered without delay
    - LBAs with ECC detected error
    - LBAs with ID type error
    - Total errors recovered
    - Number of times recovery invoked
    - Total bytes verified
    - LBAs with hard error.
- Page **06h** parameters: (Counters for non medium errors, seek and other hardware type failures)
    - Non-Medium Error Counter
- Page **15h** parameters: (Background Medium Scan information)
    - BMS Status parameter
    - all Medium Scan parameters

- Page **30h** parameters:
  - Zero Seeks counter
  - Seeks > = to 2/3 counter
  - Seeks > = 1/3 and < 2/3 counter
  - Seeks > = 1/6 and < 1/3 counter
  - Seeks > = 1/12 and < 1/6 counter
  - Seeks > 0 and < 1/12 counter
  - Overrun Counter
  - Under run Counter
  - Device Cache Full Read Hits
  - Device Cache Partial Read Hits
  - Device Cache Write Hits
  - Device Cache Fast Writes
  - Device Cache Misses on Reads
- Page **37h** parameters:
  - Media PFA
  - Hardware PFA
  - Total Read Commands
  - Total Write Commands

17.7 LOG SENSE (4D)

Table 41: Log Sense (4D)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 4Dh							
1	Reserved = 0			Reserved = 0			PPC=0	SP
2	PC		Page Code					
3 4	Reserved = 0							
5 6	(MSB) Parameter Pointer = 0 (LSB)							
7 8	(MSB) Allocation Length (LSB)							
9	Reserved = 0						FLAG	LINK

The LOG SENSE command allows the Initiator to retrieve the statistical data regarding the drive.

- **PPC** (Parameter Pointer Control) bit must be set to zero. This specifies that the drive start transferring data starting from the field specified in the parameter pointer field for the number of bytes specified by the allocation length. If the PPC bit is set to 1, *Check Condition* status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB.*
- **SP** (Save Parameters) bit set to 0 specifies that the drive does not save any log parameters. If it is set to 1, all page parameters that are savable (those pages denoted by a DS = 0 in the parameter header control byte) are saved.
- **PC** (Page Control) field defines the type of parameters to be selected. This field must be set to 01b to specify the current cumulative values. Any other value in this field will cause the command to end with a *Check Condition* status with a sense key of *Illegal Request* and an additional sense code of *Invalid Field in CDB.*
- **Page Code** field identifies which page is being requested. This field must be set to the values indicated in Page 0. If the Page Code value is invalid a *Check Condition* status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB.*
- **Parameter Pointer Field** specifies the beginning field for the transfer. This field must be set to 0000h. If the Parameter Pointer Field is not zero a *Check Condition* status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB.*
- **Allocation Length** field specifies the maximum number of bytes the Initiator has allocated for returned Log Sense Data. No bytes are transferred if the length is zero. This condition is not considered an error. TheTarget terminates the Data In phase when all available Log Sense data has been transferred or when the number of bytes equals the allocation length, whichever is less.

17.7.1 Log Page parameters

Each log page begins with a 4-byte page header followed by zero or more variable-length log parameters.

Page header

Page Code field identifies which log page is being transferred.
The Page Length field specifies the length in bytes of the following log parameters.

Log parameters

Each log parameter begins with a 4-byte parameter header followed by one or more bytes of parameter value data.
The Parameter Code field identifies which log parameter is being transferred for that log page.
The Parameter Control field, the 3rd byte of each parameter header, contains several fields.

- **DU** The Disable Update bit is set to 0 to indicate that the drive updates the log parameter value to reflect events that should be noted by that parameter.
- **TSD** The Target Save Disable bit is set to zero to indicate that the drive provides a Target defined method for saving log parameters.
- **ETC** The enable Threshold Comparison bit is set to 0 to indicate the drive does not perform comparisons between cumulative and any threshold values.
- **TMC** The Threshold Met Criteria field is not valid because this drive does not perform threshold comparisons. This field is set to 0.
- **Format and Linking** The F & L field indicates the type of log parameter and how parameters that reach their maximum value are handled.
  - 00b: Data counter: If any other parameter in this log page reaches its maximum value, then this parameter shall stop incrementing until reinitialized by a Log Select command.
  - 01b: List format ASCII data: No maximum values to handle
  - 10b: Data counter: If another parameter reported in this log page reaches its maximum value, then this parameter shall not stop incrementing. This parameter may be reinitialized by a Log Select command.
  - 11b: List format binary data: No maximum values to handle.

17.7.2 Log Sense Page 0

Page 0 indicates the supported log sense pages. This page is used to determine which additional pages an Initiator can request.

Table 42: Log Sense Page 0

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 0					
1	Reserved							
2-3	Page Length = 000Dh(Number of Pages Supported)							
4	First supported page 00h							
5	Second supported page 02h							
6	Third supported page 03h							
7	Fourth supported page 05h							
8	Fifth supported page 06h							
9	Sixth supported page 0Dh							
10	Seventh supported page 0Eh							
11	Eighth supported page 0Fh							
12	Ninth supported page 10h							
13	Tenth supported page 15h							
14	Eleventh supported page 2Fh							
15	Twelfth supported Page Code =30h							
16	Thirteenth supported Page Code = 37h							

17.7.3 Log Sense Page 2

This page contains counters for write errors.

Table 43: Log Sense Page 2 (part 1 of 2)

Byte	Bit 7	6	5	4	3	2	1	0
0	Reserved		Page code = 02h					
1	Reserved							
2-3	PageLength = 54h							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
7	Parameter Length = 08h							
8-15	Errors recovered without delays							
16-17	Parameter Code = 0001h							
18	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
19	Parameter Length = 08h							
20-27	Errors recovered with possible delays							
28-29	Parameter Code = 0002h							
30	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
31	Parameter Length = 08h							
32-39	Reserved = 0							
40-41	Parameter Code = 0003h							
42	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
43	Parameter Length = 08h							
44-51	Total errors recovered							
52-53	Parameter Code = 0004h							
54	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
55	Parameter Length = 08h							
56-63	Times recovery invoked							

Table 44: Log Sense Page 2 (part 2 of 2)

Byte	Bit 7	6	5	4	3	2	1	0
64-65	Parameter Code = 0005h							

66	**DU = 0**	**DS = 0**	**TSD = 0**	**ETC = 0**	**TMC = 0**	**F&L = 00b**
67	**Parameter Length = 08h**					
68-75	**Total bytes written**					
76-77	**Parameter Code = 0006h**					
78	**DU = 0**	**DS = 0**	**TSD = 0**	**ETC = 0**	**TMC = 0**	**F&L = 00b**
79	**Parameter Length = 08h**					
80-87	**Count of hard errors**					

All parameter counts indicate the number of sectors with the specified types of errors, except Times Recovery Invoked, which is a cumulative count of all recovery steps attempted on all sectors written.

17.7.4 Log Sense Page 3

This page contains counters for read errors.

Table 45: Log Sense Page 3 (part 1 of 2)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 03h					
1	Reserved							
2-3	PageLength = 54h							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 0	TSD=0	ETC = 0	TMC = 0		F&L = 00b	
7	Parameter Length = 08h							
8-15	Errors recovered without delay							
16-17	Parameter Code = 0001h							
18	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
19	Parameter Length = 08h							
20-27	Errors recovered with possible delays							
28-29	Parameter Code = 0002h							
30	DU = 0	DS = 0	TSD=0	ETC = 0	TMC = 0		F&L = 00b	
31	Parameter Length = 08h							
32-39	Reserved = 0							
40-41	Parameter Code = 0003h							
42	DU = 0	DS = 0	TSD=0	ETC = 0	TMC = 0		F&L = 00b	
43	Parameter Length = 08h							
44-51	Total errors recovered							
52-53	Parameter Code = 0004h							
54	DU = 0	DS = 0	TSD=0	ETC = 0	TMC = 0		F&L = 00b	
55	Parameter Length = 08h							
56-63	Times recovery invoked							
64-65	Parameter Code = 0005h							

Table 46: Log Sense Page 3 (part 2 of 2)

Byte	Bit							
	7	6	5	4	3	2	1	0

66	**DU = 0**	**DS = 0**	**TSD = 0**	**ETC = 0**	**TMC = 0**	**F&L = 00b**
67	**Parameter Length = 08h**					
68-75	**Total bytes read**					
76-77	**Parameter Code = 0006h**					
78	**DU = 0**	**DS = 0**	**TSD = 0**	**ETC = 0**	**TMC = 0**	**F&L = 00b**
79	**Parameter Length = 08h**					
80-87	**Count of hard errors**					

All parameter counts indicate the number of sectors with the specified types of errors, except Times Recovery Invoked, which is a cumulative count of all recovery steps attempted on all sectors read. ECC-on-the-fly correction is not included in any counters.

17.7.5 Log Sense Page 5

This page contains counters for verify errors.

Table 47: **Log Sense Page 5 (part 1 of 2)**

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 05h					
1	Reserved							
2-3	PageLength = 54h							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
7	Parameter Length = 08h							
8-15	Errors recovered without delay							
16-17	Parameter Code = 0001h							
18	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
19	Parameter Length = 08h							
20-27	Errors recovered with possible delays							
28-29	Parameter Code = 0002h							
30	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
31	Parameter Length = 08h							
32-39	Reserved = 0							
40-41	Parameter Code = 0003h							
42	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
43	Parameter Length = 08h							
44-51	Total errors recovered							
52-53	Parameter Code = 0004h							
54	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
55.	Parameter Length = 08h							
56-63	Times recovery invoked							
64-65	Parameter Code = 0005h							

Table 48: Log Sense Page 5 (part 2 of 2)

Byte	Bit							
	7	6	5	4	3	2	1	0
66	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
67	Parameter Length = 08h							
68-75	Total Bytes Verified							
76-77	Parameter Code = 0006h							
78	DU = 0	DS = 0		TSD = 0	TMC = 0		F&L = 00b	
79	Parameter Length = 08h							
80-87	Count of hard errors							

All parameter counts indicate the number of sectors with the specified types of errors, except Times Recovery Invoked, which is a cumulative count of all recovery steps attempted on all sectors verified. ECC-on-the-fly correction is not included in any counters.

17.7.6 Log Sense Page 6

This page contains counters for non-medium errors. This includes seek errors and other hardware type failures.

Table 49: Log Sense Page 6

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="2">Reserved</td><td colspan="6">Page code = 06h</td></tr>
<tr><td>1</td><td colspan="8">Reserved</td></tr>
<tr><td>2-3</td><td colspan="8">PageLength = 0Ch</td></tr>
<tr><td>4-5</td><td colspan="8">Parameter Code = 00h</td></tr>
<tr><td>6</td><td>DU = 0</td><td>DS = 0</td><td>TSD = 0</td><td>ETC = 0</td><td colspan="2">TMC = 0</td><td colspan="2">F&L = 00b</td></tr>
<tr><td>7</td><td colspan="8">Parameter Length = 08h</td></tr>
<tr><td>8-15</td><td colspan="8">Error count</td></tr>
</table>

17.7.7 Log Sense Page D

This page contains temperature information.

Table 50: Log Sense Page D

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 0Dh					
1	Reserved							
2-3	PageLength = 0Ch							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 1	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
7	Parameter Length = 02h							
8	Reserved							
9	Temperature (degrees Celsius)							
10-11	Parameter Code 0001h							
12	DU = 0	DS = 1	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
13	Parameter Length = 02h							
14	Reserved							
15	Reference Temperature (degrees Celsius)							

17.7.8 Log Sense Page E

This page contains the start-stop cycle information.

Table 51: Log Sense Page E

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 0Eh					
1	Reserved							
2-3	PageLength = 24h							
4-5	Parameter Code = 0001h							
6	DU=0	DS=1	TSD=0	ETC=0	TMC = 0		F&L = 00b	
7	Parameter Length = 06h							
8-11	Year of Manufacture (4 ASCII characters)							
12-13	Week of Manufacture (2 ASCII characters)							
14-15	Parameter Code 0002h							
16	DU=0	DS=0	TSD=0	ETC=0	TMC = 0		F&L = 00b	
17	Parameter Length = 06h							
18-21	Accounting Date Year (4 ASCII characters)							
22-23	Accounting Date Week (2 ASCII characters)							
24-25	Parameter Code 0003h							
26	DU=0	DS=1	TSD=0	ETC=0	TMC = 0		F&L = 00b	
27	Parameter Length = 04h							
28-31	Specified cycle count over device lifetime							
32-33	Parameter Code 0004h							
34	DU=0	DS=1	TSD=0	ETC=0	TMC = 0		F&L = 00b	
35	Parameter Length = 04h							
36-39	Accumulated start-stop cycles (4 byte binary number)							

The week and year that the device was manufactured shall be set in the parameter field defined by parameter code 0001h. The date of manufacture cannot be saved using the LOG SELECT command. The data is expected in numeric ASCII characters (30-39h) in the form YYYYWW. The accounting date specified by parameter code 0002h is a parameter that can be saved using the LOG SELECT command.

17.7.9 Log Sense Page F

This page contains the Application Client Log.

Table 52: Log Sense Page F

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 0Fh					
1	Reserved							
2-3	Page length = 4000h							
	Application client log parameter							
4-259	1st application client log parameter							
16132-16387	64th application client log parameter							

The following table describes the application client log parameter structure.

Table 53: Log Sense Page F, Application Client Log

Byte	Bit							
	7	6	5	4	3	2	1	0
0-1	Parameter code							
2	DU = 1	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
3	Parameter length = FCh							
4-	First parameter byte							
255	Last parameter byte							

Parameter code 0000h through 003Fh are supported.

The values stored in the parameter bytes represent data sent to the device in a previous LOG SELECT command.

17.7.10 Log Sense Page 10

This page contains self-test results. The results of the 20 most recent self-tests are stored in this Log page.

Table 54: Log Sense Page 10

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 10h					
1	Reserved							
2-3	PageLength = 190h							
4-23	1st self-test results log parameter							
384- 403	20th self-test results log parameter							

The following table describes the self-test results log parameter structure

Table 55: Log Sense Page 10, self-test results

Byte	Bit							
	7	6	5	4	3	2	1	0
0-1	Parameter code							
2	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 11b	
3	Parameter Length = 10h							
4	Function Code			RSVD	Self-Test Results Value			
5	Extended Segment Number							
6-7	Timestamp							
8-15	LBA of First Failure							
16	Reserved				Sense Key			
17	Additional Sense Code							
18	Additional Sense Code Qualifier							
19	Vendor specific							

- **Parameter Code** identifies the log parameter for the log page. The parameter code field for the results of the most recent test will be 0001h. The parameter for the next most recent will be 0002h.
- **Function Code** contains the content of the Function Code field in the SEND DIAGNOSTIC command that initiated this self-test.
- **Self-Test Results Value** is described in the table below.

Table 56: Log Sense Page 10, self-test results

Value	Description
0h	**The self-test routine completed without error.**
1h	**The background self-test routine was aborted by the initiator using a SEND DIAGNOSTIC command with the Abort Background self-test function.**
2h	**The self-test routine was aborted by the application client by a Task Management function or a reset.**
3h	**An unknown error occurred while the Target was executing the self-test routine and the Target was unable to complete the self-test routine.**
4h	**The self-test completed with a test element that failed and it is not known which test element failed.**
5h	**The first segment of the self-test failed.**
6h	**The second segment of the self-test failed.**
7h	**The third or greater segment of the self-test failed (see the Extended segment number field).**
8h-Eh	**Reserved.**
Fh	**The self-test is in progress.**

- **Extended Segment Number** This field identifies the number of the segment that failed during self-test. If no segment failed, this field will be 00h.

Table 57: Log Sense Page 10, Extended Segment Number

Extended Segment Number	Short Self-Test	Extended Self-Test
1h	Drive Ready Test	
2h	Drive Diagnostics	
3h	SMART	
4h	Low Level Format check	
5h	Physical Head Check	
6h	Random Verify	
7h	- Verify First 300 MB - Verify Last 100 MB	Verify all LBAs
8h	Recheck SMART	

- **Timestamp** This field contains the total accumulated power-on hours of the Target at the time the self-test completed.
- **LBA of first failure** This field contains the LBA of the first logical block address where a self-test error occurred. If no errors occurred during the self-test or the error is not related to a LBA then the field will be FFFFFFFFFFFFFFFFh.
- **Sense Key, Additional Sense Code and Additional Sense Code Qualifier** These fields will contain the additional information relating to the error or exception conditions during self-test.

See Section 19.40 “SEND DIAGNOSTIC (1D)” on page 262, for detailed listing of operations carried out by SEND DIAGNOSTIC command and Power on Diagnostics.

17.7.11 Log Sense Page 15

This page contains information about Background Medium Scan operations.

Table 58: Log Sense Page 15

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 15h					
1	Reserved							
2-3	Page Length = (19 + 24N -3)							
	Background Medium Scan parameters							
4-19	BMS Status Parameter							
20-43	First Medium Scan Parameter							
	...							
19+24N	Last Medium Scan Parameter							

The following table describes the BMS Status Parameter structure.

Byte	Bit							
	7	6	5	4	3	2	1	0
0-1	Parameter Code = 0000h							
2	DU=0	DS=0	TSD=0	ETC=0	TMC=0		F&L = 11b	
2-3	Page Length = 0Ch							
4-7	Power On Minutes							
8	Reserved = 0							
9	BMS Status							
10-11	Number of Scans Performed							
12-13	Medium Scan Progress							
14-15	Reserved = 0							

- **Power On Minutes** indicates the total power on minutes at the time the log page is requested
- **BMS Status** is described in the following table

BMS Status	Description
00h	No scans active
01h	Background medium scan is active
03h-04h	Not supported

BMS Status	Description
05h	**Background scan halted due to medium formatted without P-List**
06h	**Background scan halted due to a vendor-specific cause**
07h	**Background scan halted due to temperature out of range**
08h	**Scan suspended until BMS Interval Timer expires**
09h - FFh	**Reserved**

- Number of Scans Performed indicates the number of background scans that have been performed over the life of the-drive.
- Medium Scan Progress is a percent complete indication of the medium scan. The returned value is a numerator that has 65,536 (1 00 00h) as its denominator.

The following table describes the Medium Scan Parameter structure.

Byte	Bit							
	7	6	5	4	3	2	1	0
0-1	Parameter Code = 0001h - 0800h							
2	DU=0	DS=0	TSD=0	ETC=0	TMC=0		F&L = 11b	
2-3	Page Length = 14h							
4-7	Power On Minutes							
8	Reassign Status				Sense Key			
9	Additional Sense Code							
10	Additional Sense Code Qualifier							
11	Head				(MSB) Cylinder			
12-13	(MSB) Cylinder (LSB)							
14-15	(MSB) Sector (LSB)							
16-23	LBA							

- **Power On Minutes** indicates the total power on minutes at the time the error was detected.
- **Reassign Status** is set as shown below. Reassignment during the background scan is not supported.

Reassign Status	Description
0h	**No reassignment needed**
1h	**Reassignment pending receipt of Reassign command or write command (if auto write reallocation is allowed) from the initiator**

Reassign Status	Description
02h-5h	**Not supported**
6h - Fh	**Reserved**

Additional Sense Code and Additional Sense Code Qualifier provide details about the error detected.

17.7.12 Log Sense Page 2F

This page contains SMART Status and Temperature Reading.

Table 59: Log Sense Page 2F

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 2Fh					
1	Reserved							
2-3	PageLength = 8							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 11b	
7	Parameter Length = 04h							
8	SMART Sense Code Byte							
9	SMART Sense Qualifier							
10	Most Recent Temperature Reading							
11	Vendor HDA Temperature Trip Point							

17.7.13 Log Sense Page 30

This page contains Performance Counters.

Table 60: Log Sense Page 30

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved		Page code = 30h					
1	Reserved							
2-3	Page Length = 0030h							
4-5	Parameter Code = 0000h							
6	DU = 0	DS = 0	TSD = 0	ETC = 0	TMC = 0		F&L = 00b	
7	Parameter Length = 2Ch							
8-9	Zero Seeks							
10-11	Seeks > = to 2/3							
12-13	Seeks > = 1/3 and < 2/3							
14-15	Seeks > = 1/6 and < 1/3							
16-17	Seeks > = 1/12 and < 1/6							
18-19	Seeks > 0 and < 1/12							
20-23	Reserved = 0							
24-25	Overrun Counter							
26-27	Under run Counter							
28-31	Device Cache Full Read Hits							
32-35	Device Cache Partial Read Hits							
36-39	Device Cache Write Hits							
40-43	Device Cache Fast Writes							
44-47	Device Cache Read Misses							
48-51	Reserved = 0							

Page 30h returns performance counter information. This includes seek counters and buffer overrun/under run counters.

The appropriate seek counter is incremented once during execution of Pre-Fetch, Read, Verify, Write, Write and Verify, Write Same, and Seek commands.

Buffer Overrun conditions are detected during Read commands.

Buffer Underrun conditions are detected during Verify with ByteChk=1, Write, Write and Verify, and Write Same commands.

Only one seek counter is incremented for each of these commands and the counter is incremented only once per command. The length of the initial seek that is required to access the first Logical Block specified for the SCSI command determines which seek counter is incremented. The Zero Seek counter is incremented if a seek is not required or if only a head switch is required to access the first Logical Block. After the initial seek, no further counter incrementing is performed for that command.

Note: The length of a seek as reported in page 30 may differ from expected results. The reason for this is that the drive executes Idle Time Functions between operations of the drive. The seek operations that occur in Idle Time Functions are not directly entered into page 30 seek counters but they change the length of the following seek. This is because after the Idle Time Function is completed, the heads will not necessarily be in the same position as they were at the completion of the previous command.

A buffer overrun or under run condition occurs when the Initiator does not transfer data to or from the Target data buffer fast enough to keep up with reading or writing the media. The buffer overrun counter is incremented during operations that require a Data In phase when a buffer full condition prevents the continued transfer of data from the media to the data buffer. The buffer under run counter is incremented during operations that require a Data Out phase when a buffer empty condition prevents the start or continuation of a data transfer from the data buffer to the media (or a data transfer from the media for a Verify command with BytChk=1).

Buffer Overrun conditions are detected during the following SCSI commands:

- READ (6)
- READ (10)

Buffer Under Run conditions are detected during the following SCSI commands:

- VERIFY WITH BytChk=1
- VERIFY (16) WITH BytChk=1
- WRITE (6)
- WRITE (10)
- WRITE AND VERIFY
- WRITE AND VERIFY (16)
- WRITE SAME
- WRITE SAME (16)
- **ZERO SEEKS**
  The number of times no seek was required. The operation may have resulted in a head switch.

- **SEEKS >= 2/3 DISK**
  The number of seeks equal to or greater than 2/3 of the disk.

- **SEEKS >= 1/3 AND < 2/3 DISK**
  The number of seeks equal to or greater than 1/3 and less than 2/3 of the disk.

- **SEEKS >= 1/6 AND < 1/3 DISK**
  The number of seeks equal to or greater than 1/6 and less than 1/3 of the disk.

- **SEEKS >= 1/12 AND < 1/6 DISK**
  The number of seeks equal to or greater than 1/12 and less than 1/6 of the disk.

- **SEEKS > 0 AND < 1/12 DISK**
  The number of seeks less than 1/12 of the disk.

- **OVERRUN COUNTER**
  The number of times that data was available to be transferred from the media but the device buffer still contained data that had not been retrieved by the Initiator. Consequently, the disk had to take additional revolutions until the buffer was available to accept data.

- **UNDER RUN COUNTER**

The number of times that the drive was ready to transfer data to its disk (on a write), but its buffer was empty (i.e., had not been filled by the Initiator), thus the disk was forced to take extra revolutions.

- **DEVICE CACHE FULL READ HITS**
  The number of times that all of the data requested by the read operation was obtained from the device read or write cache.

- **DEVICE CACHE PARTIAL READ HITS**
  The number of times that a portion, but not all, of the data requested by the read operation was obtained from the device read or write cache. A physical operation to the device media was required to obtain the remaining data.

- **DEVICE CACHE WRITE HITS**
  The number of times that the data associated with a write operation replaces, or is combined with, existing data in the device write cache, thereby eliminating a write operation.

- **DEVICE CACHE FAST WRITES**
  The number of times that space was available in the device write cache for the data associated with a write operation and a response was returned immediately.

- **DEVICE CACHE READ MISSES**
  The number of times that none of the data requested by the read operation was obtained from the read cache.

The statistics reported by this page are lost on a self-initiated reset or when the Drive is powered off. Even though the DS field equals zero, the parameters on this page are not savable.

17.7.14 Log Sense Page 37

This page contains a series of miscellaneous data counters including information about predictive failure analysis occurrences.

Table 61: Log Sense Page 37

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="2">Reserved</td><td colspan="6">Page code = 37h</td></tr>
<tr><td>1</td><td colspan="8">Reserved</td></tr>
<tr><td>2-3</td><td colspan="8">Page Length = 0030h (48)</td></tr>
<tr><td>4-5</td><td colspan="8">Parameter Code = 0000h</td></tr>
<tr><td>6</td><td>DU=0</td><td>DS=0</td><td>TSD=0</td><td>ETC=0</td><td colspan="2">TMC = 0</td><td colspan="2">F&L = 00b</td></tr>
<tr><td>7</td><td colspan="8">Parameter Length = 2Ch</td></tr>
<tr><td>8
-
11</td><td colspan="8">(MSB)
Power on Hours (hours only)
(LSB)</td></tr>
<tr><td>12
-
19</td><td colspan="8">(MSB)
Total Bytes Read
(LSB)</td></tr>
<tr><td>20
-
27</td><td colspan="8">(MSB)
Total Bytes Written
(LSB)</td></tr>
<tr><td>28</td><td colspan="8">Max Drive Temp (degrees Celsius)</td></tr>
<tr><td>29 - 30</td><td colspan="8">(MSB) GList Size
(LSB)</td></tr>
<tr><td>31</td><td colspan="8">Number of PFA Occurrences</td></tr>
<tr><td>32</td><td>MED PFA</td><td>HDW PFA</td><td colspan="6">Reserved = 0</td></tr>
<tr><td>33 - 40</td><td colspan="8">Total Read Commands</td></tr>
<tr><td>41 - 48</td><td colspan="8">Total Write Commands</td></tr>
<tr><td>49</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>50-51</td><td colspan="8">Flash Correction Count</td></tr>
</table>

The **Power on Hours** field specifies the total time the drive has been powered on in hours only.

The **Max. Drive Temperature** field specifies the maximum temperature, in degrees Celsius, the drive has ever reached.

The **Glist Size** field gives the total number of LBAs that have been reassigned on the drive.

The **Number of PFA Occurrences** field gives the number of PFA occurrences during the life of the drive and not the number of PFA events that have been reported. The number of reported PFA events may be less due to the settings of Mode Page 0x1C.

If set, the **Media** and **Hardware PFA** bits indicate that a PFA trip has occurred during the life of the drive. These flags are set during a PFA occurrence that may or may not coincide with the reporting of a PFA event as mentioned above.

Total Read Commands counter is incremented for each Read (6) and Read (10) command received.

Total Write Commands counter is incremented for each Write (6), Write (10), Write Verify and Write Verify (16) command received.

Flash Correction Count is incremented each time ECC correction is applied to data stored in Flash ROM.

17.8 MODE SELECT (15)

Table 62: Mode Select (15)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 15h							
1	Reserved = 0			PF=1	Reserved = 0			SP
2 3	Reserved = 0							
4	Parameter List Length							
5	VU = 0		Reserved = 0				FLAG	LINK

The MODE SELECT (15) command provides a means for the Initiator to specify LUN or device parameters to the Target. It also allows an Initiator to specify options the Target uses in error recovery, caching, and formatting.

There is a single set of Mode Page parameters shared by all Initiators.

- **PF** A PF (Page Format) bit value of one indicates that the data sent by the Initiator after the Mode Select Header and the Block Descriptor, if any, complies to the Page Format. The Target ignores this field since it only accepts mode parameters in the Page Format.
- **SP** Save Pages. This indicates

0 The drive shall not save the pages sent during the Data Out phase but will use them for all following commands until the power is removed, a reset is received, or a new MODE SELECT command is received.

1 The drive will save the data in the reserved area of the disk. It will be used for all the following commands until another MODE SELECT command is issued. This information is maintained over a power cycle or reset of the drive.

- **Parameter List Length** This specifies the number of bytes to be sent from the Initiator. A parameter list length of zero suppresses data transfer and is not considered an error.

The MODE SELECT parameter list contains a 4-byte header followed by zero or one block descriptor followed by zero or more pages. The pages that are valid with this command are defined in the addendum under the heading **Mode Select Data**, as they vary with the drive model.

Application Note

The Initiator should issue a MODE SENSE command requesting all Changeable values (see PCF field in byte two of the CDB in) prior to issuing a MODE SELECT command. This is necessary to find out which pages are implemented by the drive and the length of those pages. In the Pages of the MODE SENSE command the drive will return the number of bytes supported for each Page. The Page Length set by the Initiator in the MODE SELECT command must be the same value as returned by the drive in MODE SENSE Page Length. If not, the drive will return *Check Condition* status with sense key of *Illegal Request*.

Note: If an Initiator sends a MODE SELECT command that changes any parameters that apply to other Initiators, the drive shall generate an unit attention condition for all Initiators except for the one that issued the MODE SELECT command. The drive shall set the additional sense code to *Parameters Changed* (2Ah).

17.9 MODE SELECT (55)

Table 63: Mode Select (55)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 55h</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td>PF=1</td><td colspan="3">Reserved = 0</td><td>SP</td></tr>
<tr><td>2-6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">(MSB) Parameter List Length (LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The MODE SELECT (55) command provides a means for the Initiator to specify LUN or device parameters to the Target. See the MODE SELECT (15) command for a description of the fields in this command.

17.10 MODE SENSE (1A)

Table 64: Mode Sense (1A)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 1Ah							
1	Reserved			RSVD	DBD	Reserved = 0		
2	PCF		Page Code					
3	Subpage Code							
4	Allocation Length							
5	VU = 0		Reserved = 0				FLAG	LINK

The MODE SENSE (1A) command provides a means for the drive to report various device parameters to the Initiator. It is the complement to the MODE SELECT command.

If the **DBD** (Disable Block Descriptor) bit is zero, the Target will return the Block Descriptor. If the DBD bit is set to 1, the Target will not return the Block Descriptor.

Allocation Length indicates the maximum number of bytes that the Initiator has set aside for the DATA IN phase. A value of zero is not considered an error. If the allocation length is smaller than the amount available, that portion of the data up to the allocation length will be sent. This may result in only a portion of a multi-byte field being sent.

Page Control Field: PCF (Page Control Field) defines the type of Page Parameter values to be returned.

PCF Meaning

0 0 **Report current values.** The drive returns the current values under which the logical unit is presently configured for the page code specified. The current values returned are

1. Initially following power-up but before the media is accessed, the default values become current. Once the media can be accessed, the saved values are read from the Reserved Area and become current.
2. The parameters set in the last successful MODE SELECT command.
3. The saved values if a MODE SELECT command has not been executed since the last power-on, hard RESET condition, or TARGET RESET message.

Following the completion of start-up, execution of the MODE SELECT command can modify the current values.

Note: Those parameters associated with format are not considered current and are not saved until the successful completion of a FORMAT UNIT command.
In addition, the current values take on the saved values after a reset if the parameters were saved. If the Page Code is 3Fh, then all pages implemented by the Target are returned to the Initiator with fields and bit values set to current values.
If the Page Code is not 3Fh, the page defined by the Page Code, if supported by the Target, is returned with fields and bits set to current values.

Note: The drive will not process the MODE SELECT command until the completion of spin-up. Therefore, the Initiator cannot modify the current values prior to the saved values being read in.

0 1 **Report changeable value.** The drive returns the changeable values for the page code specified. The page requested is returned containing information that indicates which fields are changeable. All bits of parameters that are changeable shall be set to one. Parameters that are *defined by the drive* shall be set to zero. If any part of a field is changeable, all bits in that field shall be set to one.

Note: For a value field such as the buffer ratios of page 2 the bit field will not indicate the range of supported values but rather that the field is supported.

1 0 **Report default value.** The drive returns the default values for the page code specified. The parameters not supported by the drive are set to zero.

1 1 **Report saved value.** The drive returns the saved value for the page code specified.

Saved values are one of the following:

- the values saved as a result of MODE SELECT command
- identical to the default values
- zero when the parameters are not supported

The Page Length byte value of each page returned by the drive indicates up to which fields are supported on that page.

Page Code: This field specifies which page or pages to return. Page code usage is defined in the figure below.

Table 65: Page Code Usage

Page Code	Description
00h - 1Ch	**Return specific page, if supported.**
3Fh	**Return all supported pages.**

If a Page Code of 3Fh is used, MODE SENSE returns the pages in ascending order with one exception. Page 0 is always returned last in response to a MODE SENSE command.

If an unsupported page is selected, the command is terminated with a CHECKT CONDITION status and available sense of ILLEGAL REQUEST/INVALID FIELD IN CDB.

Subpage Code: This field specifies the subpage to return, and may be set to a specific page, or to FFh for all supported subpages.

17.10.1 Mode Parameter List

The mode parameter list contains a header followed by zero or more block descriptors followed by zero or more variable length pages.

17.10.1.1 Header

The 6-byte command descriptor block header is defined below.

Table 66: Mode parameter header (6)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Mode Data Length							
1	Medium Type = 0							
2	WP=0	Reserved=0		DPOFUA =1	Reserved = 0			
3	Block Descriptor Length (= 0 or 8)							

The 10-byte CDB header is defined below.

Table 67: Mode parameter header (10)

Byte	Bit							
	7	6	5	4	3	2	1	0
0 1	(MSB)			Mode Data Length				(LSB)
2	Medium Type = 0							
3	WP=0	Reserved=0		DPOFUA =1	Reserved = 0			
4 5	Reserved = 0							
6 7	(MSB)			Block Descriptor Length (= 0 or 8)				(LSB)

- **Mode Data Length**. When using the MODE SENSE command, the mode data length field specifies the length in bytes of the following data that is available to be transferred. The mode data length does not include the length byte itself. When using the MODE SELECT command, this field is reserved.
- **Medium Type** field is always set to zero in the drive (Default Medium Type).
- **WP**. When used with the MODE SELECT command, the Write Protect (WP) bit is reserved.
  When used with the MODE SENSE command, a Write Protect (WP) bit of zero indicates that the medium is write enabled.
- **DPOFUA** bit value of 1 indicates that the Target supports the FUA and DPO bits in the Read and Write Commands.
- **Block Descriptor Length** specifies the length in bytes of the block descriptors. When used with the MODE SELECT command, zero or eight is supported by the drive. When used with the MODE SENSE command, the drive returns eight to indicate that only a single block descriptor is available.

Note: DPOFUA is ignored during Mode Select command processing although the SCSI Standard states that it is reserved during Mode Select. Ignoring it allows the Mode Sense Parameter List for the byte containing this bit to be re-used as a Mode Select Parameter List.

17.10.1.2 Block Descriptor

Table 68: Mode Parameter Block Descriptor

Byte 0	**(MSB)**
Byte 1	**Number of Blocks**
Byte 2	
Byte 3	**(LSB)**
Byte 4	**Density code = 0**
Byte 5	**(MSB)**
Byte 6	**Block Length**
Byte 7	**(LSB)**

The Block descriptor provides formatting information about the Number of Blocks (user addressable) to format at the specified Block Length.

- Number of Blocks

  When used with the MODE SELECT command, the **Number of Blocks** field must be

  - Zero to indicate not to change available blocks
  - 0xFFFFFFFF to indicate all available blocks
  - The exact number of blocks in the data area of the drive, which can be obtained with the MODE SENSE
  - The number of blocks less than exact one, in order to **CLIP** the number of blocks

  Any other value is invalid and causes the command to fail with *Check Condition* status.

  When used with the MODE SENSE command, the field contains the exact number of blocks.

- Density Code
  - Always 0 for direct access devices.
- Block Length

  The Block Length field reflects the number of bytes of user data per sector (not including any protection information). When used with the MODE SELECT command, the **Block length** field must contain the value from 512 to 528 (8 bytes step) or zero. Otherwise the drive will terminate the command with *Check Condition* status.

  A FORMAT UNIT command is required to cause these parameters to become current only if the block length parameter is different from the current block length.

  When used with the MODE SENSE command, the field is dependent on how the media is currently formatted.

17.10.1.3 Page Descriptor

Table 69: Mode Parameter Page Format

Byte 0	**PS**	**SPF**	**Page Code**
Byte 1	**Page Length**		
Byte 2-n	**Mode Parameters**		

Each mode page contains a page code, a page length, and a set of mode parameters.

When using the MODE SENSE command, a Parameter Savable (PS) bit of one indicates that the mode page can be saved by the drive in the reserved area of the drive. A PS bit of zero indicates that the supported parameters cannot be saved. When using the MODE SELECT command, the PS bit is reserved (zero).

SPF (Sub-Page Format) is set to zero to indicate the short page format is used. The bit is set to one to indicate the long format is used, supporting sub-pages. The drive supports the following mode page codes:

Table 70: Mode Parameter Page Format

Page	Description	PS
00	**Vendor Unique Parameters**	**1**
01	**Read-Write Error Recovery Parameters**	**1**
02	**Disconnect/Reconnect Control Parameters**	**1**
03	**Format Device Parameters**	**0**
04	**Rigid Disk Geometry Parameters**	**0**
07	**Verify Error Recovery Parameters**	**1**
08	**Caching Parameters**	**1**
0A	**Control Mode Page**	**1**
0C	**Notch Parameters**	**1**
19	**Port Control Page**	**1**
1A	**Power Control Parameters**	**1**
1C	**Informational Exceptions Control**	**1**

The page length field specifies the length in bytes of the mode parameters that follow. If the Initiator does not set this value to the value that is returned for the page by the MODE SENSE command, the drive will terminate the command with *Check Condition* status.

17.10.2 Mode Page 00 (Vendor Unique Parameters)

Table 71: Vendor Unique Parameters - Page00

Byte	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Default
0	PS	0	Page Code = 00h						80h
1	Page Length = 0Eh								0Eh
2	Ignored			UAI	MRG	Ignored			10h
3	ASDPE	VGMDE	CMDAC	Ignored			RRNDE	Ignored	20h
4	Ignored								00h
5	Ignored			FDD	DPSDP	Ignored	CAEN	Ignored	02h
6	IGRA	AVERP	Ignored		OCT (high nibble)				00h
7	Overall Command Timer (low byte)								00h
8	Ignored				LED Mode				00h
9	Temperature Threshold								00h
10	Command Aging Limit (Hi byte)								00h
11	Command Aging Limit (Low byte)								30h
12	Read Reporting Threshold								0Ah
13	Write Reporting Threshold								0Ah
14	DRRT	Ignored			FFMT	Ignored			00h
15	Ignored		FCERT	Ignored			Reserved = 0		00h

- **UAI** (Unit Attention Inhibit) bit is not used during normal operation when the UAI jumper is removed from the drive. It may however be changed by the user with no effects. If the UAI jumper is added to the drive, then this bit controls the generation of unit attention conditions.
- **MRG** (Merge Glist into Plist) bit is set to 1 for merging the Glist entries into the Plist during FORMAT UNIT command.
- **ASDPE** (Additional Save Data Pointer Enable) bit is used to control the sending of additional save data pointers messages. When set it will cause a save data pointers message to be sent on every disconnection. This bit is only used by the Target after the Default Mode parameter values are overridden with the Saved values that are read from the Reserved Area of the media as a part of the motor startup sequence. Before the Saved values are read from the Reserved Area of the media, the Save Data Pointer message is always sent to the Initiator prior to disconnection. When not set, a save data pointers message is sent only if the current connection contained a data phase and a further data phase will be required to complete the command.
- **VGMDE** (Veggie Mode) bit set to 1 will cause the drive to execute random self-seeks. To enable this mode, the initiator must perform the mode select to set the bit while the drive is spinning, then Stop Unit, then Start Unit. VGMDE set to 0 disables the self-seeks (normal operation).
- **CMDAC** (Command Active) bit in conjunction with **LED Mode** bits determines if an LED on the drive is activated while commands are active. If CMDAC bit is one and LED Mode bits are zero, an LED driver is active when a command is queued or executed.

- **RRNDE** (Report Recovered Non Data Errors) bit controls the reporting of recovered Non Data Errors when the PER bit is set. If RRNDE is set, recovered Non Data Errors are reported. If the RRNDE bit is not set, then recovered Non Data Errors are not reported.
- **FDD (**Format Degraded Disable) controls the reporting of Format Degraded sense data for Test Unit Ready commands when the drive is in a format degraded state. When the FDD bit is one, Format Degraded sense data will not be reported for a Test Unit Ready command. When the FDD bit is zero, Format Degraded sense data will be reported for Test Unit Ready commands when the drive is in a format degraded state. This bit does not affect the reporting of Format Degraded conditions for any media access commands.
- **DPSDP** The Data Phase Save Data Pointer bit controls whether the Drive sends a Save Data Pointer message at the end of the data phase. A DPSDP bit of 0 indicates that the Drive sends a Save Data Pointer message prior to disconnection only if the following conditions are true:

  - A data phase has occurred since the connection for the current command was established.
  - Another data phase is required to successfully complete the command.

  A DPSDP bit of 1 indicates that the Drive will send a Save Data Pointer message prior to every disconnection once a data phase has occurred for the current command. If the ASDPE bit is set to 1, the Save Data Pointer message is sent prior to every disconnect regardless of the value of the DPSDP bit.
- **CAEN** (Command Aging Enable) When set this bit causes the Command Age Limit timer to be used to avoid commands waiting in the command queue for an indefinite period. When commands have been in the queue for a period of time greater than the timer limit they will be reordered to be executed on a first come first served basis. When this bit is reset, commands are always executed based on the queue reordering rules.
- **IGRA** (Ignore Reassigned LBA) bit works in conjunction with the RC bit (Mode Page 01h, byte 2, bit 4). The main purpose of this bit is to avoid undesirable read processing time delays due to reassigned LBA processing for continuous data availability requirements such as Audio Visual applications. If IGRA is set to one and RC is set to one, out-of-line reassigned LBAs will not be processed. If IGRA is set to one and RC is set to zero, or if IGRA is set to zero, reassigned LBAs will be processed normally.
- **AVERP** (AV ERP Mode) bit is set to one in order to specify maximum retry counts during Read DRP. When AVERPbit is set to one, the maximum retry counts for read operations is specified by Read Retry Count (Mode Page 1 Byte 3). AVERP bit is set to zero to specify that the drive shall process read DRP up to the default maximum retry count when Read Retry Count is set to a non-zero value.
- **OCT** (Overall Command Timer) controls the maximum command execution time, from receipt by the drive until status is returned. If the command is unable to complete in the specified amount of time, it will be aborted with Check Condition status, Aborted Command sense key. The Overall Command Timer does not alter the behavior of the Command Aging Limit or Recovery Time Limit. Each unit of this timer is 50 milliseconds. Setting the value to zero disabled the feature.
- **LED Mode** is designed to control the operation of a drive LED driver.

  - LED Mode = 0000b

    The CMDAC bit controls the LED.

    CMDAC = 1 (Command Active)

    CMDAC = 0 (Motor Active)
  - LED Mode = 0001b (Motor Active)

    When the motor is spinning, the LED is high.
  - LED Mode = 0010b (Command Active).

    When there is a command active or in the queue, the LED is high.
- **Temperature Threshold** specifies the threshold value in degrees Celsius for the thermal sensor warning message. A value of 0 selects the default value (85 degrees Celsius).

- **Command Aging Limit** This value controls the maximum time a command should wait in the command queue when the CAEN bit is set. Each unit of this timer is 50 ms.
- **Read Reporting Threshold** specifies the error reporting threshold for recovered data errors during read operations when PER=1.
- **Write Reporting Threshold** specifies the error reporting threshold for recovered data errors during write operations when PER=1.
- **DRRT** (Disable Restore Reassign Target) bit disables the reading and restoration of the target LBA during a Reassign Blocks command. If the DRRT bit is zero, the reassign command attempts to restore the target LBA's data. If the data cannot be restored, the target LBA is reassigned and written with a data pattern of all 00s. If the DRRT bit is one, no attempt is made to restore the target LBA.
- **FFMT** (Fast Format Enable) bit allows the formatting of the drive without any writes to the customer media. All format operations are allowed including changing block sizes and manipulating defects. The drive will operate normally after a fast format with the following caveat: since no data is written to any customer data blocks as a result of a Fast Format operation, there is a possibility that a read attempt to any particular block (without having previously written to that block) will result in an unrecoverable data error. This will most likely happen if the block size is changed as every LBA will contain data of an incorrect length and apparently an incorrect starting point. It is also possible to generate an uncorrectable data error without changing block sizes if the defect list is shortened and previously bad blocks become visible in the customer address space. Of course ALL DATA ON THE DRIVE WILL BE LOST as the result of any format operation and so any attempt to read blocks which have not been written to will result in unpredictable behavior.
- **FCERT** (Format Certification) bit determines whether the certification step will be performed during a Format Unit command. FCERT bit set to 0 disables certification. FCERT bit set to 1 enables the certification step.

17.10.3 Mode Page 01 (Read/Write Error Recovery Parameters)

Table 72: Mode Page 01 (Vendor Unique Parameters)

Byte	Bit 7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 01h						81h
1	Page Length = 0Ah								0Ah
2	AWRE	ARRE	TB	RC	EER=0	PER	DTE	DCR	C0h
3	Read Retry Count								01h
4	Obsolete = 0								00h
5	Obsolete = 0								00h
6	Obsolete = 0								00h
7	Reserved								00h
8	Write Retry Count								01h
9	Reserved								00h
10 11	(MSB) Recovery Time Limit (LSB)								00h

The Read-Write recovery parameters that will be used during any command that performs a read or write operation to the medium are as follows:

- **AWRE** Automatic Write Reallocation Enabled bit, set to zero indicates that the drive shall not perform automatic reallocation of defective data blocks during write operations. An AWRE bit set to one indicates that the drive shall perform automatic reallocation of defective data blocks during write operations.
- **ARRE** Automatic Read Reallocation Enabled bit, set to zero indicates that the drive shall not perform automatic reallocation of defective data blocks during read operations. ARRE bit set to one indicates that the drive shall perform automatic reallocation of defective data blocks during read operations.
- **TB** Transfer Block bit, set to one indicates that a data block that is not recovered within the recovery limits specified shall be transferred to the Initiator before *Check Condition* status is returned. A TB bit set to zero indicates that such a data block shall not be transferred to the Initiator. Data blocks that can be recovered within the recovery limits are always transferred regardless of the value of the bit.
- **RC** Read Continuous bit, set to one requests the Target to transfer the entire requested length of data without adding delays that would increase or ensure data integrity. This implies that the Target may send erroneous data. This bit has priority over all other error control bits (PER, DTE, DCR, TB). RC set to zero indicates normal interpretation of PER, DTE, DCR, and TB values. The RC bit setting is used by the Target when reporting errors associated with the transfer of the Initiator's data for the Read commands interpretation of PER, DTE, DCR, and TB values. The RC bit applies only to READ commands.

  Note: The Target implementation of the RC option is to disable error detection of the data fields but continue normal error detection and recovery for errors occurring in the servo field. If a servo field failure occurs, normal DRP could result in considerable recovery action, including proceeding through all levels of DRP.
- **EER** an Enable Early Recovery bit, **must be set to zero**, indicating that the drive shall use an error recovery procedure that minimizes the risk of misdetection or miscorrection during the data transfer. Data shall not be fabricated.

- **PER** a Post Error bit, is set to one to indicate that the drive reports recovered errors.
- **DTE** (Data Terminate on Error) bit set to one specifies that data transfer will be halted when the first recovered error is encountered. PER must be set to one when DTE is set to one. DTE set to zero will cause data transfer to continue when recovered errors are encountered.
- **DCR** a Disable Correction bit, is set to one to indicate that Error Correction Code is not used for data error recovery. A DCR bit of zero indicates that ECC is applied to recover the data.
- **Read Retry Count** sets a limit on the amount of DRP passes in which the Target attempts to recover read errors. A value of zero disables all data recovery procedures. When AVERP bit (Mode Page 0 Byte 6 Bit 6) is zero, a value of non-zero in Read Retry Count enables all steps of DRP. When AVERP bit is one, the number in Read Retry Count sets the maximum retry count of DRP.
- **Write Retry Count** sets a limit on the amount of DRP passes in which the Target attempts to recover write errors. A value of zero disables all data recovery procedures..
- **Recovery Time Limit** indicates the period in 1 millisecond increments for the maximum recovery time of a single LBA. The value must be from 40 ms to 65535 ms (65.5 seconds). The granularity of the timer is 50 ms. If an LBA is not able to be recovered within the limit, a Check Condition will be returned. The Recovery Time Limit will not be applied yo Writes when WCE=1. A value of zero disables the timer.

The following summarizes valid modes of operation. If an illegal mode is set, the MODE SELECT command will complete successfully but the action of the drive when an error occurs is undefined.

PER DTE DCR TB DESCRIPTION

0 0 0 0 Retries and Error Correction are attempted. Recovered or corrected data (if any) or both are transferred with no *Check Condition* status at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is not transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

0 0 0 1 Retries and Error Correction are attempted. Recovered or corrected data (if any) or both are transferred with no *Check Condition* status at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

0 0 1 0 Retries are attempted but no error correction (ECC) is applied. Recovered data (if any) are transferred with no *Check Condition* status at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is not transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

0 0 1 1 Retries are attempted but no error correction (ECC) is applied. Recovered data (if any) are transferred with no *Check Condition* status at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

0 1 0 0 Illegal Request-DTE must be zero when PER is zero.

0 1 0 1 Illegal Request-DTE must be zero when PER is zero.

0 1 1 0 Illegal Request-DTE must be zero when PER is zero.

0 1 1 1 Illegal Request-DTE must be zero when PER is zero.

1 0 0 0 The highest level error is reported at the end of transfer. Retries and error correction are attempted. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors. The information byte in the sense data will contain the LBA of the last recovered error.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is not transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 0 0 1 The highest level error is reported at the end of transfer. Retries and error correction are attempted. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors. The information byte in the sense data will contain the LBA of the last recovered error.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 0 1 0 The highest level error is reported at the end of transfer. Retries are attempted but ECC is not applied. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer length is exhausted. Transferred data includes blocks containing recovered errors. The information byte in the sense data will contain the LBA of the last recovered error.

hard err Data transfer stops when an unrecoverable error is encountered. The unrecoverable block is not transferred to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 0 1 1 The highest level error is reported at the end of transfer. Retries and error correction are attempted. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer stops on the first soft error detected. The information in the sense data shall contain the LBA of the block in error.

hard err Data transfer stops on the unrecoverable error. The unrecoverable error block is not returned to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 1 0 0 The highest level error is reported at the end of transfer. Retries and error correction are attempted. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer stops on the first soft error detected. The information in the sense data shall contain the LBA of the block in error.

hard err Data transfer stops on the unrecoverable error. The unrecoverable error block is not returned to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 1 0 1 The highest level error is reported at the end of transfer. Retries and error correction are attempted. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer stops on the first soft error detected. The information in the sense data shall contain the LBA of the block in error.

hard err Data transfer stops on the unrecoverable error. The unrecoverable error block is returned to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 1 1 0 The highest level error is reported at the end of transfer. Retries are attempted but ECC is not applied. Recovered data are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer stops on the first soft error detected. The recovered error block is returned to the initiator. The information in the sense data shall contain the LBA of the block in error.

hard err Data transfer stops on the unrecoverable error. The unrecoverable error block is not returned to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

1 1 1 1 The highest level error is reported at the end of transfer. Retries are attempted but ECC in not applied. Recovered or corrected data (if any) or both are transferred with *Check Condition* status and *Recovered Error* sense key set at the end of the transfer.

no err The transfer length is exhausted.

soft err The transfer stops on the first soft error detected. The information in the sense data shall contain the LBA of the block in error.

hard err Data transfer stops on the unrecoverable error. The unrecoverable error block is returned to the Initiator. The drive then creates the *Check Condition* status with the appropriate sense key.

17.10.4 Mode Page 02 (Disconnect/Reconnect Parameters)

Table 73: Mode Page 02 (Disconnect/Reconnect Parameters)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th><th></th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th><th>Default</th></tr>
<tr><td>0</td><td>PS</td><td>0</td><td colspan="6">Page Code = 02h</td><td>82h</td></tr>
<tr><td>1</td><td colspan="8">Page Length = 0Eh</td><td>0Eh</td></tr>
<tr><td>2</td><td colspan="8">Read Buffer Full Ratio</td><td>00h</td></tr>
<tr><td>3</td><td colspan="8">Write Buffer Empty Ratio</td><td>00h</td></tr>
<tr><td>4-5</td><td colspan="8">(MSB) Bus Inactivity Limit = 0 (LSB)</td><td>00h</td></tr>
<tr><td>6-7</td><td colspan="8">(MSB) Disconnect Time Limit = 0 (LSB)</td><td>00h</td></tr>
<tr><td>8-9</td><td colspan="8">(MSB) Connect Time Limit = 0 (LSB)</td><td>00h</td></tr>
<tr><td>10-11</td><td colspan="8">(MSB) Maximum Burst Size (LSB)</td><td>00h</td></tr>
<tr><td>12</td><td>EMDP=0</td><td colspan="3">Fair Arbitration</td><td>DIMM</td><td colspan="3">DTDC</td><td></td></tr>
<tr><td>13-15</td><td colspan="8">Reserved = 0</td><td>00h</td></tr>
</table>

The disconnect/reconnect page provides the Initiator with the means to tune the performance of the SCSI bus.

An Initiator may use the IDENTIFY message to grant the drive the general privilege of disconnecting. (Disconnect requests may still be selectively rejected by the Initiator by issuing a MESSAGE REJECT.)

The drive uses the disconnect/reconnect parameters to control recondition during READ (operation code 08h and 28h) and WRITE (0Ah, 2Ah and 2E).

- **Read Buffer Full Ratio** is the numerator of a fraction whose denominator is 256. The fraction indicates how full the drive data buffer should be before attempting to reconnect to the SCSI bus. If the ratio is set to 0h, the target will calculate and use an optimal ratio based on the negotiated transfer rate.
- **Write Buffer Empty Ratio** is the numerator of a fraction whose denominator is 256. The fraction indicates how empty the drive data buffer should be before attempting to reconnect to the SCSI bus. If the ratio is set to 0h, the target will calculate and use an optimal ratio based on the negotiated transfer rate.
- **Fair Arbitration** set to 000b indicates that the drive will not use arbitration fairness during normal arbitration. Fair Arbitration set to nonzero value indicates that the drive will use arbitration fairness during normal arbitration. Regardless of the value of Fair Arbitration, the drive will use arbitration fairness during QAS.
- **DIMM** (Disconnect Immediate) set to one indicates that the drive is required to disconnect after receiving a command and prior to starting a data phase. A DIMM bit of zero indicates that the drive may transfer data for a command immediately after receiving it, without disconnecting. Whether or not the drive does so depends upon the workload and the settings of the other parameters in this mode page. **Note:** Priority commands do not disconnect from the SCSI bus.

- **DTDC:** (Data Transfer Disconnect Control) defines further restrictions for when a disconnect is permitted.
  - A value of 000b indicates that DTDC is not used by the drive and disconnects are controlled by the other fields in this page.
  - A value of 001b indicates that the drive shall not attempt to disconnect when the data transfer of a command has started until all requested data has been transferred.
  - A value of 011b indicates that the drive shall not attempt to disconnect once the data transfer of a command has started, until the command is complete.
  - All other values are reserved.
  - When DTDC is non-zero, the WCE bit in Mode Page 8 is ignored.

NOTE: If DTDC is nonzero and the maximum burst size is nonzero, a CHECK CONDITION status will be returned. The sense key shall be set to ILLEGAL REQUEST and the additional sense code set to ILLEGAL FIELD IN PARAMETER LIST.

- **Maximum Burst Size** is the maximum amount of data that the Target transfers during a data phase before disconnecting if the Initiator has granted the disconnect privilege. This value is expressed in increments of single block size (for example, a value of 0001h means 512 bytes, 0002h means 1024 bytes when the block size is 512 bytes). Disconnections attempted by the Target are on block boundaries only. For the case when (Maximum Burst Size x Block Size) is less than the Block Length, the Target will transfer 1 block of data before attempting to disconnect. Value of 0000h indicates there is no limit on the amount of data transferred per connection. Regardless of the value in Maximum Burst Size the Target disconnects prior to completion of the data phase if the internal data buffer segment becomes empty during a READ command or full during a WRITE command.

Both the Read Buffer Full Ratio and the Write Buffer Empty Ratio pertain to the current active notch. For each active notch as defined in page 0Ch there are separate Read Buffer Full Ratios and Write Buffer Empty Ratios. When the active notch is set to zero, the values are applied in mode page 0Ch across all notches.

17.10.5 Mode Page 03 (Format Device Parameters)

Table 74: Mode Page 03 (Format Device Parameters)

Byte	Bit								
	7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 03h						03h
1	Page Length = 16h								16h
2-3	(MSB)	Tracks per Zone							xxh
								(LSB)	xxh
4-5	(MSB)	Alternate Sectors per Zone = 0							00h
								(LSB)	00h
6-7	(MSB)	Alternate Tracks per Zone = 0							00h
								(LSB)	00h
8-9	(MSB)	Alternate Tracks per Logical Unit = 0							00h
								(LSB)	00h
10-11	(MSB)	Sectors Per Track							xxh
								(LSB)	xxh
12-13	(MSB)	Data Bytes per Physical Sector							xxh
								(LSB)	xxh
14-15	(MSB)	Interleave = 0001h or 0000h							00h
								(LSB)	01h
16-17	(MSB)	Track Skew Factor							xxh
								(LSB)	xxh
18-19	(MSB)	Cylinder Skew Factor							xxh
								(LSB)	xxh
20	SSEC	HSEC	RMB	SURF	RESERVED = 0				40h
21-23	Reserved = 0								00h

The format device page contains parameters that specify the medium format. This page contains no changeable parameters.

- **Tracks per Zone** specifies the number of tracks within the zone. This field is a function of the active notch.
- **Sectors per Track** specifies the number of physical sectors within each track. This field is a function of the active notch.
- **Data Bytes per Physical Sector** specifies the number of user data bytes per physical sector. The value depends upon the current formatted Block Length.
- **Interleave** value of 1 or 0 is valid. However, the drive will ignore this.
- **Track Skew Factor** indicates the number of physical sectors between the last block of one track and the first block on the next sequential track of the same cylinder. This field is a function of the active notch.
- **Cylinder Skew Factor** indicates the number of physical sectors between the last block of one cylinder and the first block on the next sequential cylinder. This field is a function of the active notch.
- **SSEC** = Zero indicates that the drive does not support soft sector formatting.
- **HSEC** = One indicates that the drive supports hard sector formatting.
- **RMB** = Zero indicates that the media does not support removable Fixed Disk.
- **SURF** = Zero indicates that progressive addresses are assigned to all logical blocks in a cylinder prior to allocating addresses within the next cylinder.

17.10.6 Mode Page 04 (Rigid Disk Drive Geometry Parameters)

Table 75: Mode Page 04 (Rigid Disk Drive Geometry Parameters)

Byte	Bit 7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 04h						04h
1	Page Length = 16h								16h
2-4	(MSB) Number of Cylinders (LSB)								xxh xxh
5	Number of heads								xxh
6-8	(MSB) Starting Cylinder - Write Precompensation = 0 (LSB)								00h 00h
9-11	(MSB) Starting Cylinder - Reduced Write Current = 0 LSB)								00h 00h
12-13	(MSB) Drive Step Rate = 0 (Not used) (LSB)								00h 00h
14-16	(MSB) Landing Zone Cylinder = 0 (Not used) (LSB)								00h 00h
17	RESERVED = 0						RPL = 0		00h
18	Rotational Offset = 0 (Not used)								00h
19	RESERVED = 0								00h
20-21	(MSB) Medium Rotation Rate in RPM (LSB)								3Ah 98h
22-23	Reserved = 0								00h

The rigid disk drive geometric page specifies various parameters for the drive.

- **RPL** = 0, Indicates that the drive does not support spindle synchronization.

17.10.7 Mode Page 07 (Verify Error Recovery Parameters)

Table 76: Mode Page 07 (Verify Error Recovery Parameters)

Byte	Bit								
	7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 07h						87h
1	Page Length = 0Ah								0Ah
2	Reserved = 0				EER=0	PER	DTE	DCR	00h
3	Verify Retry Count								01h
4	Obsolete =0								00h
5 - 9	Reserved = 0								00h
10-11	(MSB) Verify Recovery Time Limit (LSB)								00h

The Verify recovery parameters are used by the Target when recovering from and reporting errors associated with the verification of the Initiator's Data for the following commands:

- VERIFY
- WRITE AND VERIFY - the verify portion of the command only.
- **EER**. This bit is 0 since the Target does not support early recovery.
- **PER**. See below for description of bit values.
- **DTE**. (Data Terminate on Error) bit set to one specifies that data transfer will be halted when the first recovered error is encountered. PER must be set to one when DTE is set to one. DTE set to zero will cause data transfer to continue when recovered errors are encountered.
- **DCR**. See below for description of bit values.

The PER, DTE, and DCR bit settings in mode page 7 override those of mode page 1 during VERIFY and the Verify portion of WRITE AND VERIFY. The following combinations of PER and DCR are valid:

PER	DCR	DESCRIPTION
0	**0**	Soft errors are not reported. ECC is applied to recover the data.
1	**0**	Soft errors are reported. ECC is applied to recover the data.
0	**1**	Soft errors are not reported. ECC is not used to recover the data.
1	**1**	Soft errors are reported. ECC is not used to recover the data.

- **Verify Retry Count** sets a limit on the amount of verify recovery procedure (VRP) passes the Target attempts when recovering verify errors. The Verify Retry Count of one causes the Target to attempt up to one VRP pass per command when a medium error occurs during a verify operation. Only values of 0h and 01h are valid. The value of 0h disables all recovery.
- **Verify Recovery Time Limit** indicates the period in 1 millisecond increments for the maximum recovery time of a single LBA during the verify operation. The value must be from 40 ms to 65535 ms (65.5 seconds). The granularity of the timer is 50 ms. If an LBA is not able to be recovered within the limit, a Check Condition will be returned.

17.10.8 Mode Page 08 (Caching Parameters)

Table 77: Page 08 (Caching Parameters)

Byte	Bit 7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 08h						88h
1	Page Length = 12h								12h
2	IC	ABPF	CAP	DISC	SIZE	WCE	MF	RCD	04h
3	Demand Read Retention Priority				Write Retention Priority				00h
4-5	(MSB) Disable Pre-fetch Transfer Length (LSB)								FFh
6-7	(MSB) Minimum Pre-fetch (LSB)								00h
8-9	(MSB) Maximum Pre-fetch (LSB)								FFh
10-11	Maximum Pre-fetch Ceiling								FFh
12	FSW	LBCSS	DRA	Reserved = 0					00h
13	Number of Cache Segments								08h
14-15	(MSB) Cache Segment Size (LSB)								00h
16	Reserved = 0								00h
17-19	(MSB) Non Cache Segment Size (LSB)								00h

The caching parameters page defines parameters that affect the use of the cache.

- **IC** (Initiator Control) bit of one specifies that the drive will honor the following parameters to control cache segmentation and pre-fetch: SIZE, FSW, LBCSS, Number of Cache Segments, Cache Segment Size. The drive will never pre-fetch less data than specified by ABPF, MF, Demand Read Retention Priority, Write Retention Priority, Disable Pre-fetch Transfer Length, Minimum Pre-fetch, Maximum Pre-fetch, and Maximum Pre-fetch Ceiling, but may pre-fetch more based on internal cache algorithms. When the IC bit is set to zero, all the parameters listed above are ignored, and an internal caching algorithm is used.
- **ABPF** (Abort Pre-fetch) bit of one, with DRA set to zero, causes the drive to abort the pre-fetch upon receipt of a new command. ABPF set to one takes precedence over Minimum Pre-fetch. When ABPF is zero, with DRA set to zero, the termination of any active pre-fetch is dependant upon the other parameters in this page.
- **CAP** (Caching Analysis Permitted) is not supported and is ignored. The IC bit can be used to enable or disable adaptive caching.
- **DISC** (Discontinuity) is not supported and is ignored. Pre-fetch operations will continue across cylinders, within the limits of other caching parameters on this page.
- **SIZE** (Size Enable) bit when set to one indicates that the Cache Segment Size is to be used to control caching segmentation. When SIZE is set to zero, the Initiator requests that the Number of Cache Segments is to be used to control caching segmentation.
- **WCE** (Write Cache Enable) bit when set at zero indicates that the drive must issue *Good* status for WRITE (6) or WRITE (10) command only after successfully writing the data to the media. When the WCE bit is set to one, the drive may issue *Good* status for a WRITE (6) or WRITE (10) command after successfully receiving the data but before writing it to the media. When WCE = 1, the drive operates as if AWRE = 1.

Note: When WCE = 1, a SYNCHRONIZE CACHE command must be done to ensure data are written to the media before powering down the Target.

- **MF** (Multiplication Factor) bit determines how the Maximum and Minimum Pre-fetch parameters are interpreted. If this bit is set to zero, the parameters are used as is. If the bit is set to one, the parameters are multiplied by the number of blocks requested in the Read Command.
- **RCD** (Read Cache Disable) bit set at zero indicates that the drive may return some or all of the data requested by a READ (6) or READ (10) command by accessing the data buffer, not the media. An RCD bit set at one indicates that the Target does not return any of the data requested by a READ (6) or READ (10) command by accessing the data buffer. All of the data requested is read from the media instead.
- **Demand Read Retention Priority** sets the retention priority of data requested on a Read command. It may be set as defined below:

Value	Definition
0h	Do not distinguish between requested data and other data
1h	Replace requested data before other data.
Fh	Replace other data before requested data.

where the value is the Demand Read Retention Priority or Write Retention Priority. Requested data is the blocks specified in the Read or Write command. Other data is data in the cache from any other operation such as pre-fetch, read-ahead, etc.

If the Read Retention Priority is not set to Fh or if the DPO bit on the Read command is set to one, the requested data is overwritten by read-ahead data.

If the DPO bit is set to zero and the Read Retention Priority is set to Fh, the requested data is not overwritten with read-ahead data. If the requested transfer is larger than the segment, the requested data is overwritten with more requested data and there is no read-ahead

- **Write Retention Priority** sets the retention priority of data provided on a Write command. See the definition of Demand Read Retention Priority above for more details.
- **Disable Pre-fetch Transfer Length** is used to prevent read-ahead after Read commands that are longer than the specified number of blocks. If this parameter is set to zero, a read-ahead is not performed.
- **Minimum Pre-fetch** specifies the minimum number of LBAs that the drive should
- after each READ command. A value of zero indicates that read ahead should be terminated immediately after a new command arrives, except when the new command is on the current head and track.
- **Maximum Pre-fetch** specifies the maximum number of LBAs to read ahead after a Read command.
- **Maximum Pre-fetch Ceiling** specifies the maximum number of blocks the drive should attempt to read ahead. This field is ignored.
- **FSW** (Force Sequential Write) is not supported and is ignored. All logical blocks will be written in sequential order.
- **LBCSS** (Logical Block Cache Segment Size) bit when set to one indicates that the Cache Segment Size field units shall be interpreted as logical blocks. When it is set to zero, the Cache Segment Size field units shall be interpreted as bytes.
- **DRA** (Disable Read Ahead) bit when set to one requests that the Target not read into the buffer any logical block beyond the addressed logical block(s). When it is set at zero, the Target may continue to read logical blocks into the buffer beyond the addressed logical block(s).
- **Number of Cache Segments** field is used to select the number of data buffer cache segments. This parameter is valid only when the SIZE bit is set at zero. It is ignored when SIZE is set at one.
- **Cache Segment Size** field indicates the requested segment size in Bytes or Blocks, depending upon the value of the LBCSS bit. The Cache Segment Size field is valid only when the SIZE bit is one. It is ignored when SIZE is set at zero.
- **Non Cache Segment Size** is not supported and is ignored.

17.10.9 Mode Page 0A (Control Mode Page Parameters)

Table 78: Page 0A (Control Mode Page Parameters)

Byte	Bit								Default
	7	6	5	4	3	2	1	0	
0	PS	0	Page Code = 0Ah						8Ah
1	Page Length = 0Ah								0Ah
2	TST=0			RSVD=0		D_Sense=0	GLTSD=0	RLEC=0	00h
3	Queue Algorithm Modifier				Rsvd=0	QErr		DQue	00h
4	RSVD=0	RAC=0	UA_INTLCK_CTRL=0		SWP=0	Obsolete			00h
5	ATO	TAS=0	Reserved=0						00h
6-7	Obsolete=0								00h
8-9	(MSB) Busy Timeout Period (LSB)								00h
10-11	(MSB) Extended Self-test Routine Completion Time (LSB)								XXh

Following are parameter options for Page 0A.

- **Queue algorithm modifier** specifies restrictions on the algorithm used for reordering commands that are tagged with the SIMPLE message.
  0h : Restricted reordering. The Target shall reorder the actual execution sequence of the queued commands from each Initiator such that data integrity is maintained for that Initiator.

  1h : Unrestricted reordering allowed. The Target may reorder the actual execution sequence of the queued commands in any manner it selects. Any data integrity exposures related to command sequence order are explicitly handled by the Initiator through the selection of appropriate commands and queue tag messages.

  2h-7h : RESERVED.

  8 : Command reordering is disabled

  9-Fh : RESERVED
- **QErr** (Queue Error Management) The queue error management (QERR) field specifies how the device server shall handle blocked tasks when another task receives a *Check Condition* status.

QERR value	Description
00b	**Specifies that all tasks from all Initiators are blocked from execution when a Contintent Alligience (CA condition) is pending. Those blocked tasks are allowed to resume execution in a normal fashion after the CA condition is cleared.**
01b	**Specifies that all tasks from all Initiators are aborted when the Target returns *Check Condition* status. A unit attention condition will be generated for each Initiator that had commands in the queue except for the Initiator that received the *Check Condition* status. The sense key will be set to *Unit Attention* and the additional sense code will be set to *Commands Cleared by Another Initiator.***

10b	**Reserved**
11b	**Blocked tasks in the task set belonging to the Initiator to which a *Check Condition* status is sent shall be aborted when the status is sent.**

- **DQue** (Disable Queuing) bit set at zero specifies that tagged queuing shall be enabled if the Target supports tagged queuing. A DQue bit set at one specifies that tagged queuing shall be disabled. Command queuing is always enabled on the drive, therefore this bit is ignored.
- **ATO** (Application Tag Owner) bit set to one specifies that the contents of the Logical Block Application Tag field in the protection information, if any, shall not be modified by the drive. An ATO bit set to zero specifies that the contents of the Logical Block Application Tag field in the protection information, if any, may be modified by the drive. If the ATO bit is set to zero, the drive will ignore the contents of the Logical Block Application Tag field in the protection information.
- **Busy Timeout Period** is not supported and is ignored.
- **Extended Self-test Routine Completion Time** is an advisory parameter that an Initiator may use to determine the time in seconds that the Target requires to complete self-test routine when the Target is not interrupted by an Initiator and no errors occur during execution of the self-test routine.

17.10.10 Control Extension Subpage

Table 79: Control Extension Subpage

CByte	Bit								Default
	7	6	5	4	3	2	1	0	
0	PS	SPF=1	Page Code = 0Ah						5Ah
1	Subpage Code = 1								01h
2-3	Page Length = 001Ch								001Ch
4	Reserved = 0					TCMOS	SCSIP	IALUAE	00h
5	Reserved = 0			Initial Priority					00h
6-31	Reserved = 0								00h

No fields in the Control Extension subpage are currently changeable. The page is supported for compatibility only.

17.10.11 Mode Page 0C (Notch Parameters)

Table 80: **Page 0C (Notch Parameters)**

Byte	Bit 7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 0Ch						8Ch
1	Page Length = 16h								16h
2	ND=1	LPN=0	Reserved = 0						80h
3	Reserved = 0								00h
4-5	(MSB) Maximum Number of Notches (LSB)								XXh XXh
6-7	(MSB) Active Notch (LSB)								00h 00h
8-11	(MSB) Starting Boundary (LSB)								XXh XXh
12-15	(MSB) Ending Boundary (LSB)								XXh XXh
16-23	(MSB) Pages Notched (LSB)								0000h 0000h 0000h 100Ch

The notch page contains parameters for direct-access devices that implement a variable number of blocks per cylinder. Each section of the logical unit with a different number of blocks per cylinder is referred as a notch. The only field that is changeable is the **Active Notch** field.

- **ND** = One meaning that this device is a notched drive.
- **LPN** = Zero meaning that the notches are based upon physical parameters of the drive (cylinder #), not logical parameters.
- **Maximum Number of Notches** is the number of notches the drive can support. This value is drive model dependent.
- **Active Notch** indicates to which notch subsequent MODE SELECT/SENSE command parameters pertain. A value of 0 is used for parameter values which apply to all notches. Values from 1 to the maximum value depending on the model specify the notch number, where notch 1 is the outermost notch. Following mode parameters are based on the current active notch:

•**Mode Page 2**
- Read Buffer Full Ratio
- Write Buffer Empty Ratio

•**Mode Page 3**
- Alternate Sector per Zone
- Alternate Track per Zone

- Alternate Track per Logical Unit
- Sector per Track
- Track Skew Factor
- Cylinder Skew Factor

- **Starting Boundary** contains the first physical location of the active notch. The first three bytes are the cylinder number and the last byte is the head. The value sent in this field is ignored.
- **Ending Boundary** contains the last physical location of the active notch. The first three bytes are the cylinder number and the last byte is the head. The value sent in this field is ignored.
- **Pages Notched** is a bit map of the mode page codes that indicates which pages contain parameters that may be different for different notches. The most significant bit of this field corresponds to page code 3Fh and the least significant bit corresponds to page code 00h. If a bit is one, then the corresponding mode page contains parameters that may be different for different notches. If a bit is zero, then the corresponding mode page contains parameters that are constant for all notches.

17.10.12 Mode page 19 (Port Control Parameters)

17.10.12.1 Short Format of Port Control Page

Table 81: Short (Port Control Parameters) Short Format

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th><th rowspan="2">Default</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td>PS</td><td>SPF=0</td><td colspan="6">Page Code = 19h</td><td>99h</td></tr>
<tr><td>1</td><td colspan="8">Page Length = 06h</td><td>06h</td></tr>
<tr><td>2</td><td colspan="4">RSVD</td><td colspan="4">Protocol Indentifier = 1</td><td>01h</td></tr>
<tr><td>3</td><td colspan="8">RSVD</td><td>00h</td></tr>
<tr><td>4
|
5</td><td colspan="8">(MSB)
Synchronous transfer timeout
(LSB)</td><td>00h
|
00h</td></tr>
<tr><td>6</td><td colspan="8">RSVD</td><td>00h</td></tr>
<tr><td>7</td><td colspan="8">RSVD</td><td>00h</td></tr>
</table>

- **Protocol Identifier** is set to 1h to indicate SPI SCSI devices.

- Synchronous Transfer Timeout indicates the maximum amount of time that the drive will wait before generating an error by entering an unexpected Bus Free phase. The unit of time is 1 msec. The drive will only enter a Bus Free phase if one of the following events causes the timer, once started, to not reset or reload before expiring:

  - If there is a REQ transition when there are no outstanding REQs waiting for an ACK, then load and start the timer.
  - If there is a REQ transition when there are any outstanding REQs waiting for an ACK, then there is no effect on the timer.
  - If there is an ACK transition when there are outstanding REQs waiting for an ACK, then load and start the timer.
  - If after an ACK transition there are no outstanding REQs waiting for an ACK, then stop the timer.

A Synchronous Transfer Timeout of zero disables the function. A value of FFFFh indicates an unlimited period.

17.10.12.2 Long Format of Port Control Page

Table 82: Page 19 (Port Control Parameters) Long Format

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th><th rowspan="2">Default</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td>PS</td><td>SPF=1</td><td colspan="6">Page Code = 19h</td><td>D9h</td></tr>
<tr><td>1</td><td colspan="8">Subpage Code</td><td></td></tr>
<tr><td>2
|
3</td><td colspan="8">(MSB)
Page Length (n-3)
(LSB)</td><td>00h
|
00h</td></tr>
<tr><td>4</td><td colspan="8">RSVD</td><td>00h</td></tr>
<tr><td>5</td><td colspan="4">RSVD</td><td colspan="4">Protocol Identifier = 1h</td><td>01h</td></tr>
<tr><td>6</td><td colspan="8" rowspan="2">Protocol Specific Mode Parameters</td><td></td></tr>
<tr><td>n</td><td></td></tr>
</table>

The drive maintain an independent set of port control mode page parameters for each SCSI initiator port.

- **Subpage Code** indicates which subpage is being accessed. The drive support the following subpage codes. If the Subpage Code is not supported or is not zero, the drive returns a CHECK CONDITION status, the sense key is set to ILLEGAL REQUEST and the additional sense code set to ILLEGAL FIELD IN PARAMETER LIST..

  - 01h: Margin Control Subpage
  - 02h: Saved Training Configuration Values Subpage
  - 03h: Negotiated Settings Subpage
  - 04h: Report transfer Capabilities Subpage
  - FFh: All supported subpages.
- **Page Length** specifies the length in bytes of the subpage parameters after the Page Length.

- **Protocol Identifier** has a value of 1h to indicate SPI SCSI devices.

17.10.12.3 Margin Control Subpage

Table 83: Margin Control Subpage

Byte	Bit 7	6	5	4	3	2	1	0
0	RSVD							
1	Driver Strength				RSVD			
2	Driver Asymmetry				Driver Precomenstation			
3	Driver Slew Rate				RSVD			
4 | 6	RSVD							
7	Vendor Specific							
8 | 15	RSVD							

The margin control subpage contains parameters that set and report margin control values for usage between the initiator and the drive on subsequent synchronous and paced transfers. MODE SENSE command returns the current settings for the initiator.

- **Driver Strength** indicates the relative amount of driver source current used by the driver. The Driver Strength field affects both the strong and weak drivers. A larger value indicates more driver source current.

- **Driver Precompensation** indicates the relative difference between the weak driver and the strong driver amplitudes when precompensation is enabled. A larger value indicates a larger difference between the weak and strong amplitudes.

- **Driver Asymmetry** indicates the relative difference between the amplitudes of asserted and negated signals launched from the driver. A larger value indicates a relatively stronger asserted signal compared to the negated signal.

- **Driver Slew Rate** indicates the relative difference between the assertion and negation magnitudes divided by the rise or fall time. A larger value indicates a faster slew rate.

- **Vendor Specific** is reserved, and should be zero.

The default value of each margin control field should be 0000b.
The margin control fields indicate absolute conditions centered around their default values. The maximum supported setting for each field is 0111b and the minimum supported setting for each field is 1000b.

17.10.12.4 Saved Training Configuration Values Subpage

Table 84: Saved Training Configuration Subpage

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	RSVD							
4-7	DB (0) Value							
64-67	DB (15) Value							
68-71	P_CRCA Value							
72-75	P1 Value							
76-79	BSY Value							
80-83	SEL Value							
84-87	RST Value							
88-91	REQ Value							
92-95	ACK Value							
96-99	ATN Value							
100-103	C / D Value							
104-107	I / O Value							
108-111	MSG Value							
112-227	RSVD							

The saved training configuration values subpage is used to report the drive's saved training configuration values. These vendor specific values are maintained by the drive when the retain training information option is enabled.

17.10.12.5 Negotiated Setting Subpage

Table 85: Negotiated Settings Subpage

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Transfer Period Factor							
1	RSVD							
2	REQ / ACK Offset							
3	Transfer Width Exponent							
4	RSVD	Protocol Options Bits						
5	RSVD				Transceiver Mode		1	2
6	RSVD							
7	RSVD							

1: Sent PCOMP_EN
2: Received PCOMP_EN
The negotiated settings subpage is used to report the negotiated settings of the drive for the current I_T nexus.

- **Transfer Period Factor** indicates the negotiated Transfer Period Factor.
- **REQ/ACK Offset** indicates the negotiated REQ/ACK Offset.
- **Transfer Width Exponent** indicates the negotiated Transfer Width Exponent.
- **Protocol Options Bits** contain the negotiated protocol options except PCOMP_EN.
- **Received PCOMP_EN** contains the value of PCOMP_EN received by the drive.
- **Send PCOMP_EN** contains the value of PCOMP_EN sent by the drive.
- **Transceiver Mode** specifies the current bus mode of the drive as defined below.
  - 00b: Not used
  - 01b: Single Ended
  - 10b: Low Voltage Differential
  - 11b: High Voltage Differential

17.10.12.6 Report Transfer Capabilities Subpage

Table 86: Report Transfer Capabilities Subpage

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Minimum Transfer Period Factor							
1	RSVD							
2	Maximum REQ / ACK Offset							
3	Maximum Transfer Width Exponent							
4	Protocol Options Bits							
5-7	RSVD							
6	RSVD							
7	RSVD							

The report transfer capabilities subpage is used to report the transfer capabilities of the drive. The values in this subpage are not changeable via a MODE SELECT command.

- **Minimum Transfer Period** Factor is set to the smallest value of the Transfer Period Factor supported by the drive which is 08h.
- **Maximum REQ/ACK Offset** is set to the largest value of the REQ/ACK Offset supported by the drive which is 127.
- **Maximum Transfer Width Exponent** is set to the largest value of the Transfer Width Exponent supported by the drive which is 01h.
- **Protocol Options Bits Supported** indicates the protocol options supported by the drive which is FFh.

17.10.13 Mode Page 1A (Power Control)

Table 87: Page 1A (Power Control)

Byte	Bit								
	7	6	5	4	3	2	1	0	Default
0	PS	0	Page Code = 1Ah						9Ah
1	Page Length = 0Ah								0Ah
2	Reserved = 00h								00h
3	Reserved = 0						Idle	Standby	00h
4-7	(MSB) Idle Condition Timer = 0 (LSB)								00h
8-11	(MSB) Standby Condition Timer (LSB)								00h

- The value for the **Idle Condition Timer** and the corresponding **Idle** control bit is accepted. Despite this, the drive does not alter the internal power saving algorithms based upon this value.
- **Standby** bit of one indicates that the Target shall use the **Standby Condition Timer** to determine the length of inactivity time to wait before entering the Standby condition. A standby bit of zero indicates that the Target shall not enter the Standby condition.
- **Standby Condition Timer** field indicates the inactivity time in 100 millisecond increments that the Target shall wait before entering the Standby condition. The minimum allowable inactivity time is 60 minutes. Any value less than this is accepted, but will automatically default to 60 minutes.

17.10.14 Mode Page 1C (Informational Exceptions Control)

Table 88: Page 1C (Informational Exceptions Control)

Byte	BIT								Default
	7	6	5	4	3	2	1	0	
0	PS	0	Page Code = 1Ch						9Ch
1	Page Length = 0Ah								0Ah
2	PERF	RSVD	EBF	EWASC	DEXCPT	TEST	EBACK-ERR	LOGERR	10h
3	Reserved = 0				Method of Reporting				00h
4-7	(MSB) Interval Timer (LSB)								00h 00h 00h 00h
8-11	(MSB) Report Count (LSB)								00h

- **PERF** (Performance) bit is not supported and is ignored. Informational Exception operations will not cause performance delays.
- **EBF** (Enable Background Function) bit is not supported and is ignored. Background functions are always enabled.
- **EWASC** (Enable Warning ASC) bit of zero indicates that Temperature Warnings will not be reported. An EWASC bit of one allows Temperatuer Warnings to be reported, if the temperature inside the disk enclosure exceeds the threshold set in Mode Page 00h. The Method of Reporting field controls the reporting method. EWASC is independent of DEXCPT.
- **DEXCPT** (Disable Exception Control) bit of zero indicates information exception operations are enabled. The reporting of information exception conditions when the DEXCPT bit is set to zero is determined from the Method of Reporting field. A DEXCPT bit of one indicates the Target disabled all information exception operations.
- **TEST** bit of one instructs the drive to generate false drive notifications at the next interval time, (as determined by the INTERVAL TIMER field), if the DEXCPT is zero. The Method of Reporting and Report Count would apply. The false drive failure is reported as sense qualifier 5DFFh. The TEST bit of zero instructs the drive to stop generating any false drive notifications.
- **Enable Background Error (EBACKERR)** bit of zero disables reporting of background self-test errors and background scan errors via Information Exceptions Control. An EBACKERR bit of one enables reporting of these background errors. The method of reporting these errors is determined from the MRIE field.
- **LOGERR** (Log Errors) is not used and ignored internally by the Target.

- **Method of Reporting** Informational Exceptions indicates the methods used by the Target to report informational exception conditions.

Code	Description
0h	**No reporting of informational exception condition:** This method instructs the Target to not report informational exception condition.
1h	**Asynchronous event reporting:** Not supported.
2h	**Generate unit attention:** This method instructs the Target to report informational exception conditions by returning a *Check Condition* status on any command. The sense key is set to *Unit Attention* and the additional sense code indicates the cause of the informational exception condition. The command that has the *Check Condition* is not executed before the informational exception condition is reported.
3h	**Conditionally generate recovered error:** This method instructs the Target to report informational exception conditions, dependent on the value of the PER bit of the error recovery parameters mode page, by returning a *Check Condition* status on any command. The sense key is set to *Recovered Error* and the additional sense code indicates the cause of the informational exception condition. The command that has the *Check Condition* completes without error before any informational exception condition is reported.
4h	**Unconditionally generate recovered error:** This method instructs the Target to report informational exception conditions, regardless of the value of the PER bit of the error recovery parameters mode page, by returning a *Check Condition* status on any command. The sense key is set to *Recovered Error* and the additional sense code indicates the cause of the informational exception condition. The command that has the *Check Condition* completes without error before any informational exception condition is reported.
5h	**Generate no sense:** This method instructs the Target to report informational exception conditions by returning a *Check Condition* status on any command. The sense key is set to *No Sense* and the additional sense code indicates the cause of the informational exception condition. The command that has the *Check Condition* completes without error before any informational exception condition is reported.
6h	**Only report informational exception condition on request:** This method instructs the Target to preserve the informational exception(s) information. To find out about information exception conditions the Application Client polls the Target by issuing an unsolicited *Request Sense* command. The sense key is set to *No Sense* and the additional sense code indicates the cause of the informational exception condition.
7h-Fh	Reserved.

- **Interval Timer** field indicates the period in 100 millisecond increments for reporting that an informational exception condition has occurred. The target shall not report informational exception conditions more frequently than the time specified by the Interval Timer field and as soon as possible after the time interval has elapsed. After the informational exception condition has been reported the interval timer is restarted. A value of zero or 0xFFFFFFFF in the Interval Timer field indicates that the target only reports the informational exception condition one time and will override the value set in the Report Count field.
- **Report Count** field indicates the number of times the Target reports an informational exception condition. The Report Count of ZERO indicates no limits on the number of times the Target reports an informational exception condition.

17.10.14.1 Background Control (Subpage 01h)

Table 89: Background Control (Subpage 01h)

Byte	BIT								
	7	6	5	4	3	2	1	0	Default
0	PS	SPF=1	Page Code = 1Ch						DCh
1	Subpage Code = 01h								01h
2-3	Page Length = 0Ch								000Ch
4	Reserved = 0					S_L_Full	LOWIR	EN_BMS	01h
5	Reserved = 0							EN_PS	00h
6-7	Background Medium Scan Interval Time								00A8h
8-9	Background Pre-Scan Time Limit								0000h
10-11	Minumum Idle Time Before Background Scan								0000h
12-13	Maximum Time To Suspend Background Scan								0000h
14-15	Reserved = 0								0000h

- **Suspend On Log Full (S_L_FULL)** bit set to zero allows background scans to continue if the results log (Log Sense Page 15h) is full. S_L_FULL bit set to one will cause background scans to suspend when the log is full.
- **Log Only When Intervention Required (LOWIR)** bit set to zero allows logging of all medium errors in the results log (Log Sense Page 15h). When the LOWIR bit is set to one, only unrecovered medium errors will be logged.
- **EN_BMS (Enable Background Medium Scan)** bit set to zero specifies that the background medium scan is disabled. EN_BMS bit set to one specifies that background medium scan operations are enabled. If a background medium scan is in progress when the EN_BMS bit is changed from one to zero, then the medium scan shall be suspended until the EN_BMS bit is set to one, at which time the medium scan shall resume from the suspended location.
- **EN_PS (Enable Pre-Scan)** bit set to zero specifies that the pre-scan is disabled. If a pre-scan operation is in progress when EN_PS is changed from a one to a zero, then pre-scan is halted. An EN_PS bit set to one specifies that a pre-scan operation is started after the next power-on cycle. Once this pre-scan has completed, another pre-scan shall not occur unless the EN_PS bit is set to zero, then set to one, and another power-on cycle occurs.
- **Background Medium Scan Interval Time** specifies the minimum time, in hours, between the end of one background medium scan operation and the start of the next background medium scan operation. The BMS Interval Time shall occur before the initial background medium scan operation is started.
- **Background Pre-Scan Time Limit** specifies the maximum time, in hours, for a pre-scan operation to complete. If the pre-scan operation does not complete within the specified time, then it is halted. A value of zero specifies an unlimited time limit.
- **Minimum Idle Time Before Background Scan** specifies the minimum time, in milliseconds, that the drive must be idle before resuming a background media scan or pre-scan. A value of zero will be treated as the default value of 1.0 second. Any value less than 100 milliseconds will be treated as 100 milliseconds. The internal timer granularity is 50 milliseconds.

Maximum Time To Suspend Background Scan specifies the maximum time, in milliseconds, that the drive should delay before processing a new command while background scanning is in progress.

17.11 MODE SENSE (5A)

Table 90: Mode Sense (5A)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 5Ah</td></tr>
<tr><td>1</td><td colspan="4">Reserved = 0</td><td>DBD</td><td colspan="3">Reserved = 0</td></tr>
<tr><td>2.</td><td colspan="2">PCF</td><td colspan="6">Page Code</td></tr>
<tr><td>3-6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">(MSB) Allocation Length (LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The MODE SENSE (5A) command provides a means for the drive to report various device parameters to the Initiator. See the MODE SENSE (1A) command for a description of the fields in this command.

17.12 PERSISTENT RESERVE IN (5E)

Table 91: Persistent Reserve In (5E)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 5Eh							
1	Reserved = 0			Service Action				
2-6	Reserved = 0							
7-8	(MSB) Allocation Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The PERSISTENT RESERVE IN command is used to obtain information about persistent reservations and reservation keys that are active within the controller. This command is used in conjunction with the PERSISTENT RESERVE OUT command “PERSISTENT RESERVE OUT (5F)” on page 142.

The **Allocation Length** indicates how much space has been allocated for the returned parameter list. If the length is not sufficient to contain all parameter data, the first portion of the data will be returned. If the remainder of the data is required, the initiator should send a new PERSISTENT RESERVE IN command and an Allocation Length large enough to contain all data.

17.12.1 Service Action

The following service action codes are implemented. If a reserved service action code is specified, the drive returns a **Check Condition** status. The sense key is set to *Illegal Request* and the additional sense data is set to *Invalid Field in CDB*.

Table 92: PERSISTENT RESERVE IN, Service Action Codes

Code	Name	Descriptions
00h	Read Keys	Reads all registered Reservation Keys
01h	Read Reservations	Reads all current persistent reservations
02h	Report Capabilities	Returns capability information
03h	Read Full Status	Reads complete information about all registrations and the persistent reservation, if any
04h-1Fh	Reserved	Reserved

17.12.2 Parameter data for Read Keys

Table 93: PERSISTENT RESERVE IN, parameter data for Read Keys

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	(MSB) Generation (LSB)							
4-7	(MSB) Additional length (n-7) (LSB)							
8-15	(MSB) First reservation key (LSB)							
	:							
(n-7) - n	(MSB) Last reservation key (LSB)							

Generation is a counter that increments when PERSISTENT RESERVE OUT command with "Register" or "Preempt and Clear" completes successfully. Generation is set to 0 as part of the power on reset process and hard reset process.

The **Generation** field contains a 32-bit counter that the Target shall increment every time a PERSISTENT RESERVE OUT command requests a Register, a Clear, a Preempt, or a Preempt and Abort service action. The counter shall not be incremented by a PERSISTENT RESERVE IN command, by a PERSISTENT RESERVE OUT command that performs a Reserve or Release service action, or by a PERSISTENT RESERVE OUT command that is not performed due to an error or reservation conflict. Regardless of the APTPL value the generation value shall be set to 0 as part of the power on reset process.

The **Additional Length** field contains a count of the number of bytes in the reservation key list. If the allocation length specified by the PERSISTENT RESERVE IN command is not sufficient to contain the entire parameter list, then only the bytes from 0 to the maximum allowed allocation length shall be sent to the Initiator. The incremental remaining bytes shall be truncated, although the Additional Length field shall still contain the actual number of bytes in the reservation key list without consideration of any truncation resulting from an insufficient allocation length. This shall not be considered an error.

The **Reservation Key** list contains the 8-byte reservation keys for all Initiators that have registered through all ports with the Target.

17.12.3 Parameter Data for Read Reservations

Table 94: PERSISTENT RESERVE IN, parameter data for Read Reservations

Byte	Bit 7	6	5	4	3	2	1	0
0-3	(MSB) Generation (LSB)							
4-7	(MSB) Additional length (n-7) (LSB)							
8-n	(MSB) Reservation descriptors (LSB)							

The **Generation** field shall be as defined for the Persistent Reserve In Read Keys parameter data. The Additional Length field contains a count of the number of bytes to follow in the Reservation Descriptor(s).

If the **Allocation length** specified by the PERSISTENT RESERVE IN command is not sufficient to contain the entire parameter list, then only the bytes from 0 to the maximum allowed allocation length shall be sent to the Initiator. The remaining bytes shall be truncated, although the Additional Length field shall still contain the actual number of bytes of the Reservation Descriptor(s) and shall not be affected by the truncation. This shall not be considered an error.

The format of the **Reservation Descriptors** is defined in the Persistent Reserve In Reservation Descriptor table. There shall be a Reservation Descriptor for the persistent reservation, if any, present in the Target having a persistent reservation.

Table 95: PERSISTENT RESERVE IN, Read Reservation Descriptor

Byte	Bit 7	6	5	4	3	2	1	0
0-7	(MSB) Reservation key (LSB)							
8-11	(MSB) Scope-specific address (LSB)							
12	Reserved							
13	Scope=0				Type			
14-15	(MSB) Extent Length=0 (LSB)							

.The **Scope** of each persistent reservation created by a PERSISTENT RESERVE OUT command will be returned. See the PERSISTENT RESERVE OUT command section for details.

17.13 PERSISTENT RESERVE OUT (5F)

Table 96: PERSISTENT RESERVE OUT (5F)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 5Fh</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="5">Service Action</td></tr>
<tr><td>2</td><td colspan="4">Scope=0</td><td colspan="4">Type</td></tr>
<tr><td>3-6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">Parameter List Length = 18h</td></tr>
<tr><td>11</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The PERSISTENT RESERVE OUT command is used to request service actions that reserve the drive for the exclusive or shared use of the initiator. The command uses other service actions to manage and remove such reservations. This command is used in conjunction with the PERSISTENT RESERVE IN command, and should not be used with the RESERVE and RELEASE commands.

Note: If a PERSISTENT RESERVE OUT command is received when a RESERVE is active for the drive, the command will be rejected with **Reservation Conflict** status.

Parameter List Length must be 18h. If not, Check Condition status will be returned, with sense key of Illegal Request and additional sense code of Parameter List Length Error.

.

17.13.1 Service Action

The following service action codes are supported.

Table 97: PERSISTENT RESERVE OUT, Service Action Code

Code	Name	Description
00h	**Register**	Register a reservation key
01h	**Reserve**	Create a persistent reservation using a reservation key
02h	**Release**	Release a persistent reservation
03h	**Clear**	Clear all reservation keys and all persistent reservations
04h	**Preempt**	Preempt persistent reservations from another Initiator
05h	**Preempt and Abort**	Preempt persistent reservations from another Initiator and clear the task set for the preempted Initiator
06h	**Register and Ignore existing key**	Register a reservation key
07h-1Fh	**Reserved**	Reserved

17.13.2 Type

The **Type** field specifies the characteristics of the persistent reservation being established for all customer data sectors. Thetable below describes the supported types and how read and write commands are handled for each reservation type.

Table 98: PERSISTENT RESERVE OUT, Type Code

Code	Name	Description
0h	Reserved	Reserved
1h	Write Exclusive	Reads Shared: Any initiator may execute commands that transfer from the media. Writes Exclusive: Only the initiator with the reservation may execute commands that transfer data to the media; Reservation Conflict status will be returned to other initiators.
2h	Reserved	Reserved
3h	Exclusive Access	Reads Exclusive: Only the initiator with the reservation may execute commands that transfer data from the media; Reservation Conflict status will be returned to other initiators. Writes Exclusive: Only the initator with the reservation may execute commands that transfer data to the media; Reservation Conflict status will be returned to other initators.

4h	Reserved	Reserved
5h	Write Exclusive Registrants Only	Reads Shard: Any initiator may execute commands that transfer from media. Writes Exclusive: Only registered initiators may execute commands that transfer data to the media; Reservation Conflict status will be returned to other initiators.
6h	Exclusive Access Registrants Only	Reads Exclusive: Only registered initiators may execute commands that transfer data from the media; Reservation Conflict status will be returned to other initiators. Writes Exclusive: Only registered initiators may execute commands that transfer data to the media; Reservation Conflict status will be returned to other initiators.
7h-Fh	Reserved	Reserved

17.13.3 Parameter list

The **Parameter List** required to perform the PERSISTENT RERSERVE OUT command is defined in the table below. All fields must be sent on all PERSISTENT RESERVE OUT commands, even if the field is not required for the specified service action.

Table 99: Parameter List

Byte	**Bit**							
	7	**6**	**5**	**4**	**3**	**2**	**1**	**0**
0-7	**(MSB)** **Reservation Key** **(LSB)**							
8-15	**(MSB)** **Service Action Reservation Key** **(LSB)**							
16-19	**(MSB)** **Scope-Specific Address** **(LSB)**							
20	**Reserved = 0**							**APTPL**
21-23	**Reserved = 0**							

Reservation Key contains an 8-byte value provided by the initiator, and identifies the initiator that issued the PERSISTENT RESERVE OUT command. The Reservation Key must match the registered reservation key for the initiator for all service actions except REGISTER and REGISTER AND IGNORE EXISTING KEY.

Service Action Reservation Key contents vary based on the service action. For REGISTER and REGISTER AND IGNORE EXISTING KEY, the Service Action Reservation Key must contain the new reservation key to be registered. For PREEMPT and PREEMPT AND ABORT , the field contains the reservation key of the persistent reservation that is being preempted. This field is ignored for all other service actions.

Scope-Specific Address is ignored.

APTPL (Activate Persist Through Power Loss) bit is valid only for REGISTER and REGISTER AND IGNORE EXISTING KEY, and is ignored for all other service actions. If the last valid APTPL bit value received is zero, power loss will cause all persistent reservations to be released, and all reservation keys to be removed. If the last valid APTPL bit value received is one, any persistent reservation and all reservation keys for all initiators will be retained across power cycles.

17.13.4 Summary

Table 100: PERSISTENT RESERVE OUT, Service Action, Parameters

Service Action	Parameters						Generation counter
	Scope Type	Rsv Key	SvcAct RsvKey	S-spec addr	Extent length	APTPL	
(0) Register	ignore	verify	save	ignore	ignore	apply	+ 1
(1) Reserve	apply	verify	ignore	ignore	ignore	ignore	---
(2) Release	apply	verify	ignore	ignore	ignore	ignore	---
(5) Preempt and Abort	apply	verify	save	ignore	ignore	ignore	+ 1

17.13.4.1 Scope, Type

The Scope and the Type are applied in the process for the Reserve, Release, and Preempted and Clear service action but they are ignored in the process for the Register service action because they are not used.

17.13.4.2 Reservation Key

The Reservation Key is verified in each service action process. If the Initiator that registered a key is different from the Initiator requesting PERSISTENT RESERVE OUT command, the drive returns a **Reservation Conflict** status.

17.13.4.3 Service Action Reservation Key

On Register service action, the drive saves the key specified in the Service Action Reservation Key field as a key of Initiator requesting PERSISTENT RESERVE OUT command.

On Preempt and Clear service action, the reservation that has a key specified in the Service Action Reservation Key field is preempted.

On other service actions, this field is ignored.

17.13.4.4 Scope-specified address

Parameter in the Scope-specified address field is ignored by the drive.

17.13.4.5 Extent length

Parameter in the Extent length field is ignored by the drive.

17.13.4.6 APTPL

The APTPL (Active Persist Through Power Loss) is valid only for the Register service action. The drive ignores the APTPL in other service actions.

The following table shows the relationship between the last valid APTPL value and information held by the drive.

Table 101: APTPL and information held by a drive

Information held by the drive	**The last valid APTPL value**	
	0	**1**
Registration	**all keys are set to 0**	**retained**
Persistent Reservation	**all are removed**	**retained**
Generation counter	**set to 0**	**set to 0**

17.13.4.7 Generation counter

The drive increments the Generation counter when Register service action or Preempt and Clear service action complete successfully.

17.14 PRE-FETCH (34)

Table 102: PRE-FETCH (34)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 34h							
1	Reserved = 0			Reserved = 0			Immed = 0	Obso-lete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Transfer Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The PRE-FETCH command requests the drive to transfer data to the cache. No data is transferred to the Initiator.

- **Transfer length** field specifies the number of contiguous blocks of data that are to be transferred into the cache. A transfer length of zero indicates that blocks are to be transferred into the cache until the segment is filled or until the last block on the media.
- **Immed** (Immediate) must be zero. An immediate bit of zero indicates that the status shall not be returned until the operation has completed.

  If the Immed bit is set to one, the drive returns a **Check Condition** status. The sense key shall be set to *Illegal Request* and the additional sense code shall be set to *Invalid Field in CDB*.

17.15 READ (6) - (08)

Table 103: READ (6) - (08)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 08h							
1	Reserved = 0			(MSB) LBA				
2-3	Logical Block Address (LSB)							
4	Transfer Length							
5	VU = 0		Reserved = 0				FLAG	LINK

The READ command requests the drive to transfer from the medium to the initiator the specified number of blocks (Transfer Length) starting at the specified Logical Block Address (LBA).

- **Logical block address** field specifies the logical unit at which the READ operation shall begin.
- **Transfer length** field specifies the number of blocks to be transferred. A value of zero implies 256 blocks are to be transferred.

Note: Error recovery procedure (ERP) handles errors. The error recovery parameters specified by the MODE SELECT command control ERPs. If the drive is formatted with protection information, no protection information will be transmitted or checked.

17.16 READ (10) - (28)

Table 104: READ (10) - (28)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 28h							
1	RDPROTECT			DPO	FUA	Rsvd= 0	FUA_ NV	Obso- lete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Transfer Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The READ (10) command requests the drive to transfer data to the Initiator. The larger LBA and Transfer Length fields permit greater quantities of data to be requested per command than with the READ command and are required to access the full LBA range of the larger capacity drives.

- **FUA_NV** (F orce Unit Access Non-Volatile Cache) may be set to 0 or 1, but is ignored since NV_SUP=0 in Inquiry Page 86h.
- **Transfer length** The number of contiguous blocks to be transferred. If the transfer length is zero, the seek occurs, but no data is transferred. This condition is not considered an error. If read ahead is enabled, a read ahead is started after the seek completes.
- **DPO** (Disable Page Out) bit of one indicates that the data accessed by this command is to be assigned the lowest priority for being written into or retained by the cache. A DPO bit of one overrides any retention priority specified in the Mode Select Page 8 Caching Parameters. A DPO bit of zero indicates the priority is determined by the retention priority. The Initiator should set the DPO bit when the blocks read by this command are not likely to be read again in the near future.
- **FUA** (Force Unit Access) bit of one indicates that the data is read from the media and not from the cache. A FUA bit of zero allows the data to be read from either the media or the cache.
- **RDPROTECT** defines the manner in which protection information read from disk shall be checked during processing of the command. Protection information is stored on disk, and may be transmitted to the drive's internal data buffer and to the initiator with the user data. If the drive is not formatted with protection information, RDPROTECT must be set to 000b, else **Check Condition** status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

- **RDPROTECT=000b**

Protection information is not transmitted to the initiator and is not checked.

RDPROTECT=001b

- Protection information is transmitted to the initiator with the user data
- Logical Block Guard is checked
- Logcial Block Application Tag is checked (applies to READ(32) command only)
- Logical Block Reference Tag is checked

RDPROTECT=010b
- Protection information is transmitted to the initiator with the user data
- Logical Block Guard is not checked
- Logcial Block Application Tag is checked (applies to READ(32) command only)
- Logical Block Reference Tag is checked

RDPROTECT=011b
- Protection information is transmitted to the initiator with the user data
- Logical Block Guard is not checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

RDPROTECT=100b
- Protection information is transmitted to the initiator with the user data
- Logical Block Guard is checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

RDPROTECT=101b, 110b, 111b
These values are reserved. **Check Condition** status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

If a check of the protection information fails, **Check Condition** status will be returned with sense key of Aborted Command and additional sense code indicating which protection field check failed.

Refer to the ANSI T10 standards for additional details of protection information.

If the transfer length is zero, no data is transferred. The CDB is validated and protocol checked and, if no problems are found, **Good** status is returned immediately. This condition is not considered an error.

17.17 READ (12) - (A8)

Table 105: Read (12) - (A8)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = A8h							
1	RDPROTECT			DPO	FUA	Rsvd = 0	FUA_NV	Rsvd = 0
2 - 5	(MSB) Logical Block Address (LSB)							
6 - 9	(MSB) Transfer Length (LSB)							
10	Reserved = 0							
11	VU = 0		Reserved = 0				FLAG	LINK

The READ(12) command causes the drive to transfer data to the initiator. See the READ(10) description for the definitions of the fields in this command.

17.18 READ (16) - (88)

Table 106: READ (16) - (88)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 88h</td></tr>
<tr><td>1</td><td colspan="3">RDPROTECT</td><td>DPO</td><td>FUA</td><td>Rsvd=0</td><td>FUA_NV</td><td>Rsvd=0</td></tr>
<tr><td>2 - 5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6 - 9</td><td colspan="8">(MSB)
Transfer Length
(LSB)</td></tr>
<tr><td>10</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>11</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The READ(16) command causes the drive to transfer data to the initiator. See the READ(10) description for the definitions of the fields in this command.

17.19 READ BUFFER (3C)

Table 107: READ BUFFER (3C)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 3Ch</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="5">Mode</td></tr>
<tr><td>2</td><td colspan="8">Buffer ID = 0</td></tr>
<tr><td>3-5</td><td colspan="8">(MSB)
Buffer Offset
(LSB)</td></tr>
<tr><td>6-8</td><td colspan="8">(MSB) Allocation Length
(LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The READ BUFFER command is used in conjunction with the WRITE BUFFER command as a diagnostic function for testing the memory of the drive and the SCSI bus integrity. This command does not alter the medium.

The function of this command and the meaning of fields within the command descriptor block depend on the contents of the mode field.

MODE	**Description**
00000	Read Combined Header and Data
00010	Read Data
00011	Descriptor
01010	Read Data from Echo Buffer
01011	Echo Buffer Descriptor
11010	Enable Expander Communications Protocol and Echo Buffer
All others	Not supported

17.19.1 Combined Header And Data (Mode 00000b)

In this mode a 4-byte header followed by data bytes is returned to the Initiator during the DATA IN phase. The Buffer ID and the buffer offset field are reserved.

The drive terminates the DATA IN phase when allocation length bytes of header plus data have been transferred or when the header and all available data have been transferred to the Initiator, whichever is less.

The 4-byte READ BUFFER header (see figure below) is followed by data bytes from the data buffer of the drive.

Table 108: Read Buffer Header

Byte	Bit						
	7	6	5	4	3	2	1
0	RSVD = 0						
1-3	(MSB) Buffer Capacity (LSB)						

The buffer capacity specifies the total number of data bytes that are available in the data buffer of the drive. This number is not reduced to reflect the allocation length nor is it reduced to reflect the actual number of bytes written using the WRITE BUFFER command.

Following the READ BUFFER header the drive will transfer data from its data buffer.

17.19.2 Read Data (Mode 00010b)

In this mode, the DATA IN phase contains buffer data.

- **Buffer ID** field must be set to zero, indicating the data transfer buffer. If another value is specified, the command is terminated with **Check Condition** status. The drive shall set sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB.*
- **Buffer Offset** specifies the offset of the memory space specified by the Buffer ID. The Initiator should conform to the offset boundary requirements returned in the READ BUFFER descriptor. If the value exceeds the buffer specified, the command is terminated with **Check Condition** status. The drive shall set sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB.*
- **Allocation Length** The drive terminates the DATA IN phase when allocation length bytes of data have been transferred or when the header and all available data have been transferred to the Initiator, whichever is less.

17.19.3 Descriptor (Mode 00011b)

In this mode, a maximum of four bytes of READ BUFFER descriptor information are returned. The drive returns the descriptor information for the buffer specified by the Buffer ID.

- **Buffer ID** field should normally be set to zero, indicating the drive data transfer buffer. If any other value is specified, the drive returns all zeros in the READ BUFFER descriptor.
- **Buffer Offset** field is reserved.
- **Allocation Length** should be set to four or greater. The drive transfers the allocation length or four bytes of READ BUFFER descriptor, whichever is less. The allocation length of zero indicates no data is transfered.The allocation length of greater than zero and less than four (size of the Descriptor) is an invalid request and will cause the command to be terminated with **Check Condition** status. The drive shall set sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB.*

The READ BUFFER descriptor is defined in the figure below.

Table 109: Read Buffer Description

Byte	Bit						
	7	6	5	4	3	2	1
0	Offset Boundary = 0x09						
1-3	(MSB) Buffer Capacity (LSB)						

The value contained in the Buffer Offset field of subsequent WRITE BUFFER and READ BUFFER commands should be a multiple of two to the power of the offset boundary. The offset boundary is always set to nine, which indicates Sector Boundaries.

17.19.4 Read Data from Echo Buffer (Mode 01010b)

In this mode the drive transfers data from the echo buffer. The echo buffer will transfer the same data as when the WRITE BUFFER command was issued with the mode field set to echo buffer.

WRITE BUFFER command with the mode field set to echo buffer should be sent prior to the READ BUFFER command; otherwise the READ BUFFER command will be terminated with **Check Condition** status and *Illegal Request*.

In this mode Read Buffer transfers the specified amount of data or the amount previously written with a Write Buffer using mode 1010b from the echo buffer, whichever is less.

Issuing a Read Buffer mode 1010b before a Write Buffer mode 1010b will cause indeterminate data to be returned.

The most significant two bytes of the Allocation Length are ignored. The specified amount of data transferred should not be larger than the echo buffer capacity. The echo buffer capacity may be determined by using Read Buffer mode 1011b. Any additional data transferred over and above the echo buffer capacity is regarded as indeterminate.

The Buffer ID and Buffer Offset fields are ignored in this mode.

Note: The echo buffer is a separate buffer from the data buffer used with other read buffer modes. It is intended to be used for domain validation purposes.

17.19.5 Echo Buffer Descriptor (Mode 01011b)

In this mode, a maximum of four bytes of Read Buffer Descriptor information is returned. The drive returns the descriptor information for the echo buffer. The Buffer Offset field is reserved in this mode and must be zero. The drive transfers the lesser of the allocation length or four bytes of following Echo Buffer Descriptor.

Table 110: Echo Buffer Descriptor

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Reserved = 0							EBOS=0
1	Reserved = 0							
2	Reserved = 0				(MSB) Buffer Capacity			
3	Buffer Capacity (LSB)							

- **EBOS** (Echo Buffer Overwritten Supported) bit of zero indicates that the echo buffer is shared by all Initiators.
- **Buffer Capacity** field returns the size of the echo buffer in byte aligned to a 4-byte boundary.

17.19.6 Enable Expander Communications Protocol and Echo Buffer (Mode 11010b)

Receipt of a READ BUFFER command with this mode (11010b) causes a communicative expander to enter the expanded communication protocol mode. SCSI target devices that receive a READ BUFFER command with this mode shall process it as if it were a READ BUFFER command with mode 01010b (see 17.17.4 Read Data from Echo Buffer).

17.20 READ CAPACITY (10) - (25)

Table 111: READ CAPACITY (10) - (25)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 25h</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="4">Reserved = 0</td><td>Obso-
lete</td></tr>
<tr><td>2-5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6-7</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>8</td><td colspan="7">Reserved = 0</td><td>PMI</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The READ CAPACITY command returns information regarding the capacity of the drive.

- **Logical Block Address** is used in conjunction with the PMI bit.
- **PMI** (Partial Medium Indicator) indicates:

PMI	**Description**
0	The drive returns the last LBA of the drive.
1	The drive returns the last LBA and block length in bytes are that of the LBA after which a substantial delay in data transfer will be encountered. This returned LBA shall be greater than or equal to the LBA specified by the LBA fields in the CDB.

This option provides the information that the Initiator needs to determine the amount of space available on the same track that is accessible without a head switch or seek.

17.20.0.1 Returned Data Format

The data returned to the Initiator in response to the READ CAPACITY command is described here. The data is returned in the DATA IN phase.

Table 112: Format of READ CAPACITY command reply

Byte	Bit							
	6	7	5	4	3	2	1	0
0-3	(MSB) Maximum Logical Block Address (LSB)							
4-7	(MSB) Block Length (LSB)							

- **Block Length** specifies the length in bytes of each block of user data (not including protection information).

17.21 READ CAPACITY (16) (9E/10)

Table 113: Read Capcity (16) (9E/10)

Byte	Bit							
	6	7	5	4	3	2	1	0
0	Command Code = 9Eh							
1	Reserved = 0			Service Action = 10h				
2-9	(MSB) Logical Block Address (LSB)							
10-13	(MSB) Allocation Length (LSB)							
14	Reserved = 0							PMI
15	VU = 0		Reserved = 0				FLAG	Link

The READ CAPACITY (16) (9E/10) command returns information regarding the capacity of the drive. This command is processed like the standard READ CAPACITY (25) command.

17.21.1 Returned Data Format

The following data is returned to the initiator in the DATA OUT phase.

Table 114: Returned Data Format

Byte	Bit							
	6	7	5	4	3	2	1	0
0 - 7	(MSB) Maximum Logical Address (LSB)							
8 - 11	(MSB) Block Length (LSB)							
12	Reserved = 0						RTO_EN	PROT_EN
13 - 31	Reserved = 0							

- **RTO_EN (Reference Tag Own Enable)** bit set to one indicates that application client ownership of the Logical Block Reference Tag field in protection information is enabled (i.e. the drive was formatted with protection information enabled and the RTO_REQ bit was set to one). An RTO_EN bit set to zero indicates that application client ownership of the Logical Block Reference Tag field in protection information is disabled.
- **PROT_EN (Protection Enable)** bit set to one indicates that the drive was formatted with protection information enabled. A PROT_EN bit set to zero indicates that the drive was not formatted with protection information enabled.

17.22 READ DEFECT DATA (37)

Table 115: READ DEFECT DATA (37)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 37h							
1	Reserved = 0			Reserved = 0				0
2	Reserved = 0			Plist	Glist	Defect List Format		
3-6	Reserved = 0							
7-8	(MSB) Allocation Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The READ DEFECT DATA command requests that the Target transfer the medium defect data to the Initiator.

If the Target is unable to access any medium defect data it will return a **Check Condition** status with the appropriate sense key. The sense key will be set to either *Medium Error* (03h) if a medium error occurred or *No Sense* (00h) if the list does not exist and the additional sense code will be set to *Defect List Error* (19h).

- **Plist** bit set to one indicates that the Target returns the Plist. A Plist bit of zero indicates that the Target shall not return the Plist of defects.
- **Glist** bit set to one indicates that the Target returns the Glist. A Glist bit of zero indicates that the Target shall not return the Glist.

  Note: With both bits set to one Plist and Glist the Target will return both the primary and grown defect lists. With both bits set to zero, the Target will return only a 4-byte Defect List Header.
- **Defect List format** field is used by the Initiator to indicate the preferred format for the defect list.

  The Defect List Format of '100 (Bytes from Index Format)' and '101 (Physical Sector Format)' are supported. If the requested format is not supported by the drive, it will return the defect list in its default format '101' and then terminate the command with **Check Condition** status. The sense key will be set to *Recovered Error* (01h) and the additional sense code will be set to *Defect List Not Found* (1Ch).

The drive sends defect list (Defect Descriptors) in a 8-byte Absolute Block Address (ABA) format that follows a four byte Defect List Header.

The Target will transfer all of the Read Defect Data up to the number of bytes allocated by the Initiator.

Table 116: Defect List Format

Preferred Defect List Format	Returned Defect List Format
Block (000b)	Physical Sector
Bytes from Index (100b)	Bytes from Index
Physical Sector (101b)	Physical Sector
Vendor Unique (110b)	Physical Sector
Reserved (001b)	
Reserved (010b)	
Reserved (011b)	
Reserved (111b)	

Note: The drive will terminate the Data In phase when the Allocation Length has been transferred or when all available Defect Data has been transferred to the Initiator, whichever is less.

The Read Defect Data contains a 4-byte header followed by zero or more defect descriptors.

17.22.1 Defect List Header

Table 117: Defect List Header

<table>
<tr><th rowspan="3">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><th colspan="8">Defect List Header</th></tr>
<tr><td>0</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td>Plist</td><td>Glist</td><td colspan="3">Defect List Format</td></tr>
<tr><td>2-3</td><td colspan="8">(MSB) Defect List length (LSB)</td></tr>
</table>

17.22.2 Defect List Descriptor

Table 118: Defect List Descriptor

<table>
<tr><th rowspan="3">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><th colspan="8">Defect List Descriptor</th></tr>
<tr><td>0-7</td><td colspan="8">Defect Descriptor 0</td></tr>
<tr><td>.</td><td colspan="8"></td></tr>
<tr><td>8n - (8n+7)</td><td colspan="8">Defect Descriptor n</td></tr>
</table>

17.22.3 Bytes from Index Format (100b)

Table 119: Defect Descriptors of Bytes from Index Format

Byte	Defect Descriptors
0-2	(MSB) Cylinder Number of Defect (LSB)
3	Head Number of Defect
4-7	(MSB) Defect Bytes from Index (LSB)

Defect Bytes from Index is gotten using the following equation:

Bytes from Index = (Physical Sector Number) * N

Where: N = Bytes per sector

17.22.4 Physical Sector Format (101b)

Table 120: Defect Descriptors of Physical Sector Format

Byte	Defect Descriptors
0-2	(MSB) Cylinder Number of Defect (LSB)
3	Head Number of Defect
4-7	(MSB) Defective Sector Number (LSB)

The Defect List Format field specifies the format of the defect list data returned by the Target.

The Defect List Length field specifies the length in bytes of the defect descriptors that follow. The Defect List Length is equal to eight times the number of defect descriptors.

Normally the Target will set the Defect List Length field to the amount of space needed to contain the entire defect list. However, the Target is capable of building a defect list with a length such that the entire list cannot be transferred using the maximum allocation length. If the defect list grows beyond 8191 entries, the defect data cannot be transferred with an allocation length of 0FFFFh. The Target will transfer a partial defect list and return **Check Condition** status with the sense key set to *Recovered Error* and the additional sense code set to *Partial Defect List Transferred*. The defect list length will be set to 0FFF8h, indicating the maximum number of defect descriptors that can be transferred. Defects beyond this number cannot be read by the Initiator.

17.23 READ DEFECT DATA (B7)

Table 121: READ DEFECT DATA (B7)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = B7h							
1	Reserved = 0			Plist	Glist	Defect List Format		
2-5	Reserved = 0							
6-9	(MSB) Allocation Length (LSB)							
10	Reserved = 0							
11	VU = 0		Reserved = 0				FLAG	LINK

(See Section 17.22 “READ DEFECT DATA (37)” on page 160.)

17.23.1 Defect List Header

Table 122: Defect List Header

Byte	Bit							
	7	6	5	4	3	2	1	0
	Defect List Header							
0	Reserved = 0							
1	Reserved = 0			Plist	Glist	Defect List Format		
2-3	Reserved = 0							
4-7	(MSB) Defect List length (LSB)							

(See Defect List Header for Read Defect Data (37) in Section Table 17.22.1, “Defect List Header,” on page 162.)

17.23.2 Defect List Descriptor

Table 123: Defect List Descriptor

Byte	Bit							
	7	**6**	**5**	**4**	**3**	**2**	**1**	**0**
	Defect List Descriptor							
0-7	**Defect Descriptor 0**							
.								
8n - (8n+7)	**Defect Descriptor n**							

(See Defect List Descriptor for Read Defect Data (37) in Section 17.22.2 “Defect List Descriptor” on page 162.)

17.23.3 Bytes from Index Format (100b)

Table 124: Defect Descriptors of Bytes from Index Format

Byte	Defect Descriptors
0-2	**(MSB)** **Cylinder Number of Defect** **(LSB)**
3	**Head Number of Defect**
4-7	**(MSB)** **Defect Bytes from Index** **(LSB)**

Defect Bytes from Index is derived using the following equation:

Bytes from Index = (Physical Sector Number) + N

where N = Bytes per sector.

17.23.4 Physical Sector Format (101b)

Table 125: Defect Descriptors of Physical Sector Format

Byte	Defect Descriptors
0-2	**(MSB)** **Cylinder Number of Defect** **(LSB)**
3	**Head Number of Defect**
4-7	**(MSB)** **Defective Sector Number** **(LSB)**

17.24 READ LONG (3E)

Table 126: READ LONG (3E)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 3Eh</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="3">Reserved = 0</td><td>Cor-rect = 0</td><td>Obso-lete</td></tr>
<tr><td>2-5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">(MSB) Byte Transfer Length
(LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The READ LONG command requests the drive to transfer one block of data to the Initiator. The transfer data includes data and ECC field data.

- **Correct** bit of zero causes the logical block to be read without any correction attempts. When the bit is one, data will be corrected with offline ECC correction before being transferred.
- **Logical Block Address** field specifies the logical block at which the read operation shall occur.
- **Byte Transfer Length** field must specify exactly the number of bytes of data that are available for transfer. If a non-zero byte transfer length does not match the available data length, the Target terminates the command with **Check Condition** status, the sense key is set to *Illegal Request*, and an additional sense code set to *Invalid Field in CDB*. The valid and ILI bits are set to one and the information field is set to the difference of the requested length minus the actual length in bytes. Negative values are indicated by two's complement notation.

  The transfer length is calculated as follows:

  transfer length = logical block size + 56

The data read by this command is neither read from nor retained in the cache.

17.25 REASSIGN BLOCKS (07)

Table 127: REASSIGN BLOCKS (07)

Byte	Bit 7	6	5	4	3	2	1	0
0	Command Code = 07h							
1	Reserved = 0				Reserved = 0			
2 3 4	Reserved = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

The REASSIGN BLOCKS command requests the drive to reassign a logical block to an available spare. The REASSIGN BLOCKS command attempts to allocate spare blocks on a spare track. The LBA is transferred to the drive during the DATA OUT phase. One to four blocks may be specified for relocation per REASSIGN BLOCKS command.

Reassignment is complete upon the completion of the REASSIGN BLOCKS command. At this time, the defective logical block address has been added to the Glist.

All data is preserved during a reassign command except for the target LBA data. The Mode Page 0h DRRT (Disable Restore Reassign Target) bit determines if the reassign blocks command will attempt to recover the Target LBA data. If the Target cannot recover the data at the Target LBA then the Initiator will have to restore the data after the REASSIGN BLOCKS command completes successfully.

If the reassignment begins to move data and is interrupted or fails to complete successfully, the Target enters a degraded mode of operation. In this mode data can be read but writing to the drive is prohibited.

Upon successful completion of this command, the location of the physical sectors reassigned during the command are added to the Glist. The reassigned sectors are marked defective and cannot be accessed again until after a format operation discards the Glist.

Following is the format of the data sent by the Initiator during the DATA OUT phase.

Table 128: Format of Reassign Blocks data

Byte	Bit						
	7	6	5	4	3	2	1
0	Reserved = 0						
1	Reserved = 0						
2-3	(MSB) Defect List Length = 4/8/12/16 (LSB)						
4-7	(MSB) Defect Logical Block Address 1 (LSB)						
8-11	(MSB) Defect Logical Block Address 2 (LSB)						
12-15	(MSB) Defect Logical Block Address 3 (LSB)						
16-19	(MSB) Defect Logical Block Address 4 (LSB)						

- **Defect List Length** must be 4, 8, 12, or 16. Otherwise, the drive returns *Check Condition* with a sense key of *Illegal Request*.
- **Defective Logical Block Address** is four bytes in length. The Initiator can specify from 1 to 4 Defective Logical Block Addresses according to the Defect List Length from 4 to 16, respectively. LBAs are not required to be in ascending order.

17.26 RECEIVE DIAGNOSTICS RESULTS (1C)

Table 129: RECEIVE DIAGNOSTIC RESULTS (1C)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 1Ch</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="4">Reserved = 0</td><td>PCV</td></tr>
<tr><td>2</td><td colspan="8">Page Code</td></tr>
<tr><td>3
4</td><td colspan="8">(MSB) Allocation Length
(LSB)</td></tr>
<tr><td>5</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The RECEIVE DIAGNOSTIC RESULTS command requests that analysis data requested by a SEND DIAGNOSTIC command be sent to the Initiator.

- **PCV** (Page Code Valid) bit of zero indicates that the most recent SEND DIAGNOSTIC command shall define the data returned by this command. PCV bit of one indicates that the contents of the Page Code field shall define the data returned by this command.
- **Allocation Length** specifies the amount of data to be returned to the Initiator. This value may be zero and this is not considered an error. The Target terminates the Data In phase when all available data has been transferred or when the number of bytes transferred equals the Parameter List Length.

17.26.1 Receive Diagnostic Results Page 0

This page contains a list of supported pages.

Table 130: Receive Diagnostic Results page 0

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Page Code = 0</td></tr>
<tr><td>1</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>2-3</td><td colspan="8">Page Length = 02h</td></tr>
<tr><td>4</td><td colspan="8">(Supported Pages) Page = 02h</td></tr>
<tr><td>5</td><td colspan="8">Translate address page = 40h</td></tr>
</table>

The supported diagnostic page returns a list of supported pages in ascending order.

17.26.2 Receive Diagnostic Results Page 40

Using the SEND DIAGNOSTIC command, an address in either physical or logical format is supplied to the drive. This page is then used to retrieve the address translated into the other format.

Table 131: Receive Diagnostic Results Page 40

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Page Code = 40h							
1	Reserved = 0							
2-3	Page Length = 0Ah							
4	Reserved = 0					Supplied format		
5	RA	ALTS	ALTT	Reserved=0		Translate format		
6-n	Translated Address							

- **Page Length** is set to 02h if the address is in a Reserved Area (RA =1). Otherwise, Page Length is set to 06h if the Translate Format is Block format, or 0Ah if the Translate Format is Bytes From Index format or Physical Sector format.
- **Supplied Format** is the value supplied by the SEND DIAGNOSTIC command; it may be one of the three following values:
  - **000b** Block format
  - **100b** Bytes From Index format
  - **101b** Physical Sector format
- **Translate Format** is the value supplied by the SEND DIAGNOSTIC command and specifies the format in which the address has been translated into List. If the supplied format is the Block format, the Translate format must be either Bytes from Index or Physical Sector format. If the supplied format is the Bytes from Index or Physical Sector format, the Translate format must be Block format. Otherwise the Target will terminate the command with **Check Condition** status.
- **RA (Reserved Area)** is set to on if the translated block is an inaccessible sector, which could reflect a defect, an unused sector on a spare cylinder, or a sector beyond the Maximum Customer LBA.
- **ALTS (Alternate Sector)** is set to one if the translated block is a sector in a spare cylinder that points to a reassigned customer sector.
- **ALTT (Alternate Track)** is not used.
- **Translated Address** contains the address in the translate format. If it is an LBA, it is contained within the first four bytes of the field (bytes 6 to 9) of the page data. For a physical format it is as follows:

Table 132: Translated address

Byte	Bit							
	7	6	5	4	3	2	1	0
6-8	Cylinder Number							
9	Head Number							
10-13	Sector Number or Bytes from Index							

17.27 RELEASE (17)

Table 133: RELEASE (17)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 17h							
1	Reserved = 0			3rdPty=0	3rd Party ID			Ext=0
2	Reservation Identification							
3-4	Reserved = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

The RELEASE command is used to release a LUN previously reserved. It is not an error for an Initiator to release a LUN that is not currently active. The drive returns **Good** status without altering the reservation.

- **3rdPty** must be 0. Third Party reservations are not supported. If the 3rdPty bit is not zero, Check Condition status is returned with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.
- **3rd Party ID** is ignored.
- **Ext**ents must be 0. Extension is not supported by the drive.
- **Reservation Identification** field is ignored.

17.28 RELEASE (57)

Table 134: RELEASE (57)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 57h							
1	Reserved = 0			3rdPty=0	Reserved = 0			Ext = 0
2	Reservation Identification							
3	3rd Party Device ID							
4-8	Reserved = 0							
9	VU = 0		Reserved = 0				FLAG	LINK

The RELEASE command is used to release a LUN previously reserved. It is not an error for an Initiator to release a LUN that is not currently active. The drive returns **Good** status without altering the reservation.

- **3rdPty** must be 0. Third Party reservations are not supported. If the 3rdPty bit is not zero, Check Condition status is returned with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.
- **3rd Party ID** is ignored.
- **Ext**ents must be 0. Extension is not supported by the drive.
- **Reservation Identification** field is ignored.

17.29 REPORT DEVICE IDENTIFIER (A3/05)

Table 135: REPORT DEVICE IDENTIFIER (A3/05)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = A3h</td></tr>
<tr><td>1</td><td colspan="4">Reserved = 0</td><td colspan="4">Service Action = 05h</td></tr>
<tr><td>2</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>3</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>4-5</td><td colspan="8">(MSB)
LUN=0
(LSB)</td></tr>
<tr><td>6-9</td><td colspan="8">(MSB)
Allocation Length
(LSB)</td></tr>
<tr><td>10</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>11</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The **REPORT DEVICE IDENTIFIER** command requests that the device server send device identification information to the application client.

The **LUN** contains the logical unit number parameter. This parameter is expected to be zero. Other value for this parameter will cause the command to terminate with a CHECK CONDITION status. The sense key is set to ILLEGAL REQUEST, and the additional sense code is set to INVALID FIELD IN CDB.

The **ALLOCATION LENGTH** field indicates how much space has been reserved for the returned parameter data. If the length is not sufficient to contain all the parameter data, the first portion of the data is returned. This is not considered an error. The actual length of the parameter data is available in the IDENTIFIER LENGTH field in the parameter data. If the remainder of the parameter data is required, the application client should send a new REPORT DEVICE IDENTIFIER command with an ALLOCATION LENGTH field large enough to contain all the data.

The REPORT DEVICE IDENTIFIER parameter list contains a 4-byte field that contains the length in bytes of the parameter list and the logical unit's identifier.

Table 136: Report Device Identifier parameter list

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	(MSB) Identifier Length = n - 3 (LSB)							
4-n	Identifier							

The IDENTIFIER LENGTH field specifies the length in bytes of the IDENTIFIER field. If the ALLOCATION LENGTH field in the CDB is too small to transfer all of the identifier, the length is not adjusted to reflect the truncation. The identifier length initially equals zero and is changed only by a successful SET DEVICE IDENTIFIER command.

The IDENTIFIER field contains a vendor specific value. The value reported is the last value written by a successful SET DEVICE IDENTIFIER command. The value of the identifier is changed only by a SET DEVICE IDENTIFIER command. The identifier value persist through resets, power cycles, media format operations.

The Target return the same Identifier to all Initiators on all ports.

The execution of a REPORT DEVICE IDENTIFIER requires the enabling of a nonvolatile memory within the logical unit. If the nonvolatile memory is not ready, the device server returns **Check Condition** status rather than wait for the device to become ready. The sense key is set to *Not Ready* and the additional sense data is set as described in the TEST UNIT READY command. This information should allow the application client to determine the action required to cause the device server to become ready.

17.30 REPORT LUNS (A0)

Table 137: REPORT LUNS (A0)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = A0h							
1-5	Reserved							
6-9	(MSB) Allocation Length (LSB)							
10	Reserved							
11	VU = 0		Reserved = 0				FLAG	LINK

The REPORT LUNS command requests that the Target return the known LUN to the Initiator. The REPORT LUNS command should always be available and is unaffected by any reservations.

The Allocation Length must be at least 16 bytes. If the Allocation Length is less than 16 bytes, the Target will return a **Check Condition** status with sense key of *Illegal Reques*t and additional sense code of *Invalid Field in CDB*. If the Allocation Length is not sufficient to contain the LUN values for all configured logical units, the Target shall report as many LUN values as will fit in the specified Allocation Length. This is not considered an error.

The REPORT LUNS command will send the LUN list in the subsequent Data Out Phase. The format of the LUN list is shown in the following table.

Table 138: LUN Reporting parameter list format

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	(MSB) LUN List Length = 8 (LSB)							
4-7	Reserved							
8-15	(MSB) LUN = 0 (LSB)							

The LUN list length shall contain the length in bytes of the LUN list that is available to be transferred. This product only supports one LUN. Therefore, the LUN list length must be set to 8. The only supported LUN is zero.

17.31 REPORT SUPPORTED OPERATION CODES (A3/0C)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = A3h							
1	Reserved = 0			Service Action = 0Ch				
2	Reserved = 0					Reporting Options		
3	Requested Operation Code							
4-5	Requested Service Action							
6-9	Allocation Length							
10	Reserved = 0							
11	VU = 0		Reserved = 0				FLAG	LINK

The REPORT SUPPORTED OPERATION CODES command requests information on commands that the drive supports. The initiator may request a list of all operation codes and service actions supported, or the command support data for a specific command.

Reporting Options specifies the information to be returned in the parameter data.

Reporting Options	Description
000b	A list of all operation codes and service actions supported by the drive will be returned in the all_commands parameter data format. The Requested Operation Code field and Requested Service Action field will be ignored.
001b	The command support data for the operation code specified in the Requested Operation Code field will be returned in the one_command parameter data format. The Requested Service Action field will be ignored. If the Requested Operation Code field specifies an operation code that has service actions, Check Condition status will be reported with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.
010b	The command support data for the operation code and service action specified in the Requested Operation Code field and Requested Service Action field will be returned in the one_command parameter data format. If the Requested Operation Code field specifies an operation code that does not have service actions, Check Condition status will be reported with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.
011b-111b	Reserved

Requested Operation Code specifies the operation code of the command to be returned in the one_command parameter data format.

Requested Service Action specifies the service action of the command to be returned in the one_command parameter data format.

Allocation Length specifies the number of bytes that have been allocated for the returned parameter data. If the length is not sufficient to contain all the parameter data, the first portion of the data shall be returned. The actual length of the parameter data may be determined from the Additional Length field in the parameter data.

17.31.1 All_commands parameter data format

The Report Supported Operation Codes all_command parameter data format begins with a four-byte header that contains the length in bytes of the parameter data, followed by a list of supported commands. Each command descriptor contains information about a single supported command CDB (i.e. one operation code and service action combination, or one non-service-action operation code).

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	Command Data Length (n-3)							
4	Command Descriptor 0							
N	Command Descriptor X							

Each **Command Descriptor** contains information about a single supported command CDB.

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Operation Code							
1	Reserved = 0							
2-3	Service Action							
4	Reserved = 0							
5	Reserved = 0							SERVACTV
6-7	CDB Length							

Operation Code contains the operation code of a supported command.

Service Action contains a supported serivce action of the supported operation. If the operation code does not have a service action, the Service Action field will be set to zero.

SERVACTV set to zero indicates the operation code does not have service actions and the Service Action field should be ignored. SERVACTV set to one indicates the operation code field has service actions and the contents of the Service Action field are valid.

CDB Length contains the length of the command CDB in bytes.

17.31.2 One_command parameter data format

The Report Supported Operation Codes one_command parameter data format contains information about the CDB and a usage map for bits in the CDB for the command specified by the Reporting Options, Requested Operation Code, and Requested Service Action fields in the Reported Supported Operation Codes CDB.

Byte	Bit 7	6	5	4	3	2	1	0
0	**Reserved = 0**							
1	**Reserved = 0**					**Support**		
2-3	**CDB Size (n-3)**							
4-n	**CDB Usage Data**							

The **Support** field is defined in the table below.

Recording Option	Description
000b	Data about the requested command is not currently available. All data after byte 1 is not valid. A subsequent request for command support data may be successful.
001b	The requested command is not supported. All data after byte 1 is not valid.
010b	Reserved.
011b	The requested command is supported in conformance with the standard.
100b	Reserved
101b	The requested command is supported in a vendor specific manner.
110b-111b	Reserved.

CDB Size contains the size of the CDB Usage Data field in the parameter data, and the number of bytes in the CDB for the command requested.

CDB Usage Data contains information about the CDB for the command requested. The first byte of the CDB Usage Data field contains the operation code for the command. If the command contains a service action, then that service action code is returned in the same location as the Service Action field of the command CDB. All other bytes of the CDB Usage Data field contain a usage map for bits in the CDB for the command requested.

The bits in the usage map have a one-for-one correspondence to the CDB for the command requested. If the drive evaluates a bit in the CDB, the usage map will contain a one in the corresponding bit position. The usage map will contain a zero in the corresponding bit position for any field treated as ignored or reserved.

17.32 REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS (A3/0D)

Table 139: Report Supported Tasks Management Functions (A3/0D)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = A3h							
1	Reserved = 0			Service Action = 0Dh				
2-5	Reserved = 0							
6-9	Allocation Length							
10	Reserved = 0							
11	VU = 0		Reserved = 0				Flag	Link

The REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS command requests information on task management functions supported by the drive.

Allocation Length specifies the number of bytes that have been allocated for the returned parameter data. The allocation length must be at least four. If the allocation length is less than four, Check Condition Status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

The format of the returned parameter data is shown below.

Byte	Bit							
	7	6	5	4	3	2	1	0
0	ATS	ATSS	CACAS	CTSS	LURS	QTS	TRS	WAKES
1-3	Reserved = 0							

ATS (Abort Task) bit set to one indicates that ABORT TASK is supported. An ATS bit of zero indicates that ABORT TASK is not supported.

ATSS (Abort Task Set) bit set to one indicates that ABORT TASK SET is supported. An ATSS bit of zero indicates that ABORT TASK SET is not supported.

CACAS (Clear ACA) bit set to one indicates that CLEAR ACA is supported. A CACAS bit of zero indicates that CLEAR ACA is not supported.

CTSS (Clear Task Set) bit set to one indicates that CLEAR TASK SET is supported. A CTSS bit of zero indicates that CLEAR TASK SET is not supported.

LURS (Logical Unit Reset) bit set to one indicates that LOGICAL UNIT RESET is supported. An LUR bit of zero indicates that LOGICAL UNIT RESET is not supported.

QTS (Query Task) bit set to one indicates that QUERY TASK is supported. A QTS bit of zero indicates that QUERY TASK is not supported.

TRS (Target Reset) bit set to one indicates that TARGET RESET is supported. A TRS bit of zero indicates that TARGET RESET is not supported.

WAKES (Wakeup) bit set to one indicates that WAKEUP is supported. A WAKES bit of zero indicates that WAKEUP is not supported.

17.33 REQUEST SENSE (03)

Table 140: REQUEST SENSE (03)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 03h							
1	Reserved = 0			Reserved = 0				
2-3	Reserved = 0							
4	Allocation Length							
5	VU = 0		Reserved = 0				FLAG	LINK

The REQUEST SENSE command requests the drive to transfer sense data.

If REQUEST SENSE command with an invalid LUN is received, the drive returns **Good** status and reports a sense key of *Illegal Request* and an additional sense code of *Logical Unit Not Supported*.

If the drive has no sense data available to return, it shall return a sense key of *No Sense* and an additional sense code of *No Additional Sense Information*.

Separate sense data is maintained by the device for each Initiator. Therefore, there is no requirement for an Initiator to expeditiously clear a *Check Condition* as this will not affect other initiators in a multi-Initiator system.

The drive will return the number of bytes in the allocation length or 32 bytes, whichever is less.

17.34 RESERVE (16)

Table 141: RESERVE (16)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 16h							
1	Reserved = 0			3rdPty=0	3rd Party ID			Ext=0
2	Reservation Identification							
3-4	(MSB) Extent List Length = 0 (LSB)							
5	VU = 0		Reserved = 0				FLAG	LINK

The RESERVE command is used to reserve a LUN for an Initiator. This reservation can be either for the Initiator sending the command or for a third party as specified by the Initiator.

Extents are not supported by the drive. The Ext bit must be zero. If Ext bit is set to one, **Check Condition** status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB*. The Reservation Identification and Extent List Length fields are ignored.

The Reserve command requests that the entire LUN be reserved for the Initiator until

- the reservation is superseded by another valid Reserve command from the Initiator that made the reservation.
- the reservation is released by a RELEASE command from the same Initiator.
- a hard Reset condition occurs.
- a Target Reset message is received from any Initiator.
- a power off/on cycle occurs.

3rdPty must be 0. Third Party reservations are not supported. If the 3rdPty bit is not zero, Check Condition status is returned with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.

3rd Party ID is ignored.

Only the Initiator that issued the Reserve command for a LUN may release the LUN, regardless of the 3rdPty option. This Initiator may also release the LUN by issuing another RESERVE command. This superseding RESERVE command releases the previous reservation when the new reservation is granted.

Reservation queuing is not supported by the drive. If a LUN is reserved and a RESERVE command is issued from a different Initiator, the Target responds with a RESERVATION CONFLICT.

17.35 RESERVE (56)

Table 142: RESERVE (56)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 56h</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td>3rdPty=0</td><td colspan="3">Reserved</td><td>Ext=0</td></tr>
<tr><td>2</td><td colspan="8">Reservation Identification</td></tr>
<tr><td>3</td><td colspan="8">Third Pay Device ID</td></tr>
<tr><td>4-6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">(MSB) Extent List Length = 0 (LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The RESERVE command is used to reserve a LUN for an Initiator. This reservation can be either for the Initiator sending the command or for a third party as specified by the Initiator.

Extents are not supported by the drive. The Ext bit must be zero. If Ext bit is set to one, **Check Condition** status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB*. The Reservation Identification and Extent List Length fields are ignored.

The Reserve command requests that the entire LUN be reserved for the Initiator until

- the reservation is superseded by another valid Reserve command from the Initiator that made the reservation.
- the reservation is released by a RELEASE command from the same Initiator.
- a hard Reset condition occurs.
- a Target Reset message is received from any Initiator.
- a power off/on cycle occurs.

3rdPty must be 0. Third Party reservations are not supported. If the 3rdPty bit is not zero, Check Condition status is returned with a sense key of Illegal Request and additional sense code of Invalid Field in CDB.

3rd Party ID is ignored.

Only the Initiator that issued the Reserve command for a LUN may release the LUN, regardless of the 3rdPty option. This Initiator may also release the LUN by issuing another RESERVE command. This superseding RESERVE command releases the previous reservation when the new reservation is granted.

Reservation queuing is not supported by the drive. If a LUN is reserved and a RESERVE command is issued from a different Initiator, the Target responds with a RESERVATION CONFLICT.

17.36 REZERO UNIT (01)

Table 143: REZERO UNIT (01)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 01h							
1	Reserved = 0			Reserved = 0				
2-4	Reserved = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

The REZERO UNIT command requests that the Target seek LBA 0.

17.37 SEEK (6) - (0B)

Table 144: SEEK (6) - (0B)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 0Bh							
1	Reserved = 0			(MSB) LBA				
2 3	Logical Block Address (LSB)							
4	Reserved = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

The SEEK (6) command requests the drive to seek the specified LBA. If the LBA is greater than the value returned by the READ CAPACITY command, the Drive returns a **Check Condition** status with a sense key of *Illegal Request* and an additional sense code of *Invalid Field in CDB.*

17.38 SEEK (10) - (2B)

Table 145: SEEK (10) - (2B)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 2Bh							
1	Reserved = 0			Reserved = 0				0
2-5	(MSB) Logical Block Address (LSB)							
6-8	Reserved = 0							
9	VU = 0		Reserved = 0				FLAG	LINK

The SEEK (10) command requests the drive to seek the specified LBA. If the LBA is greater than the value returned by the READ CAPACITY command, the Drive returns a **Check Condition** status with a sense key of *Illegal Request* and an additional sense code of *Invalid Field in CDB.*

17.39 SEND DIAGNOSTIC (1D)

Table 146: SEND DIAGNOSTIC (1D)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 1Dh							
1	Function Code			PF	RSVD =0	SlfTst	Dev0fl	Unt0fl
2	Reserved = 0							
3-4	(MSB) Parameter List Length (LSB)							
5	VU = 0		Reserved = 0				FLAG	LINK

The SEND DIAGNOSTIC command requests the drive to perform its self-diagnostic test or to perform a function based on a page of information sent in a Data Out phase during the command.

- **PF (Page Format)** bit set to one indicates the data sent by the Initiator conform to the page structure as specified in SCSI standard. This bit must be set to one if the SlfTst bit is set to zero. This bit is ignored by the Target if the SlfTst bit is set.
- **SlfTst** set to one indicates that the device performs its default self-test. If SlfTst is one, the Function code field is ignored. If SlfTst is set to zero, the action to perform is specified in Function code field.

Table 147: SEND DIAGNOSTIC Function Code (1D)

Value	Function name	Description
000b	NA	Value to be used when the SlfTst bit is set to one or if the SEND DIAGNOSTIC command is not invoking one of the other self-test function codes.
001b	Background Short self-test	The device server starts its short self-test routine in background mode.
010b	Background extended self-test	The device server starts its extended self-test routine in background mode.
011b	NA	Reserved.
100b	Abort background self-test	Abort the current self-test in the background mode. This value is only valid if a previous SEND DIAGNOSTIC command specified a background self-test function and that function has not been completed.
101b	Foreground short self-test	The device server starts its short self-test routine in the foreground mode. This self-test will complete in two minutes or less.
110b	Foreground extended self-test	The device server starts its extended self-test routine in the foreground mode. The completion for this test is reported in Mode Page 0Ah (refer to section 17.11.9 Mode Page 0Ah).
111b		Reserved.

- **DevOfl** is ignored by the Target for compatibility.
- **UntOfl** is ignored by the Target for compatibility.
- **Parameter List Length** must be 0 when the SlfTst bit is one. Otherwise, **Check Condition** status will be generated with a sense key of *Illegal Request* and additional sense of *Invalid Field in CDB*. If the SlfTst bit is zero, it should be set to the length of the page to be transferred in the DATA OUT phase of the command. If it does not match the expected length of the page a **Check Condition** status will be also generated with a sense key of *Illegal Request* and additional sense of *Invalid Field in CDB*.

If the motor is not running at the correct speed when the command is received, it is rejected by a **Check Condition** status with a *Not Ready* sense key.

If a fault is detected during the default or foreground self-test, a **Check Condition** is reported as an end status. If a fault is detected during the background self-test, it is logged in the log page for later retrieval by a LOG SENSE command.

See Section 20.17 “Diagnostics” on page 246, for detailed listing of operations carried out by SEND DIAGNOSTIC command and Power on Diagnostics.

17.39.1 Send Diagnostic Page 0

This page requests that the drive return a list of supported pages on the next RECEIVE DIAGNOSTICS command.

Table 148: Diagnostic Page 0

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Page Code = 0							
1	Reserved = 0							
2 - 3	Page Length = 0							

17.39.2 Send Diagnostic Page 40

This allows the Initiator to translate a LBA or physical sector address to the other format. The address to be translated is passed to the Target with the SEND DIAGNOSTIC command and the results are returned to the Initiator by the RECEIVE DIAGNOSTICS command.

The Target will read the parameter list from the Initiator, and, if no errors are detected in the parameter list, **Good** status will be returned. The data translation will be performed upon receipt of the RECEIVE DIAGNOSTICS command.

Table 149: Diagnostic Page 40

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Page Code = 40h							
1	Reserved = 0							
2-3	Page Length = 0Ah							
4	Reserved = 0					Supplied format		
5	Reserved = 0					Translate format		
6-13	Address to Translate							

Supplied Format may take one of the following three values:

- **000b** Block format
- **100b** Bytes From Index format
- **101b** Physical Sector format

It specifies the format in which the address has been supplied.

- **Translate Format** specifies the format that the address should be translated into. If the supplied format is the Block format, the Translate format must be either Bytes from Index or Physical Sector format. If the supplied format is the Bytes from Index or Physical Sector format, the Translate format must be Block format. If either of the format fields is invalid or they specify the same format, the command will terminate with **Check Condition** status with a sense code of *Illegal Request* and *Illegal Field in Parameter List*.

- **Address to Translate** contains the address to translate. If the logical block format is specified, the first four bytes of the field (bytes 6 to 9) contain the LBA and the remainder must be zero. For the physical format the address must be specified as follows.

Table 150: Address to translate

Byte	Bit							
	7	6	5	4	3	2	1	0
6-8	Cylinder Number							
9	Head Number							
10-13	Sector Number or Bytes from Index							

17.40 SET DEVICE IDENTIFIER (A4/06)

Table 151: SET DEVICE IDENTIFIER (A4/06)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = A4h							
1	Reserved = 0				Service Action = 06h			
2	Reserved = 0							
3	Reserved = 0							
4-5	Restricted = 0							
6-9	(MSB) Parameter List Length (LSB)							
10	Reserved = 0							
11	VU = 0		Reserved = 0				FLAG	LINK

The SET DEVICE IDENTIFIER command requests that the device identifier information be set to the value received in the SET DEVICE IDENTIFIER parameter list.

On successful completion of a SET DEVICE IDENTIFIER command a unit attention is generated for all Initiators except the one that issued the service action. When reporting the unit attention condition the additional sense code is set to *Device Identifier Changed*.

- **Parameter List Length** field specifies the length in bytes of the Identifier that is transferred from the host system to the Target. The maximum value for this field is 512 bytes. A parameter list length of zero indicates that no data is transferred, and that subsequent REPORT DEVICE IDENTIFIER commands return an Identifier length of zero.

The SET DEVICE IDENTIFIER parameter list contains the identifier to be set by the addressed logical unit.

Table 152: SET DEVICE IDENTIFIER, Parameter List

Byte	Bit							
	7	6	5	4	3	2	1	0
0-n	Identifier							

The IDENTIFIER field is a vendor specific value, to be returned in subsequent REPORT DEVICE IDENTIFIER commands.

17.41 START STOP UNIT (1B)

Table 153: START STOP UNIT (1B)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 1Bh							
1	Reserved = 0			Reserved = 0				Immed
2-3	Reserved = 0							
4	Power Conditions = 0				Reserved=0		LoEj = 0	Start
5	VU = 0		Reserved = 0				FLAG	LINK

The START STOP UNIT command is used to spin up or stop the spindle motor.

- **Immed** bit is to specify

  0 status is to be returned at the end of the operation.

  1 **Good** status shall always be returned immediately after command has been received. The TEST UNIT READY command may be used to determine when the drive becomes ready after a spin-up.

- **Power Conditions** is not supported by the drive and must be set to 0.
- **LoEj** is not supported by the drive and must be set to 0.
- **Start** bit is to specify:

  0 stop the spindle
  1 start the spindle

Note: Once the drive has become ready (after a power on), the START STOP UNIT command can be used without any errors regardless of the state of the motor: stopped or spinning.

17.42 SYNCHRONIZE CACHE (10) - (35)

Table 154: SYNCHRONIZE CACHE (35)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Command Code = 35h							
1	Reserved = 0			Reserved = 0			Immed = 0	Obso-lete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Number of Blocks (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The SYNCHRONIZE CACHE command ensures that logical blocks in the cache have their most recent data value recorded on the media.

- **Logical Block Address** is to specify where the operation is to begin.
- **Immed** (immediate) must be zero. An immediate bit of zero indicates that the status shall not be returned until the operation has completed. If the Immed bit is set to one, the drive returns a **Check Condition** status. The sense key shall be set to *Illegal Request* and the additional sense code shall be set to *Invalid Field in CDB*.
- **Number of Blocks** specifies the total number of contiguous logical blocks within the range. Number of Blocks of zero indicates that all remaining logical blocks on the logical unit shall be within the range.

17.43 SYNCHRONIZE CACHE (16) - (91)

Table 155: Synchronize Cache (16) - (91)

Byte	BIT							
	7	6	5	4	3	2	1	0
0	Command Code = 91h							
1	Reserved = 0						Immed = 0	Rsvd= 0
2-9	Logical Block Address							
10-13	Number of Blocks							
14	Reserved = 0							
15	VU = 0		Reserved = 0				FLAG	LINK

The SYNCHRONIZE CACHE command ensures that logical blocks in the cache have their most recent data value recorded on the media. See the SYNCHRONIZE CACHE (10) description for definitions of the fields in this command.

17.44 TEST UNIT READY (00)

Table 156: TEST UNIT READY (00)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 00h							
1	Reserved = 0			Reserved = 0				
2-4	Reserved = 0							
5	VU = 0		Reserved = 0				FLAG	LINK

The TEST UNIT READY command allows the Initiator to check if the drive is READY. The SCSI specification defines READY as the condition where the device will accept a media-access command without returning **Check Condition** status.

The drive will first verify that the motor is spinning at the correct speed. If the spindle motor is not spinning at the correct speed, **Check Condition** status is returned with sense key of *Not Ready*. If the motor is spinning at the correct speed, the drive accepts normal media access commands.

The TEST UNIT READY command is not intended as a diagnostic. No self diagnostic is performed by the device as a result of this command.

The TEST UNIT READY command has special significance for power sequencing using the UNIT START command with an Immediate bit of one. In this mode the UNIT START command returns **Task Complete** status before the completion of motor spin-up and expects the initiator to issue TEST UNIT READY commands to determine when the motor has reached the proper speed.

Note: The spindle automatically starts in automatic spin-up mode. The drive does not execute any commands other than TEST UNIT READY, INQUIRY, or REQUEST SENSE command until the Power On sequence is complete. The drive will return **Check Condition** status with *Not Ready* sense key and *In Process of Becoming Ready* sense code for all other commands during the Power On period.

17.45 VERIFY (2F)

Table 157: VERIFY (2F)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">BIT</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 2Fh</td></tr>
<tr><td>1</td><td colspan="3">VRPROTECT</td><td>DPO</td><td colspan="2">Reserved = 0</td><td>Byte Chk</td><td>RSVD = 0</td></tr>
<tr><td>2-5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>7-8</td><td colspan="8">(MSB) Verification Length
(LSB)</td></tr>
<tr><td>9</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The VERIFY command requests that the drive verify the data written on the media. A verification length of zero indicates that no data will be transferred. This condition is not considered an error.

- **ByteChk** bit set to zero indicates that the data is read from the disk and verified using ECC.If an ECC error is detected in the verify process, **Check Condition** status is returned with sense key set to *Medium Error.* ByteChk bit set to one indicates that byte-by-byte comparison is performed between the data on the disk and data transferred from the initiator during the data-out phase.

  If the comparison is unsuccessful, the command is terminated with **Check Condition** status and the sense key is set to *Miscompare*.
- **DPO** (Disable Page Out) bit of one indicates that the data accessed by this command is to be assigned the lowest priority for being written into or retained by the cache. A DPO bit of one overrides any retention priority specified in the Mode Select Page 8 Caching Parameters. A DPO bit of zero indicates the priority is determined by the retention priority. The Initiator should set the DPO bit when the blocks read by this command are not likely to be read again in the near future.

If caching is enabled, the command performs an implied FUA and an implied Synchronize Cache before starting the VERIFY. This ensures that the medium, not the cache, is being verified.

The command stops on *Check Condition* and reports the LBA in error. The command must be reissued, starting with the next LBA, to verify the remainder of the Drive.

The Verification Length is the number of blocks to check.

The data (if any) from the data-out phase and the data from the media are not retained in the cache. Therefore, the DPO bit has no effect on this command and is ignored.

VRPROTECT defines the manner in which protection information read from disk shall be checked during processing of the command. Protection information is stored on disk, and may be validated using the drive's internal checking algorithms, and also byte-by-byte compared using data from the initiator when ByteChk=1.

If the drive is not formatted with protection information, VRPROTECT must be set to 000b, else Check Condition status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

VRPROTECT=000b

If the drive is not formatted with protection information, only user data is verified.

If the drive is formatted with protection information:

- Logical Block Guard is checked
- Logcial Block Application Tag is checked (applies to VERIFY(32) command only)
- Logical Block Reference Tag is checked

VRPROTECT=001b

- Logical Block Guard is checked
- Logcial Block Application Tag is checked (applies to VERIFY(32) command only)
- Logical Block Reference Tag is checked

VRPROTECT=010b

- Logical Block Guard is not checked
- Logcial Block Application Tag is checked (applies to VERIFY(32) command only)
- Logical Block Reference Tag is checked

VRPROTECT=011b

- Logical Block Guard is not checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

VRPROTECT=100b

- Logical Block Guard is checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

VRPROTECT=101b, 110b, 111b

These values are reserved. Check Condition status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

If a check of the protection information fails, Check Condition status will be returned with sense key of Aborted Command and additional sense code indicating which protection field check failed.

If ByteChk=1, the drive's internal checking of protection information is done only when VRPROTECT=000b and the drive is formatted with protection information

If ByteChk=1, and VRPROTECT is not set to 000b, checking of protection information is performed on the fields described above as a byte-by-byte comparison against the data transferred to the drive by the initiator during the Data Out phase..

Refer to the ANSI T10 standards for additional details of protection information.

17.46 VERIFY (12) - (AF)

Table 158: Verify (12) - (AF)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">BIT</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = AFh</td></tr>
<tr><td>1</td><td colspan="3">VRPROTECT</td><td>DPO</td><td>FUA</td><td>Reserved=0</td><td>Byte Chk</td><td>Reserved = 0</td></tr>
<tr><td>2-5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6 - 9</td><td colspan="8">(MSB)
Verification Length
(LSB)</td></tr>
<tr><td>10</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>11</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The VERIFY(12) command causes the drive to verify data written on the media. See the VERIFY(10) description for the definitions of the fields in this command.

17.47 VERIFY (16) - (8F)

Table 159: Verify (16) - (8F)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 08Fh							
1	VRPROTECT			DPO	Reserved = 0		Byte Chk	Rsvd = 0
2-9	(MSB) Logical Block Address (LSB)							
10-13	(MSB) Verification Length (LSB)							
14	Reserved = 0							
15	VU = 0		Reserved = 0				FLAG	LINK

The VERIFY command requests that the drive verify the data written on the media. See the VERIFY (10) description for the definitions of the fields in this command.

17.48 WRITE (6) - (0A)

Table 160: WRITE (6) - (0A)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 0Ah</td></tr>
<tr><td>1</td><td colspan="3">Reserved = 0</td><td colspan="5">(MSB) LBA</td></tr>
<tr><td>2-3</td><td colspan="8">Logical Block Address
(LSB)</td></tr>
<tr><td>4</td><td colspan="8">Transfer Length</td></tr>
<tr><td>5</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The WRITE command requests the drive to write the specified number of blocks of data (**Transfer Length**) from the Initiator to the medium starting at the specified **Logical Block Address** (**LBA**).

See Section 17.15 “READ (6) - (08)” on page 148 for the parameters.

17.49 WRITE (10) - (2A)

Table 161: WRITE (10) - (2A)

Byte	Bit 7	6	5	4	3	2	1	0
0	Command Code = 2Ah							
1	WRPROTECT			DPO	FUA	Rsvd=0	FUA_NV	Obsolete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB Transfer Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The WRITE (10) command requests that the drive write the data transferred from the Initiator. This command is processed like the standard WRITE (6) - (0A) command except for the longer transfer length.

- **Transfer Length** is the number of contiguous blocks to be transferred. If the transfer length is zero, the seek occurs, but no data is transferred. This condition is not considered an error.
- **DPO** (Disable Page Out) bit of one indicates that the data accessed by this command is to be assigned the lowest priority for being written into or retained by the cache. A DPO bit of one overrides any retention priority specified in the Mode Select Page 8 Caching Parameters. A DPO bit of zero indicates that the priority is determined by the retention priority. The Initiator should set the DPO bit when the blocks written by this command are not likely to be read in the near future.
- **FUA** (Force Unit Access) bit of one indicates that the Target must write the data to the media before returning **Good** status. A FUA bit of zero indicates that the Target may return **Good** status prior to writing the data to the media.
- **FUA_NV** (Force Unit Access Non-Volatile Cache) may be set to 0 or 1, but is ignored since NV_SUP=0 in Inquiry Page 86h.

If a WRITE(6) command is received after protection information is enabled, the drive will set the protection information as follows as it writes each block to disk:

- the Logical Block Guard field is set to a properly generated CRC
- the Logical Block Reference Tag field is set to:
- the least significant four bytes of the LBA, if the RTO_EN bit is set to zero in the READ CAPACITY (16) parameter data ; or
- FFFFFFFFh, if the RTO_EN bit is set to one;
- the Logical Block Application Tag field is set to
- FFFFh, if the ATO bit is set to one in Mode Page 0Ah
- Any value, if the ATO bit is set to zero.

WRPROTECT defines the manner in which protection information written to disk shall be checked during processing of the command. Protection information may be transmitted to the drive with the user data, based on the WRPROTECT bit and the drive format.

If the drive is not formatted with protection information, WRPROTECT must be set to 000b, else **Check Condition** status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

WRPROTECT=000b

Protection information is not transmitted to the drive.

If the drive is formatted with protection information, the drive will write protection information to disk based on its internal algorithms.

WRPROTECT=001b

- Protection information is transmitted to the drive with the user data
- Logical Block Guard is checked
- Logcial Block Application Tag is checked (applies to WRITE (32) command only)
- Logical Block Reference Tag is checked

WRPROTECT=010b

- Protection information is transmitted to the drive with the user data
- Logical Block Guard is not checked
- Logcial Block Application Tag is checked (applies to WRITE(32) command only)
- Logical Block Reference Tag is checked

WRPROTECT=011b

- Protection information is transmitted to the drive with the user data
- Logical Block Guard is not checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

WRPROTECT=100b

- Protection information is transmitted to the drive with the user data
- Logical Block Guard is checked
- Logcial Block Application Tag is not checked
- Logical Block Reference Tag is not checked

WRPROTECT=101b, 110b, 111b

These values are reserved. Check Condition status will be returned with sense key of Illegal Request and additional sense code of Invalid Field in CDB.

If a check of the protection information fails, Check Condition status will be returned with sense key of Aborted Command and additional sense code indicating which protection field check failed.

Refer to the ANSI T10 standards for additional details of protection information.

17.50 WRITE (12) - (AA)

Table 162: Write (12) - (AA)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = AAh</td></tr>
<tr><td>1</td><td colspan="3">WRPROTECT</td><td>DPO</td><td>FUA</td><td>Rsvd=0</td><td>FUA_NV</td><td>Rsvd=0</td></tr>
<tr><td>2-5</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>6-9</td><td colspan="8">(MSB)
Transfer Length
(LSB)</td></tr>
<tr><td>10</td><td colspan="8">Reserved=0</td></tr>
<tr><td>11</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The WRITE(12) command causes the drive to write data from the initiator to the media. See the WRITE(10) description for the definitions of the fields in this command.

17.51 WRITE (16) - (8A)

Table 163: Write (16) - (8A)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 8Ah							
1	WRPROTECT			DPO	FUA	Rsvd=0	FUA_NV	Rsvd=0
2-9	(MSB) Logical Block Address (LSB)							
10-13	(MSB) Tranfer Length (LSB)							
14	Reserved = 0							
15	VU = 0		Reserved = 0				FLAG	LINK

The WRITE(16) command causes the drive to write data from the initiator to the media. See the WRITE(10) description for the definitions of the fields in this command.

17.52 WRITE AND VERIFY (10) - (2E)

Table 164: WRITE AND VERIFY (10) - (2E)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 2Eh							
1	WRPROTECT			DPO	Reserved = 0		Byte Chk	Obso-lete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Transfer Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

WRITE AND VERIFY command requests that the drive writes the data transferred from the Initiator to the medium and then verify that the data is correctly written. If caching is enabled, an implied FUA (Force Unit Access) and an implied Synchronize Cache are performed before starting the operation. This insures that data from the disk, not the cache, is verified.

- See the WRITE (10) command description for the definition of the WRPROTECT field.
- **Transfer Length** is the number of contiguous blocks to to transferred. If the transfer length is zero, the seek occurs, but no data is transferred. This condition is not considered an error.

  If caching is enabled, the command performs an implied FUA and an implied Synchronize Cache before starting the operation. This insures that the medium, not the cache, is being verified.
- **ByteChk** bit set to zero indicates that the data is read back from the disk and verified using ECC after the successful write operation. If an ECC error is detected in the verify process, **Check Condition** status is returned with sense key set to *Medium Error.* ByteChk bit set to one indicates that byte-by-byte comparison is performed between data on the disk starting the block specified in LBA field and data transferred from the Initiator.

  If the comparison is unsuccessful, the command is terminated with **Check Condition** status and the sense key is set to *Miscompare*.
- **DPO** (Disable Page Out) bit of one indicates that the data written by this command is to be assigned the lowest priority for being written into or retained by the cache. A DPO bit of one overrides any retention priority specified in the Mode Select Page 8 Caching parameters. A DPO bit of zero indicates the priority is determined by the retention priority.

  The Initiator should set the DPO bit when the blocks written by this command are not likely to be read again in the near future.

17.53 WRITE AND VERIFY (12) - (AE)

Table 165: Write andVerify (12) - (AE)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = AEh							
1	WRPROTECT			DPO	Reserved = 0		ByteChk	Obsolete
2-5	(MSB) Logical Block Address (LSB)							
6-9	(MSB) Transfer Length (LSB)							
10	Reserved = 0							
11	VU = 0		Reserved = 0				FLAG	LINK

The WRITE AND VERIFY command requests that the drive write the data transferred from the Initiator to the medium and then verify that the data is correctly written. See the WRITE AND VERIFY (10) description for the definitions of the fields in this command.

17.54 WRITE AND VERIFY (16) - (8E)

Table 166: Write and Verify (16) - (8E)

<table>
<tr><th rowspan="2">Byte</th><th colspan="8">Bit</th></tr>
<tr><th>7</th><th>6</th><th>5</th><th>4</th><th>3</th><th>2</th><th>1</th><th>0</th></tr>
<tr><td>0</td><td colspan="8">Command Code = 8Eh</td></tr>
<tr><td>1</td><td colspan="3">WRPROTECT</td><td>DPO</td><td colspan="2">Reserved = 0</td><td>Byte Chk</td><td>Obso-lete</td></tr>
<tr><td>2-9</td><td colspan="8">(MSB)
Logical Block Address
(LSB)</td></tr>
<tr><td>10-13</td><td colspan="8">(MSB) Transfer Length
(LSB)</td></tr>
<tr><td>14</td><td colspan="8">Reserved = 0</td></tr>
<tr><td>15</td><td colspan="2">VU = 0</td><td colspan="4">Reserved = 0</td><td>FLAG</td><td>LINK</td></tr>
</table>

The WRITE AND VERIFY command requests that the drive write the data transferred from the Initiator to the medium and then verify that the data is correctly written.

17.55 WRITE BUFFER (3B)

Table 167: WRITE BUFFER (3B)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 3Bh							
1	Reserved = 0			Mode				
2	Buffer ID							
3-5	(MSB) Buffer Offset (LSB)							
6-8	(MSB) Parameter List Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The WRITE BUFFER command is used in conjunction with the READ BUFFER command as a diagnostic function for testing the memory of the drive and the SCSI bus integrity. This command does not alter the medium of the drive. Additional modes are provided for downloading microcode and saving microcode.

This command will cause the entire cache to be emptied.

The function of this command and the meaning of fields within the command descriptor block depend on the contents of the mode field.

MODE	Description
00000	Write combined header and data
00010	Data
00100	Download Microcode
00101	Download Microcode and Save - single binary file
00111	Download Microcode and Save - multiple binary files
01010	Write Data to Echo Buffer
11010	Enable expander Communications Protocol
All Others	Not Supported

If any values other than shown above are specified, **Check Condition** status is returned with a sense key of *Illegal Request* and additional sense code of *Invalid Field in CDB.*

17.55.1 Combined Header And Data (Mode 00000b)

In this mode, the data to be transferred is preceded by a four-byte header.

Buffer ID must be zero. If another value is specified, no download function is performed and the command is terminated with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB.*

Buffer Offset must be zero. If another value is specified, no download function is performed and the command is terminated with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB.*

Parameter List Length specifies the number of bytes that shall be transferred during the DATA OUT phase. This number includes four bytes of header, so the data length to be stored in the drive buffer is transfer length minus four. If the length exceeds the buffer size, the command is terminated with **Check Condition** status. And the drive shall set sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB*. A Parameter List Length of less than four (size of header) indicates no data is transferred.

The 4-byte header consists of all reserved bytes.

Table 168: Write Buffer Header

Byte	Bit							
	7	6	5	4	3	2	1	0
0-3	Reserved = 0							

17.55.2 Write Data (Mode 00010b)

In this mode, the DATA OUT phase contains buffer data.

Buffer ID must be zero. If another value is specified, no download function is performed and the command is terminated with Check Condition status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field In CDB.*

Buffer Offset specifies the offset of the memory space specified by the Buffer ID. The initiator should conform to the offset boundary requirements returned in the READ BUFFER descriptor. If the value exceeds the buffer specified, the command is terminated with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field In CDB.*

Parameter List Length specifies the Parameter List Length. It must be

- less than the capacity of the buffer size after adding the Buffer Offset value and
- on a sector boundary

A Parameter List Length of zero indicates no data is to be transferred and command status is returned.

If an invalid value is specified, the command is terminated with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field In CDB.*

17.55.3 Download Microcode (Mode 00100b)

NOTE: It is not expected that a customer will ever issue this format of the command.

In this mode, the microcode is transferred to the control memory space of the drive. When downloaded, the drive will operate with the newly downloaded code immediately until the next power cycle.

Buffer ID field is used to indicate which portion of the microcode image is being downloaded. The following Buffer IDs are supported by the Target:

- 00h: Main Microprocessor Code
- nnh : ID of Vendor Unique Reserved Area

Any unsupported value for the Buffer ID will cause the command to terminate with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field In CDB*.

Buffer Offset must be zero. If an invalid value is specified, the command is terminated with **Check Condition** status. The drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field in CDB*.

Parameter List Length must be the size of the data set to be downloaded. It may also be set to 0000h in which case no code is updated and command status is returned. If an invalid value is specified, the command is terminated with **Check Condition** status. And the drive shall set the sense key to *Illegal Request* and additional sense code to *Illegal Field In CDB.*

This process generates a unit attention condition for MICROCODE HAS BEEN CHANGED for all Initiators except the one which sent the WRITE BUFFER command. Upon the completion of the WRITE BUFFER command the new microcode is immediately ready for operation.

Note: The Download Microcode mode described in this specification is to indicate that the drive will accept a command with this mode, though it is not expected that a user will ever issue such a command. To use the write buffer command with this mode, a special microcode version is required from development. If such a microcode is released from development, then it will include appropriate instructions on the function of new microcode and its effect on the drive operations after download.

17.55.4 Download Microcode and Save (Mode 00101b) -Single Binary File

In this mode the data is transferred to the drive to save into the System reserved area on the disk. This is for functional upgrade and configuration change reflecting the user's requirements and the manufacturer's reason or both, and it is stored in the media as a permanent copy. The newly downloaded code becomes effective after the drive issues and completes a self-initiated Power On Reset.

Note: It requires up to 30 seconds to update the microcode including the Flash ROM update.

Note: New code to be downloaded to the drive will be provided by development either by request of a customer for an additional function or as a result of a functional change by development. However please note that not all possible fixes or new functions can be applied to a drive in this manner and that there is a very high dependency on the level of ROM code contained within the drive. If an invalid code or a code not compatible with the ROM code is downloaded, the drive will usually reject this code and will continue normal operation. However there is a small possibility that an invalid code will be accepted. If this occurs, the unit usually becomes inoperable and will have to be returned to the manufacturer for recovery.

Buffer ID field is used to indicate which portion of the microcode image is being downloaded. To download microcode, the buffer ID should be set to 00h. Other values are reserved for Hitachi development purposes only.

17.55.5 Download Microcode and Save (Mode 00111b) - Multiple Binary Files

In this mode the target receives a segment of the binary microcode file. The Parameter List Length (segment length) of each segment shall be a multiple of 4K bytes. The total length of all segments received shall be equal to the total length of the binary microcode file. All segments must be sent in the proper sequential order.
All segments must be sent in the proper sequential order.

If an invalid Parameter List Length is specified, **Check Condition** status is returned with sense key of Illegal Request and additional sense code of *Invalid Field in CDB*.

The first segment sent in this mode indicates, by default, the first segment of the binary microcode file. If a **Check Condition** status is returned in this mode, a **Buffer ID** == 00h in the subsequent Write Buffer command in this mode indicates the first segment of the binary microcode file. Otherwise the **Buffer ID** field is ignored.

The **Buffer Offset** field is ignored.

After all segments of the binary microcode file have been received, the drive behavior is the same as Download Microcode and Save (Mode 00101b) - Single Binary File.

17.55.6 Write Data to Echo Buffer (Mode 01010b)

In this mode the Target transfers data into the echo buffer. The echo buffer is assigned in the same manner by the Target as it would for a WRITE operation. Data will be sent aligned on 4-byte boundaries.

Upon successful completion of a WRITE BUFFER command the data will be preserved in the echo buffer unless there is an intervening command to any logical unit, in which case it may be changed.

17.55.7 Enable Expander Communications Protocol (Mode 11010b)

In this mode the drive behavior is the same as Write Data to Echo Buffer (Mode 0101b).

17.56 WRITE LONG (3F)

Table 169: WRITE LONG (3F)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 3Fh							
1	Reserved = 0			Reserved = 0				Obsolete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Byte Transfer Length (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The WRITE LONG command requests the drive to write **one block** of data transferred from the Initiator.

The transfer data must include

- User Data
- 56 bytes of ECC data

Parameters are

- **Logical Block Address** field specifies the logical block at which the write operation shall occur.
- **Byte Transfer Length**. This field must specify the exact number of bytes of data available for transfer. If a non-zero byte transfer length does not match the available data length, the Target terminates the command with **Check Condition** status, then the sense key is set to *Illegal Request*, and an additional sense code is set to *Invalid Field in CDB.* The valid and ILI bits are set to one and the information field is set to the difference of the requested length minus the actual length in bytes. Negative values are indicated by two's complement notation.

17.57 WRITE SAME (41)

Table 170: WRITE SAME (41)

Byte	Bit 7	6	5	4	3	2	1	0
0	Command Code = 41h							
1	WRPROTECT			Reserved = 0		PBDA TA=0	LBDA TA=0	Obso-lete
2-5	(MSB) Logical Block Address (LSB)							
6	Reserved = 0							
7-8	(MSB) Number of Blocks (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The WRITE SAME command instructs the Target to write a single block of data transferred to the Target from the Initiator to a number of sequential logical blocks. This command is useful for writing large data areas without sending all of the data over the SCSI bus.

- See the WRITE(10) command description for the definition of the WRPROTECT field.
- **Logical Block Address** specifies the address at which the write begins. The Number of Blocks specifies the number of contiguous blocks to be written. If the number is zero, all of the remaining blocks on the specified Logical Unit are written.
- **Number of Blocks** specifies the number of contiguous blocks to be written. If the number is zero, all of the remaining blocks on the specified logical unit are written.
- **RelAdr** (Relative Block Address) is not supported and must be set to be zero.

The data for this command is not retained in the cache.

17.58 WRITE SAME (16) - (93)

Table 171: Write Same (16) - (93)

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Command Code = 93h							
1	WRPROTECT					PBDATA =0	LBDATA =0	Obsolete
2-9	(MSB) Logical Block Address (LSB)							
10-13	(MSB) Number of Blocks (LSB)							
14	Reserved = 0							
7-8	(MSB) Number of Blocks (LSB)							
9	VU = 0		Reserved = 0				FLAG	LINK

The Write Same command instructs the Target to write a single block of data transferred to the Target from the Initiator to a number of sequential logical blocks. This command is useful for writing large data areas without sending all of the data over the SCSI bus. See the WRITE(10) command description for the definition of the WRPROTECT field.

18.0 SCSI Status Byte

Upon the completion of a command a status byte is sent to the initiator. Additional sense information may also be available depending on the contents of the status byte. The following section describes the possible values for the status byte and sense data. All Reserved fields are set to zero.

Table 172: SCSI Status Byte. Format of the SCSI STATUS byte.

Bit							
7	**6**	**5**	**4**	**3**	**2**	**1**	**0**
Reserved = 0		Status Code					RSVD

STATUS BYTE Description

00h GOOD

The command has been successfully completed.

02h CHECK CONDITION

An error, exception, or abnormal condition has been detected. The sense data is set by the drive. The REQUEST SENSE command should be issued to determine the nature of the condition.

04h CONDITION MET

This status is returned when an unlinked PRE-FETCH command has been successfully completed.

08h BUSY

This condition is returned when disconnect privilege is not granted while the drive is BUSY processing the other command for the other initiator. The normal initiator recovery action is to issue the command at a later time or to reissue the command and grant the disconnect privilege.

10h INTERMEDIATE

This status is returned for every command except PRE-FETCH command in a series of linked commands (except the last command), unless the command is terminated with CHECK CONDITION, RESERVATION CONFLICT, BUSY status. If INTERMEDIATE or INTERMEDIATE-CONDITION MET status is not returned, the series of linked commands is terminated and the task is ended.

14h INTERMEDIATE CONDITION MET

This status is returned when a linked PRE-FETCH command has been completed, unless the command is terminated with CHECK CONDITION, RESERVATION CONFLICT, BUSY status. If INTERMEDIATE or INTERMEDIATE-CONDITION MET status is not returned, the series of linked commands is terminated and the task is ended.

18h RESERVATION CONFLICT

This status is returned whenever an SCSI device attempts to access the drive, but it has been reserved by another initiator.

28h QUEUE FULL

This status indicates that the target's command queue is full. If a tagged command queuing feature is enabled and there is no room on the command queue, this status is returned when the initiator sends a command. For this status, sense data are not valid.

19.0 SCSI message system

This chapter details how the message system is implemented on the drive. Included is a functional description of the supported messages.

19.1 Supported messages

The messages supported by the drive are listed below:

MESSAGE	CODE(hex)	Direction		Negate ATN Before last ACK
TASK COMPLETE	00	IN		---
SYNCHRONOUS DATA TRANSFER REQUEST	01030	IN	OUT	Yes
WIDE DATA TRANSFER REQUEST*	010203	IN	OUT	Yes
PARALLEL PROTOCOL REQUEST	010604	IN	OUT	Yes
SAVE DATA POINTER	02	IN		---
RESTORE POINTERS	03	IN		---
DISCONNECT	04	IN		---
INITIATOR DETECTED ERROR	05		OUT	Yes
ABORT TASK SET	06		OUT	Yes
MESSAGE REJECT	07	IN	OUT	Yes
NO OPERATION	08		OUT	Yes
MESSAGE PARITY ERROR	09		OUT	Yes
LINKED TASK COMPLETE	0A	IN		---
LINKED TASK COMPLETE (w/FLAG)	0B	IN		---
TARGET RESET	0C		OUT	Yes
ABORT TASK	0D		OUT	Yes
CLEAR TASK SET	0E		OUT	Yes
LOGICAL UNIT RESET	17		OUT	YES
SIMPLE	20XX	IN	OUT	No
HEAD OF QUEUE	21XX		OUT	No
ORDERED	22XX		OUT	No
IGNORE WIDE RESIDUE*	2301	IN		---
IDENTIFY	80-FF	IN		---
IDENTIFY	80-FF		OUT	No

Key: IN = Target to Initiator, OUT = Initiator to Target
YES = Initiator shall negate ATN before last ACK of message
NO = Initiator may or may not negate ATN before last ACK of message
--- = Not applicable
XX = Queue Tag
* = Wide SCSI Only

If an unsupported message is received, the drive will send the MESSAGE REJECT message to the Initiator. If at the time the unsupported message is received a valid NEXUS exists, the drive will continue with the command. If no valid NEXUS exists, the drive will go to Bus Free.

19.1.1 Task Complete (00)

The drive sends this message to the Initiator to indicate that the execution of a command has been terminated and that valid status has been sent to the Initiator. After successfully sending this message the drive releases all bus signals and goes to BUS FREE phase.

19.1.2 Synchronous Data Transfer Request (01, 03, 01h)

Table 173: Synchronous Data Transfer Request.

Byte	Value	Description
0	01H	Extended message
1	03H	Extended message length
2	01H	SYNCHRONOUS DATA TRANSFER REQUEST code
3	M	Transfer period (M times 4 nanoseconds)
4	X	REQ/ACK offset

A pair of Synchronous Data Transfer Request (SDTR) messages shown in Figure 159 are exchanged between an Initiator and a Target to establish the synchronous data transfer mode between the two devices. The message exchange establishes the permissible transfer period and REQ/ACK offset for a synchronous data transfer between the two devices. The Initiator may initiate a synchronous data transfer negotiation at any time after the LUN has been identified. A SDTR message exchange shall be initiated by a SCSI device whenever a previously arranged data transfer agreement may have become invalid. *The agreement becomes invalid after any condition that may leave the data transfer agreement in an indeterminate state such as*

1. after a Power-on Reset
2. after a SCSI Bus “hard” reset condition
3. after a Target Reset message

In addition a SCSI device may initiate a SDTR message exchange whenever it is appropriate to negotiate a new data transfer agreement (either synchronous or asynchronous).

M The transfer period (M) is the minimum time allowed between leading edges of successive REQ pulses and of successive ACK pulses to meet the device requirements for successful reception of data. The drive supports transfer period in the range of 50 ns to 425 ns in increments of 25 ns. In addition to this, when the drive is working in LVD mode, it supports transfer period of 25 ns.

REQ/ACK Offset

X The ACK/REQ offset (X above) is the maximum number of REQ pulses allowed to be outstanding before the leading edge of its corresponding ACK pulses is received at the drive. A REQ/ACK offset value of zero indicates asynchronous data transfer mode. The drive supports REQ/ACK offset values in the range 0 through 63.

If ATN is negated before all bytes of a multiple-byte extended message is received, the drive will go to **BUS FREE** to signal a catastrophic error.

19.1.2.1 Synchronous Negotiation started by the Initiator

When the Target responds with REQ/ACK offset value of 0, the Initiator shall use asynchronous data transfer mode.

LVD mode. The Target responds to each Initiator requested transfer period as shown below.

Table 174: Initiator Request/Target Response (LVD mode)

Initiator request	Target response	Target transfer period (ns)	Maximum burst rate (MT/s)
0 <= Mi <= 09	Mt = 10	25.00	40.00
10 <= Mi <= 10	Mt = 10	25.00	40.00
11 <= Mi <= 12	Mt = 12	50.00	20.00
13 <= Mi <= 18	Mt = Mi	75.00	13.33
19 <= Mi <= 25	Mt = Mi	100.00	10.00
26 <= Mi <= 31	Mt = Mi	125.00	8.00
32 <= Mi <= 37	Mt = Mi	150.00	6.67
38 <= Mi <= 43	Mt = Mi	175.00	5.71
44 <= Mi <= 50	Mt = Mi	200.00	5.00
51 <= Mi <= 56	Mt = Mi	225.00	4.44
57 <= Mi <= 62	Mt = Mi	250.00	4.00
63 <= Mi <= 68	Mt = Mi	275.00	3.64
69 <= Mi <= 75	Mt = Mi	300.00	3.33
76 <= Mi <= 81	Mt = Mi	325.00	3.08
82 <= Mi <= 87	Mt = Mi	350.00	2.86
88 <= Mi <= 93	Mt = Mi	375.00	2.67
94 <= Mi <= 100	Mt = Mi	400.00	2.50
101 <= Mi <= 106	Mt = Mi	425.00	2.35
107 <= Mi <= 255	Mt = 106	(Asynch mode)	N/A

SE mode. The Target responds to each Initiator requested transfer period as shown below.

Table 175: Initiator Request/Target Response (SE mode)

Initiator request	Target response	Target transfer period (ns)	Maximum burst rate (MT/s)
0 <= Mi <= 11	Mt = 12	50.00	20.00
12 <= Mi <= 12	Mt = 12	50.00	20.00
13 <= Mi <= 18	Mt = Mi	75.00	13.33
19 <= Mi <= 25	Mt = Mi	100.00	10.00
26 <= Mi <= 31	Mt = Mi	125.00	8.00
32 <= Mi <= 37	Mt = Mi	150.00	6.67
38 <= Mi <= 43	Mt = Mi	175.00	5.71
44 <= Mi <= 50	Mt = Mi	200.00	5.00
51 <= Mi <= 56	Mt = Mi	225.00	4.44
57 <= Mi <= 62	Mt = Mi	250.00	4.00
63 <= Mi <= 68	Mt = Mi	275.00	3.64
69 <= Mi <= 75	Mt = Mi	300.00	3.33
76 <= Mi <= 81	Mt = Mi	325.00	3.08
82 <= Mi <= 87	Mt = Mi	350.00	2.86
88 <= Mi <= 93	Mt = Mi	375.00	2.67
94 <= Mi <= 100	Mt = Mi	400.00	2.50
101 <= Mi <= 106	Mt = Mi	425.00	2.35
107 <= Mi <= 255	Mt = 106	(Asynch mode)	N/A

19.1.2.2 Synchronous Negotiation started by the Target

If the drive recognizes that negotiation is required, and if the “Target Initiated SDTR” jumper is installed, the drive sends a SDTR message to the Initiator with minimum transfer period on the current receiver mode. The drive interprets the Initiator corresponding transfer period as shown in the figure below.

LVD mode

Table 176: Target response to Initiator's transfer period (LVD mode)

Initiator's request	Target transfer period (ns)	Maximum burst rate (MT/s)
0 <= Mi <= 09	Send M+=10 to negotiate	N/A
10 <= Mi <= 10	25	40.00
11 <= Mi <= 11	50	20.00
12 <= Mi <= 12	50	20.00
13 <= Mi <= 18	75	13.33
19 <= Mi <= 25	100	10.00
26 <= Mi <= 31	125	8.00
32 <= Mi <= 37	150	6.67
38 <= Mi <= 43	175	5.71
44 <= Mi <= 50	200	5.00
51 <= Mi <= 56	225	4.44
57 <= Mi <= 62	250	4.00
63 <= Mi <= 68	275	3.64
69 <= Mi <= 75	300	3.33
76 <= Mi <= 81	325	3.08
82 <= Mi <= 87	350	2.86
88 <= Mi <= 93	375	2.67
94 <= Mi <= 100	400	2.50
101 <= Mi <= 106	425	2.35
107 <= Mi <= 255	Send Message Reject (Asynch mode)	N/A

SE Mode

Table 177: Target response to Initiator's transfer period (SE mode)

Initiator's request	Target transfer period (ns)	Maximum burst rate (MT/s)
0 <= Mi <= 11	M+=12 to negotiate	N/A
12 <= Mi <= 12	50.00	20.00
13 <= Mi <= 18	75.00	13.33
19 <= Mi <= 25	100.00	10.00
26 <= Mi <= 31	125.00	8.00
32 <= Mi <= 37	150.00	6.67
38 <= Mi <= 43	175.00	5.71
44 <= Mi <= 50	200.00	5.00
51 <= Mi <= 56	225.00	4.44
57 <= Mi <= 62	250.00	4.00
63 <= Mi <= 68	275.00	3.64
69 <= Mi <= 75	300.00	3.33
76 <= Mi <= 81	325.00	3.08
82 <= Mi <= 87	350.00	2.86
88 <= Mi <= 93	375.00	2.67
94 <= Mi <= 100	400.00	2.50
101 <= Mi <= 106	425.00	2.35
107 <= Mi <= 255	Send Message Reject (Asynch mode)	N/A

19.1.3 Wide Data Transfer Request (01, 02, 03h)

An Initiator and a Target exchange a pair of Wide Data Transfer Request messages to establish a data transfer width agreement between the two devices. The Initiator may initiate a wide data transfer negotiation at any time after the LUN has been identified. The Target initiates a wide data transfer negotiation if the Target has not negotiated with the Initiator since the last time the Target was Reset (Power-on Reset, SCSI Bus Hard Reset, or Target Reset message).

Target initiated negotiation occurs either immediately following the Command phase or immediately following the first reconnection. In either case negotiation occurs before any Data phase between the Target and the Initiator. The Target will negotiate the data transfer width agreement prior to negotiating the synchronous data transfer agreement. If a synchronous data transfer agreement is in effect when a Wide Data Transfer Request message is received, the Target will reset the synchronous agreement to asynchronous mode.

The implied data transfer width agreement remains in effect until the Target is Reset (Power-on Reset, SCSI Bus Hard Reset, or Target Reset message) or a new data transfer width agreement is negotiated. If a Reset occurs, the Target goes to eight bit mode.

Table 178: Wide Data Transfer Request

Byte	Value	Description
0	01H	Extended message
1	02H	Extended message length
2	03H	WIDE DATA TRANSFER REQUEST code
3	E	Transfer width exponent

E The Transfer Width Exponent (E) is two to the transfer width exponent bytes wide. Valid data transfer widths are 8 bits (E = 00h) and 16 bits (E = 01h). Value of E greater than 01h are reserved.

19.1.3.1 Transfer Width Negotiation started by the Initiator

If the Initiator recognizes that negotiation is required and sends a Wide Data Transfer Request message out, the Target responds by changing to the Message In phase and sending a Wide Data Transfer Request message in to the Initiator prior to transferring any additional message bytes (or any other Information phase bytes) from the Initiator. This provides an interlock during the data transfer width negotiation.

The drive responds to each Initiator requested transfer width exponent as shown in the following table.

Table 179: Initiator request/Target response

Initiator's request	Target's response	Target Data Transfer Width
Ei = 00h	Et = 00h	8 Bit Data Transfers
Ei > 00h	Et = 01h	16 Bit Data Transfers

If after the Target's response above the Initiator asserts the ATN signal and the first message received is either a Message Parity Error or a Message Reject message, the Target negates the data transfer width agreement and goes to 8 bits mode. For the Message Parity Error case the implied data transfer width agreement is reinstated if the Target successfully retransmits the Wide Data Transfer Request message to the Initiator. For any other message the Target completes negotiation and goes to the negotiated data transfer width.

19.1.3.2 Transfer Width Negotiation started by the Target

If the Target recognizes that negotiation is required, the Target sends a Wide Data Transfer Request message to the Initiator with the transfer width exponent equal to 1 (E = 01h). The Initiator must respond by asserting the ATN signal prior to its release of ACK for the REQ/ACK handshake of the last byte of the Wide Data Transfer Request message. This provides an interlock during the wide data transfer negotiation. If the Initiator does not assert the ATN signal, the Target goes to 8 bit mode. If the Initiator does assert the ATN signal, the Target changes to the Message Out phase and receives a message from the Initiator.

If the first message received is a Wide Data Transfer Request message, the Target establishes the new data transfer mode. The drive interprets the Initiator corresponding transfer width exponent as shown in the following table.

Table 180: Target request to Initiator

Initiator request	Target Data Transfer Width
Ei = 00h	8 bit data transfers
Ei = 01h	16 bit data transfers
Ei > 01h	Send Message Reject (8 bit data transfer)

Note: If the corresponding transfer width exponent received from the Initiator indicates a data transfer width that is greater than 16 bits (E > 01h), the Target sends a Message Reject message to the Initiator to indicate 8-bit data transfer mode.

If the first message received from the Initiator is either a Message Parity Error or a Message Reject message, the Target goes to 8-bit data transfer mode. In the case of a Message Parity Error, the wide data transfer negotiation is restarted if the Target successfully retransmits the Wide Data Transfer Request message to the Initiator.

If the first message received from the Initiator is any other message, the Target goes to 8-bit data transfer mode. The Target assumes that the Initiator does not support wide data transfer and does not attempt to renegotiate with this Initiator.

The Target does not consider the implied agreement for wide data transfer operation to exist until the Target leaves the Message Out phase, implying that no parity error was detected. If the Target detects a parity error while attempting to receive the message from the Initiator, the Target goes to 8-bit data transfer mode. The Target will attempt to resume the wide data transfer negotiation by retrying the Message Out phase.

Note: If during the Message In phase of negotiations, either Target or Initiator started, ATN is asserted prior to transmission of the last byte of the message and the message is not Message Parity or Message Reject, the Target goes to 8-bit data transfer mode.

19.1.4 Parallel Protocol Request (01, 06, 04h)

Parallel Protocol Request messages are used to negotiate a synchronous data transfer agreement and a wide data transfer agreement and to set the protocol options between two SCSI devices.

Table 181: Parallel Protocol Request

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Extended Message = 01h							
1	Extended Message Length = 06h							
2	Parallel Protocol Request = 04h							
3	Transfer Period Factor							
4	Reserved = 00h							
5	REQ/ACK Offset							
6	Transfer Width Exponent							
7	PCOMP_EN	RTI	RD_STRM	WR_FLOW	HOLD_MCS	QAS_REQ	DT_REQ	IU_REQ

PARALLEL PROTOCOL REQUEST messages are used to negotiate a synchronous data transfer agreement and a wide data transfer agreement and to set the protocol options between the initiator and the drive.

- **Transfer Period Factor** selects the transfer period and determines the timing values for the transfer rate. When both DT_REQ and IU_REQ are set to one, the following values are used. For the values larger than 9 is received from the initiator when both DT_REQ and IU_REQ are set to one, the drive responds with these bits set to zero.

Table 182: Initiator Request/Target Response (DT_REQ = 1, IU_REQ = 1)

Initiator Request	Target Response	Target Transfer	Transfer Rate
0 <= Mi <= 8	Mt = 08h	6.25 nSec	Fast-160
9 <= Mi <= 9	Mt = 09h	12.5 nSec	Fast-80

If DT_REQ is set one and IU_REQ is set to zero, the following values are used.

Table 183: Initiator Request/Target Response (DT_REQ = 1, IU_REQ = 0)

Initiator Request	Target Response	Target Transfer	Transfer Rate
0 <= Mi <= 9	Mt = 09h	12.5 nSec	Fast-80
10 <= Mi <= 10	Mt = 0Ah	25 nSec	Fast-40
11 <= Mi <= 12	Mt = 0Ch	50 nSec	Fast-20
13 <= Mi <= 25	Mt = 19h	100 nSec	Fast-10
26 <= Mi <= 255	Mt = 19h	(Asynch Mode)	Asynch

If both DT_REQ and IU_REQ is set to zero, the following values are used

Table 184: Initiator Request/Target Response (DT_REQ = 0, IU_REQ = 0)

Initiator Request	Target Response	Target Transfer	Transfer Rate
0 <= Mi <= 10	Mt = 0Ah	25 nSec	Fast-40
11 <= Mi <= 12	Mt = 0Ch	50 nSec	Fast-20
13 <= Mi <= 25	Mt = 19h	100 nSec	Fast-10
26 <= Mi <= 50	Mt = 32h	200 nSec	Fast-5
51 <= Mi <= 255	Mt = 32h	(Asynch Mode)	Asynch

- **REQ/ACK** Offset determines the maximum number of REQs allowed to be outstanding before a corresponding ACK is received at the drive during synchronous or paced transfers.
  - **The REQ/ACK** Offset value is chosen to prevent overflow conditions in the reception buffer and offset counter of the drive. The drive supports maximum offset of 127 (7Fh). A REQ/ACK Offset value of zero indicates asynchronous data transfer mode and that the Transfer Period Factor and the protocol options bits except QAS_REQ will be ignored.
- **Transfer Wide Exponent** defines the transfer width to be used during DATA IN and DATA OUT phases. If any of the protocol options bits other than QAS_REQ are set to one, then only wide transfer agreements are valid. The following values are supported.

- 00h: 8 bit data bus (Narrow transfer agreement).
- 01h: 16 bit data bus (Wide transfer agreement).

- **PCOMP_EN** (Precompensation Enable) is used to negotiate if the pre-compensation is enabled on all signals transmitted during DT DATA phases. The drive supports PCOMP_EN.
- **RTI** (Retain Training Information) is used to negotiate if the saving of paced data transfer training information is made so that the retraining is not necessary on each connection. The drive support RTI.
- **RD_STRM** (Read Streaming and Read Flow Control Enable) is used to negotiate if read streaming and read flow control are enabled. The drive supports RD_STRM.
- **WR_FLOW** (Write Flow Control Enable) is used to negotiate if write flow control is enabled during write streaming. The drive supports WR_FLOW.
- **HOLD_MCS** (Hold Margin Control Settings) is used to negotiate if any margin control settings which has been set with the margin control subpage of the port control mode page is retained.
- **QAS_REQ** (Quick Arbitration and Selection Enable Request) is used to negotiate if QAS is enabled. The drive supports QAS when IU_REQ is negotiated to be effective.
- **DT_REQ** (DT Clocking Enable Request) is used to negotiate if DT DATA phase is enabled. The drive supports DT_REQ.
- **IU_REQ** (Information Unit Enable Request) is used to negotiate if information unit transfer is enabled. The drive supports IU_REQ.

Table 185: Bit position table for Byte 7 of Parallel Protocol Request

Bit							
7	**6**	**5**	**4**	**3**	**2**	**1**	**0**
Reserved					**QAS_REQ**	**DT_REQ**	**IU_REQ**

QAS_REQ (Quick Arbitrate and Select) is not supported. The bit should be zero.

DT_REQ A bit of zero for DT_REQ (Double Transition Enable Request) indicates that DT DATA phases are to be disabled when received from the originating SCSI device and that DT DATA phases are not supported when received from the responding SCSI device.

A DT_REQ bit of one indicates that DT DATA phases are to be enabled when received from the originating SCSI device and that DT DATA phases are supported when received from the responding SCSI device.

IU_REQ (Information Unit Request) is not supported. The bit should be 0.

19.1.5 Save Data Pointer (02)

This message is sent from the drive to direct the Initiator to copy the active data pointer to the saved data pointer. The SAVE DATA POINTER message is only sent if the Initiator has previously indicated the ability to accommodate disconnection and reconnection via the IDENTIFY message.

The drive will send the SAVE DATA POINTER message to the Initiator prior to sending a DISCONNECT message to the Initiator if a data phase has occurred and another data phase is required to successfully complete the command.

19.1.6 Restore Pointers (03)

This message is sent from the drive to direct an Initiator to copy the most recently saved pointers to the corresponding command, data, and status pointers. Command and status pointers should be restored to the beginning of the present command and status areas. The data pointer should be restored to the value at the beginning of the data area in the absence of a SAVE DATA POINTER message or to the value at the point at which the last SAVE DATA POINTER message occurred. Refer to Section

19.4, “SCSI Bus Related Error Handling protocol” on page 244.

19.1.7 Disconnect (04)

This message is sent from the drive to inform an Initiator that the present connection is going to be broken. A later reconnect will be required in order to complete the current command. The disconnection serves to free the SCSI bus while the drive performs a relatively long operation that does not require the bus. These messages are sent only if the Initiator previously indicated (via the IDENTIFY message) the ability to accommodate disconnection and reconnection.

The DISCONNECT message may also be sent from the Initiator to the drive to disconnect from the SCSI bus. The drive does not support the DISCONNECT message from the Initiator. And it always responds by sending MESSAGE REJECT message to the Initiator.

19.1.8 Initiator Detected Error (05)

This message is sent from an Initiator to inform the drive that an error has been detected that does not preclude the drive from retrying the previous COMMAND, DATA, and STATUS phase. The source of the error may be either related to previous activities on the SCSI bus or may be internal to the Initiator and unrelated to any previous SCSI bus activity.

If the Initiator intends to send this message, the Initiator must assert the ATN signal prior to its release of ACK for the last byte transferred in the information phase that is to be retried. This provides an interlock so the drive can determine which information phase to retry.

After receiving this message the drive may retry the previous phase by sending a RESTORE POINTERS message to the Initiator and then repeating the previous COMMAND, DATA, or STATUS phase.

After receiving this message the drive may retry the MESSAGE IN phase by switching to the MESSAGE IN phase with asserting REQ and repeating the previous MESSAGE IN phase.

19.1.9 Abort Task Set (06)

This message is sent from the Initiator to direct the drive to clear the present operation for this Initiator and logical unit including queued command(s). If a logical unit has been identified, then all pending data and status for the issuing Initiator and this logical unit will be cleared and the drive will go to the BUS FREE phase. Pending data and status for other logical unit and Initiators will not be cleared. If a logical unit has not been identified, the drive will go to the BUS FREE phase without affecting an operation on any logical unit for this or any other Initiator. In either case no status or ending message will be sent to the Initiator for this operation. It is not an error to send the ABORT message to a logical unit that is not currently performing an operation for the Initiator.

Note: It is permissible for an Initiator to select the drive/LUN after the drive has disconnected from the Initiator for the purpose of sending an IDENTIFY message followed by an ABORT message. This will abort the command on the specified logical unit.

19.1.10 Message Reject (07)

This message is sent from either the Initiator or the drive to indicate that the last message received was inappropriate or has not been implemented.

If the Initiator intends to send this message, the Initiator must assert the ATN signal prior to its release of ACK for the REQ/ACK handshake of the message byte that is to be rejected. This provides an interlock so the drive can determine which message is rejected.

If the drive intends to send this message, the drive will change to the MESSAGE IN phase and send the MESSAGE REJECT message to the Initiator prior to transferring any additional message bytes (or any other information phase bytes) from the Initiator regardless of ATN signal. This provides an interlock so the Initiator can determine which message is rejected. After the drive sends a MESSAGE REJECT message and if ATN signal is still asserted, it shall return to the MESSAGE OUT phase. The subsequent MESSAGE OUT phase shall begin with the first byte of a message

19.1.11 No Operation (08)

This message is sent from the Initiator to the drive when the Initiator does not currently have any other valid message to send.

This message is ignored by the drive and will not affect any operation.

19.1.12 Message Parity Error (09)

This message is sent from the Initiator to inform the drive that the last message byte received had a parity error.

If the Initiator intends to send this message, the Initiator must assert the ATN signal prior to its release of ACK for the REQ/ACK handshake of the message byte that has the parity error. This provides an interlock so the drive can determine which message byte has the parity error.

If the drive receives this message under any other circumstance, the drive will change to BUS FREE to signal a catastrophic error. After receiving this message, the drive will retry sending the previous message to the Initiator.

19.1.13 Linked Task Complete (0A)

The drive sends this message to the Initiator to indicate that execution of a linked command (with flag bit equal to zero) has completed and that valid status has been sent to the Initiator. After successfully sending this message, the drive goes to COMMAND phase to receive the next command.

19.1.14 Linked Task Complete With Flag (0B)

The drive sends this message to the Initiator to indicate that the execution of a linked command with flag bit set to one has completed and that valid status has been sent to the Initiator. After successfully sending this message, the drive goes to COMMAND phase to receive the next command.

19.1.15 Target Reset (0C)

This message is sent from an Initiator to direct the drive to clear all current commands. This message forces a hard reset condition, which will reset the drive to an initial state with no operations pending for any Initiator. After receiving this message the drive will go to the BUS FREE phase.

19.1.16 Abort Task (0D)

When the Target successfully receives this message, it clears the current I/O process and goes to Bus Free. If the Target has already started execution of an I/O process, the execution will be halted.

Pending status, data, and commands for other active or queued I/O processes shall not be affected.

19.1.17 Clear Task Set (0E)

All I/O processes for all Initiators shall be cleared. All active I/O processes shall be terminated. The Target shall go to the Bus Free phase following successful receipt of this message.

19.1.18 Logical Unit Reset (17)

This message is sent from an Initiator to direct the drive to clear all current commands and enter an initialized state with no operations pending for any Initiator. This message forces a reset similar to the Target Reset message, with the exception that initiator negotiated parameters (like interfaces speed and related options) are not reset, but are preserved.

19.1.19 Queue Tag messages (20H, 21H, 22H)

Table 186: Queue Tag messages

Byte	Value	Description
0	20H	Simple Queue
	21H	Head of Queue
	22H	Ordered
1	XXh	Queue Tag

Queue Tag messages are used to specify an identifier called a Queue Tag for an I/O process that establishes the I_T_L_Q nexus. The queue tag field is an 8-bit unsigned integer assigned by the Initiator during an initial connection. The Queue Tag for every I/O process for each I_T_L nexus must be unique. If the Target receives a Queue Tag that is currently in use for the I_T_L nexus, it will respond as “Incorrect Initiator Response”. A Queue Tag becomes available for reassignment when the I/O process ends. The numeric value of a Queue Tag has no effect on the order of execution.

Whenever an Initiator connects to the Target, the appropriate Queue Tag message must be sent immediately following the Identify message and within the same MESSAGE OUT phase to establish the I_T_L_Q nexus for the I/O process.

Whenever the Target reconnects to an Initiator to continue a tagged I/O process, the Simple message is sent immediately following the Identify and within the same MESSAGE IN phase to revive the I_T_L_Q nexus for the I/O process.

19.1.19.1 Simple (20h)

The Simple message specifies that the current I/O process be placed in the command queue. The order of execution with respect to other I/O processes received with Simple messages is up to the discretion of the Target. The Target will send Simple messages after reselection for I/O processes that were received with either Simple, Ordered, or Head of Queue messages.

19.1.19.2 Head Of Queue (21h)

Commands with this tag should be inserted into the head of the queue. When a command is being executed, this tagged command will be inserted to the head of queue to be executed after the command being currently executed. The previous executed command will not be terminated by this tagged command. This tagged command will wait until the previous command is completed. If plural head-of-queue tagged commands are received, those command will be executed in LIFO (Last in First out) order. This will be ignored if the WCE bit is set.

19.1.19.3 Ordered (22h)

This tagged command is executed in the order received. All commands received before this command should be executed before this command and all commands received after this commands should be executed after this command.

19.1.20 Ignore Wide Residue

Table 187: Ignore Wide Residue message format

Byte	Value	Description
0	23H	Ignore Wide Residue message
1	01H	Ignore

The Ignore Wide Residue message is sent from the Target to indicate that the number of valid bytes sent during the last REQ/ACK handshake of a DATA IN phase is less than the negotiated transfer width. The ignore field (always = 01h) indicates that one byte (data bits 8-15) should be ignored. This message is sent immediately after the DATA IN phase and prior to any other messages. Even though a byte is invalid, its corresponding parity bit is valid for the value transferred.

19.1.21 Identify (80 - FF)

This message is set by either the Initiator or the drive to establish the logical path connection between the two devices.

The IDENTIFY message is defined as follows:

Bit 7 This bit is always set to one to distinguish the IDENTIFY message from other messages.

Bit 6 This bit is only set to one by the Initiator to grant the drive the privilege of disconnecting. If this bit is zero, the drive will not disconnect unless the Initiator instructs the drive to disconnect by sending a DISCONNECT message to the drive. This bit is set to zero when the drive sends an IDENTIFY message to the Initiator.

Bits 5-0 These bits specify the logical unit number (LUN).

Only one LUN may be identified for any one selection sequence. If the drive receives an IDENTIFY message with a new LUN after the LUN had previously been identified, the drive will go to the BUS FREE phase to signal a catastrophic error. The Initiator may send more than one Identify message during a selection sequence in order to toggle disconnect/reconnect permission if the specified LUN remains the same.

When the IDENTIFY message is sent from the drive to the Initiator during reconnection, an implied RESTORE POINTERS message must be performed by the Initiator.

19.2 Supported message functions

The implementation of the supported messages will also include the following functions.

- Retry SCSI Command or STATUS phase
  The retry will be caused by the following error condition.
  - The drive detected SCSI bus parity error (Command phase)
  - The drive receives INITIATOR DETECTED ERROR MESSAGE during or at the conclusion of an information transfer phase (Command Data Out or Status Phase)

  Note: The Initiator may send the INITIATOR DETECTED ERROR message as a result of an Initiator detected SCSI Bus parity error or an internal error.
- Retry MESSAGE IN phase
  - The retry will be caused by the receipt of a MESSAGE PARITY ERROR message immediately following a MESSAGE IN phase.

  Note: The Initiator may send the MESSAGE PARITY ERROR message as a result of an Initiator detected SCSI Bus parity error during the Message In phase.
- Receipt of multiple Identify messages
  - The Initiator is allowed to send multiple IDENTIFY messages out in order to toggle the disconnect/reconnect permission bit. This may be used to selectively enable or disable disconnect/reconnect permission during portions of a command. Note that this function does not affect the operation of the Forced Disconnect function.
- MESSAGE REJECT during Target Disconnection
  - If the Initiator rejects the SAVE DATA POINTER message, the drive will disable disconnect/reconnect permission. This is equivalent to receiving an IDENTIFY message with bit 6 equal to zero. This will cause the drive to inhibit the pending disconnection.
  - If the Initiator rejects the DISCONNECT message, the drive will not disconnect but may attempt to disconnect at a later time. This function may be used to selectively disable disconnection during portions of a command.

19.3 Attention condition

The attention condition allows an Initiator to inform the drive that a MESSAGE OUT phase is desired. The Initiator may create the attention condition by asserting the ATN signal at any time except during the ARBITRATION or BUS FREE phases.

The Initiator must create the attention condition by asserting the ATN signal at least two deskew delays before releasing ACK for the last byte transferred in a bus phase to guarantee that the attention condition will be honored before transition to a new bus phase. This will guarantee a predictable drive response to a message received during the MESSAGE OUT phase for this attention condition. If the ATN signal is asserted later, it may be honored in the current bus phase or the next bus phase and then may not result in the expected action.

After the Initiator asserts the ATN signal, the drive will respond with the MESSAGE OUT phase as follows:

Current phase	Response
COMMAND	MESSAGE OUT phase will occur after part or all of the Command Descriptor Block has been transferred to the drive. The Initiator must continue REQ/ACK handshakes during the COMMAND phase until the drive enters the MESSAGE OUT phase.
DATA	The Message Out phase will occur after part or all of the data bytes have been transferred and not necessarily on a logical block boundary. The Initiator must continue REQ/ACK handshakes (asynchronous transfer) until it detects the phase change. **Note:** In synchronous transfer the Initiator must continue sending ACK pulses to reach an offset of zero.
STATUS	The Message Out phase will occur after the REQ/ACK handshake of the status byte has been completed.
MESSAGE IN	The Message Out phase will occur before the drive sends another message.
SELECTION	If ATN occurs during a Selection phase and before the Initiator releases the BSY signal, the Message Out phase will occur immediately after that Selection phase.
RESELECTION	The MESSAGE OUT phase will occur after the drive has sent its IDENTIFY message for that RESELECTION phase. (The drive first tries to complete the reselection.)

The Initiator must keep the ATN signal asserted if more than one message byte is to be transferred during the MESSAGE Out phase. The drive will process each message byte (multiple bytes for an extended message) prior to receiving the next message from the Initiator. The drive will continue to handshake and process byte(s) in the MESSAGE OUT phase until ATN goes false unless one of the following conditions occurs:

1. The drive receives an illegal or inappropriate message and goes to the MESSAGE IN phase to send a MESSAGE REJECT message.
2. The drive detects a catastrophic error condition and goes to the BUS FREE phase.

20.0 Additional information

This chapter provides additional information or descriptions of various functions, features, or operating models supported by the Target that are not fully described in previous chapters.

20.1 SCSI Protocol

There are various operating conditions that prevent the Target from executing a SCSI command. This section describes each of these operating conditions and their relative priority.

20.1.1 Priority of SCSI Status Byte Reporting

After establishing the I_T_L nexus or I_T_L_Q nexus the Target must first determine whether command execution is allowed. Execution is deferred until a later time if the command must be added to the command queue. Execution may also be prevented by an internal Target condition that requires the reporting of a Check Condition, Queue Full, Busy, or Reservation Conflict Status. There are several different internal conditions to be active at the same time. The order in which the Target checks for each of these conditions determines their priority (highest priority first) as follows:

1. Check Condition status for invalid Logical Unit Number. (See Section 20.1.2, “Invalid LUN in Identify Message” on page 259)
2. Check Condition status for Incorrect Initiator Connection (See Section20.1.3, “Incorrect Initiator Connection” on page 238)
3. Check Condition status for Unit Attention Condition (See Section 20.1.5, “Unit Attention Condition” on page 240
4. Busy Status or Queue Full Status (See 20.1.4, “Command Processing During Execution of Active I/O Process” on page 238
5. Check Condition status for Deferred Error Condition (See Section 20.1.8, “Deferred Error Condition” on page 241)
6. Check Condition status during Startup and Format operations (See Section “20.1.6, “Command processing during startup and format operations” on page 241)
7. Reservation Conflict status (See Section 20.1.10, “Command Processing while Reserved” on page 249)
8. Check Condition status for invalid command opcode
9. Check Condition status for invalid command descriptor block

The highest priority internal condition that prevents command execution is reported by the Target provided there is no bus error.

For all Check Conditions Sense data is built by the target provided a valid LUN address is known. Sense data is cleared by the Target upon receipt of any subsequent command to the LUN from the initiator receiving the Check Condition.

20.1.2 Invalid LUN in Identify Message

There are three different circumstances defined within the SCSI protocol when the response to an invalid LUN will occur. Each of these result in a different response.

20.1.2.1 Case 1 - Selection message sequence with Inquiry command

The INQUIRY command is a special case in SCSI. It is used to configure the bus when the drive ID's and LUN's are not known. The proper response is to return the inquiry data with a peripheral drive type of 1Fh which indicates that the specified LUN is not supported.

20.1.2.2 Case 2 - Selection message sequence with any other command

Any other commands except REQUEST SENSE return CHECK CONDITION status when an invalid LUN is specified in the message sequence following selection. In response to a REQUEST SENSE command the target shall return sense data. The sense key shall be set to ILLEGAL REQUEST and the additional sense code shall be set to LOGICAL UNIT NOT SUPPORTED.

20.1.2.3 Case 3 - After selection message sequence

It is permissible for the initiator to issue multiple IDENTIFY messages during a single command sequence provided the LUN remains the same. If the LUN is altered, the drive goes to a Bus Free Phase.

20.1.3 Incorrect Initiator Connection

Incorrect Initiator Connection error is reported if any of the following conditions occur:

- an Initiator attempts to establish an I_T_L nexus when an I/O process (either queued or active) with an I_T_L nexus already exists from a previous connection with the same Initiator.
- an Initiator attempts to establish an I_T_L_Q nexus when an I_T_L nexus already exists from a previous connection with the same Initiator.
- an Initiator attempts to establish an I_T_L nexus when an I_T_L_Q nexus already exists from a previous connection with the same Initiator.

  Note: It is not an Incorrect Initiator Connection to send a command without a Queue tag message when sense is pending on the logical unit for the Initiator that issues the REQUEST SENSE command. (If the command is not REQUEST SENSE or INQUIRY, sense data is cleared upon receipt of the command.)
- an Initiator attempts to establish an I_T_L_Q nexus when an I/O process (either queued or active) when an I_T_L_Q nexus already exists from a previous connection with the same Initiator.

If any of the above errors occur, all queued I/O processes and active I/O processes associated with the issuing Initiator on the specified logical unit are terminated. The current I/O process is ended with a **Check Condition** status, the sense key is set to *Aborted Command*, and the additional sense code is set to *Overlapped Commands Attempted*. Status is only returned for the current I/O process.

20.1.4 Command Processing During Execution of Active I/O Process

When the Target is not executing any active I/O processes, a new I/O process is permitted to execute (unless execution is prevented by another internal Target condition listed in Section “Priority of SCSI Status Byte Reporting” on page).

If an active I/O process does exist when the Target receives a new command, then the Target determines if

- Check Condition Status with Sense Key = Aborted Command is returned for an Overlapped Commands Attempted error
- the command is permitted to execute
- the command is added to the command queue
- Queue Full Status is returned
- Busy Status is returned

If an active I/O process does exist when the Target receives a new command, the Target determines how the new command should be handled based on the following rules:

- Check Condition Status is returned with Sense Key set to Aborted Command and the additional sense code set to Overlapped Commands Attempted
  - See Section 20.1.3, “Incorrect Initiator Connection” on page 238.
- the command is permitted to execute if the command is an Inquiry or Request Sense command
- Check Condition Status is returned with Sense Key set to Logical Unit Not Ready if the startup operation or format operation is an active process.
- the command is permitted to execute if the conditions to execute concurrently are met. (See Section 20.5, “Concurrent I/O Process” on page 251)
- the command is added to the command queue for an I_T_L nexus if all the following conditions exist:

- no Queue Tag message was received during the connection which established the I/O process
- disconnection is allowed for the current I/O process
- there is no queued I/O process or active I/O process corresponding to the I_T_L nexus for the current I/O process
- the command is not linked to a previous command

• the command is added to the command queue for an I_T_L_Q nexus if the following conditions exist:
  - a Queue Tag message was received during the connection which established the I/O process
  - Tagged Queuing is enabled (DQue = 0)
  - an I/O process (either active or queued) exists at the Target for this Initiator
  - disconnection is allowed for the current I/O process
  - there is no queued I/O process or active I/O process corresponding to the I_T_L_Q nexus for the current I/O process
  - the command is not linked to a previous command.

• Queue Full Status is returned if any one of the following conditions exists:
  - the command would otherwise be queued (according to the rules described above) but the command queue is full and all slots are utilized
  - the command would otherwise be queued (according to the rules described above) but all of the available command queue slots not reserved for use by another initiator are utilized
  - Tagged Queuing is enabled (DQue = 0) and a Format Unit command was previously queued but has not yet begun execution
  - Tagged Queuing is enabled (DQue = 0) and a Unit start command was previously queued but has not yet begun execution

• Busy Status is returned if any of the following conditions exists:
  - Tagged Queuing is disabled (DQue = 1) and a Format Unit command was previously queued but has not yet begun execution
  - Tagged Queuing is disabled (DQue = 1) and a Unit start command was previously queued but has not yet begun execution
  - the command would otherwise be queued (according to the rules described above) but disconnection is not allowed for the current I/O process.

If a command is queued, command execution may still be prevented at a later time when the command is dequeued to become an active I/O process. This occurs if command execution is prevented at the time the command is dequeued by another internal Target condition listed in Section 20.1.1, “Priority of SCSI Status Byte Reporting” on page 237.

20.1.5 Unit Attention Condition

The drive will generate a unit attention condition for each Initiator whenever these conditions are in effect:

- The drive has been reset. This includes Power On Reset, SCSI Bus Reset, SCSI TARGET RESET message.
- Another Initiator has changed the Mode parameters in effect for this Initiator.
- Another Initiator issued a Log Select, thus clearing log parameters.
- The microcode has been changed. WRITE BUFFER command has been executed to download microcode. In this case a unit attention condition is generated for all Initiators except the one that issued the command.
- Commands are cleared by another Initiator. This condition is generated against the Initiator that has queued commands if
  - a Clear Queue Message is received
  - a Contingent Allegiance Condition is cleared when QERR (in Mode Page 0A) is 1
  - a DQue is set to 1 while queued commands exist
- SCSI Bus transceiver mode change (LVD <==> Single Ended).
- Another Initiator preempted Reservations or Registrations.
- A Predictive Failure Analysis threshold has been reached.

The unit attention condition persists for each Initiator until that Initiator clears the condition as described in the following paragraphs.

If the drive receives a command from each Initiator before reporting a **Check Condition** status for a pending unit attention condition for that Initiator, the response of the drive varies with the command as follows:

INQUIRY	The drive executes the command with **Good** status and preserves the unit attention condition.
REQUEST SENSE	If the drive has an available pending sense data for the Initiator, the drive sends the pending sense data and preserves the unit attention condition for the Initiator. If the drive does not have an available pending sense data for the Initiator, the drive sends sense data for the unit attention condition and clears the unit attention condition for the Initiator.
ALL OTHER	The drive terminates the command with a **Check Condition** status and preserves the unit attention condition.

If the drive receives a command from each Initiator after reporting a **Check Condition** status for a pending unit attention condition for that Initiator, the response varies with the command as follows:

REQUEST SENSE	The drive sends the sense data for a pending unit attention condition, returns **Good** status, and clears the unit attention condition for the Initiator.
ALL OTHER	The drive executes the command with **Good** status and clears the unit attention condition, unless another unit attention condition exists. The sense data for the unit attention condition is then lost.

20.1.6 Command processing during startup and format operations

If the Target receives a command from an Initiator while the Target is executing a startup or format operation, the response of the Target varies with the command as follows:

INQUIRY The drive sends inquiry data and returns appropriate status.

REQUEST SENSE **Executes the command, returns a Sense key of NOT READY and an Additional Sense Code of LOGICAL UNIT NOT READY and returns GOOD STATUS.**

The Additional Sense Code Qualifier that is returned depends on type of I/O processes that are active:

For the START/UNIT STOP and the Auto-start operation, the qualifier returned is LOGICAL UNIT IS IN PROCESS OF BECOMING READY. For the FORMAT UNIT command, the qualifier returned is LOGICAL UNIT NOT READY, FORMAT IN PROGRESS, and the Sense key specific bytes are set to return the progress indication.

REPORT LUNS The drive send REPORT LUNS data and appropriate status.

ALL OTHER The drive terminates the command with CHECK CONDITION status. The Sense data generated is described in Request Sense above.

20.1.7 Internal Error Condition

The Target generates an Internal Error condition for all Initiators when an internally initiated operation ends with an unrecoverable error, that is, the startup sequence for Auto Start enabled terminates after the SCSI bus has been enabled and prior to completion of the bring-up sequence.

An Internal Error condition causes Sense data to be generated and saved for all Initiators. The Error Code field of the Sense is set for a Current Error (70h) and the Sense Key is set to HARDWARE ERROR. Recovered errors are not reported.

The Internal Error condition persists for each Initiator until that Initiator clears the condition from the logical unit as described below. Several commands are handled as special cases during an Internal Error condition. These cases are also discussed.

If the Target receives a command from an Initiator while an Internal Error condition exists for that Initiator, the response of the Target varies with the command as follows:

INQUIRY The drive executes the command with GOOD status and does not clear the Internal Error condition.

REQUEST SENSE The drive executes the command, returns the sense data generated by the Internal Error condition, returns Good Status, and clears the Internal Error condition for that Initiator.

ALL OTHER The drive terminates the command with a CHECK CONDITION status and clears the Internal Error condition.

20.1.8 Deferred Error Condition

Error code (71h) of sense data indicates that the Check Condition status returned is the result of an error or exception condition that occurred during execution of a previous command for which Good status has already been returned.

The drive creates an Deferred Error condition when

- Execution of a Format Unit command with the immediate bit of one ends with an error.
- Execution of a Write command with WCE (Write Cache Enable) bit of one ends with an error.

20.1.9 Degraded Mode

There are certain errors or conditions which may impair the ability of the drive to function normally. Rather than fail hard the drive is designed to be as responsive as possible. Also, in most cases, some action on the part of the initiator may be used to restore normal operation. This mode of limited operation is called Degraded Mode.

There are 3 conditions in the Degraded Mode:

- Spindle Motor Degrade which is caused by one of the following conditions:
  - Spindle Motor is not started by the option jumper setting (Disable Auto Spin Up)
  - Spindle Motor is delayed from spinning up by the option jumper setting (Auto Start Delay)
  - Spindle Motor was started (by POR or Unit Start command) and the Target is under Self Configuration.
  - Spindle Motor Failed to start.
  - Spindle Motor was stopped by Unit Stop command after the Target successfully completed the Self Configuration.
- Self Configuration Failure Degraded which is caused by one of the following conditions:
  - RAM Code, Configuration Sector Read Failure
  - RAM Code, Configuration Sector Revision Mismatch
- Format Command Failure Degraded. This condition is caused when Format Unit command failed or was interrupted abnormally (Mode Page 0, byte 5, bit 4 FDD controls Format Degraded mode)

20.1.9.1 Response to SCSI Command in Degraded Mode - Disable Auto Start

The tables on the following pages show the degraded mode status with acceptable commands and additional sense codes

Table 188: Spindle Motor Degraded Mode - Disable Auto Start

Command (w/Option)	**Response**
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed and Check Condition is returned with Sense Key 05h (Illegal Request) ASC/ASCQ 2400h (Invalid Field in CDB)
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)
Start Stop Unit (Start)	Executed - Success: Good Status is returned. Motor Degraded Mode is cleared - Spindle Motor Start Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail) - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM Code NOT load)

Start Stop Unit (Stop)	Executed. Good Status is returned. Motor Degraded Mode is NOT cleared
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)

20.1.9.2 Response to SCSI Command in Degraded Mode - Auto Start Delay/ Spinning Up

Table 189: Spindle Motor Degraded Mode - Auto Start Delay/Spinning Up

Command (w/Option)	**Response**
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 0401h (In Process of Becoming Ready)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed and Check Condition is returned with Sense Key 05h (Illegal Request) ASC/ASCQ 2400h (Invalid Field in CDB)
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0401h (In Process of Becoming Ready)
Start Stop Unit (Start)	Executed - Success: Good Status is returned. Motor Degraded Mode is cleared - Spindle Motor Start Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail) - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM Code NOT load)
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0401h (In Process of Becoming Ready)

20.1.9.3 Response to SCSI Command in Degraded Mode - Spindle Start Failure

Table 190: Spindle Motor Degraded Mode - Spindle Start Failure

Command (w/Option)	Response
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed and Check Condition is returned with Sense Key 05h (Illegal Request) ASC/ASCQ 2400h (Invalid Field in CDB)
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail)
Start Stop Unit (Start)	Executed - Success: Good Status is returned. Motor Degraded Mode is cleared - Spindle Motor Start Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail) - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM Code NOT load)
Start Stop Unit (Stop)	Executed. Good Status is returned. Motor Degraded Mode is NOT cleared
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail)

20.1.9.4 Response to SCSI Command in Degraded Mode - Spindle Stopped by Unit Stop Command

Table 191: Spindle Motor Degraded Mode - Spindle Stopped by Unit Stop Command

Command (w/Option)	Response
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)
Start Stop Unit (Start)	Executed - Success: Good Status is returned. Motor Degraded Mode is cleared - Spindle Motor Start Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail) - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)
Start Stop Unit (Stop)	Executed. Good Status is returned. Motor Degraded Mode is NOT cleared
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 02h (Not Ready) ASC/ASCQ 0402h (Initialize Command Required)

20.1.9.5 Self Configuration Failure Degraded Mode

Table 192: Self Configuration Failure Degraded Mode

Command (w/Option)	Response
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed and Check Condition is returned with Sense Key 05h (Illegal Request) ASC/ASCQ 2400h (Invalid Field in CDB)
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)
Start Stop Unit (Start)	Executed - Success: Good Status is returned. Motor Degraded Mode is cleared - Spindle Motor Start Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 0400h (Start Spindle Motor Fail) - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)
Write Buffer (Download and Save)	Executed. - Success: Good Status is returned. Motor Degraded Mode is cleared - Self Configuration Failure: Check Condition with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 02h (Not Ready) ASC/ASCQ 4080h (Diag Fail- Bring up Fail) Sense Key 02h (Not Ready) ASC/ASCQ 4085h (Diag Fail-RAM code NOT load)

20.1.9.6 Format Command Failure Degraded Mode

Table 193: Format Command Failure Degraded Mode

Command (w/Option)	**Response**
Request Sense	Executed. The Target may return Sense Key 02h (Not Ready) ASC/ASCQ 3100h (Format Corrupted) Sense Key 03h (Medium Error) ASC/ASCQ 3100h (Format Corrupted)
Inquiry (EVPD=0)	Executed
Inquiry (EVPD=1)	Executed
Test Unit Ready	Executed and Check Condition is returned with Sense Key 02h (Not Ready) ASC/ASCQ 3100h (Format Corrupted)
Format Unit	Executed - Success: Good Status is returned. Format Degraded Mode is cleared - Failure: Check Condition Status is returned and Format Degraded Mode is NOT cleared.
Other Commands	Not Executed. Check Condition Status is returned with Sense Key 03h (Medium Error) ASC/ASCQ 3100h (Format Corrupted)

Note: Mode Page 0 byte 5 bit 4 (FDD) = 0

20.1.10 Command Processing while Reserved

A logical unit is reserved after successful execution of the Reserve command. Each time a Reserve command is executed successfully, the Target records the SCSI ID of the Initiator that made the reservation and the SCSI ID of the Initiator that is to receive the reservation. This information is needed to determine whether subsequent commands should be permitted or if the Reservation Conflict Status should be reported. The Initiator that made the reservation is the Initiator that issued the Reserve command. The Initiator to receive the reservation may be either the same or a different Initiator (third-party reservation).

If the logical unit is reserved when a new command is received, the Target examines the command opcode and the SCSI ID of the issuing Initiator to determine whether a Reservation Conflict Status should be returned based on the following rules:

If the issuing Initiator is the one that made the reservation and also the one to receive the reservation, then all commands are permitted.

If the issuing Initiator is neither the one that made the reservation nor the one to receive the reservation, then

- A Request Sense or Inquiry command is permitted.
- A Release command is permitted but is ignored.
- Any other command results in a Reservation Conflict Status.

If the issuing Initiator is the one that made the reservation but is not the one to receive the reservation, then

- An Inquiry, Request Sense, Reserve, or Release command is permitted.
- Any other command results in a Reservation Conflict Status.

If the issuing Initiator is not the one that made the reservation but is the one to receive the reservation, then

- A Reserve command results in a Reservation Conflict Status.
- A Release command is permitted but is ignored.
- Any other command is permitted.

If a Reservation Conflict Status is not reported and the command is permitted, then the Target checks the next highest priority internal condition to determine whether execution is allowed. See Section 20.1.1, "Priority of SCSI Status Byte Reporting" on page 237.

20.2 Priority Commands

Certain SCSI commands always execute without returning a Busy Status or Reservation Conflict Status in response to the command. These commands are

- Inquiry
- Request Sense
- Report LUNs
- Test Unit Ready

These commands do not disconnect from the SCSI bus prior to completion. They are executed prior to attempting to complete the execution of any other pending command that has disconnected from the SCSI bus. Therefore, a second priority command cannot be received during the execution of a priority command.

These commands are never queued whether or not the command is sent with a queue tag. However, the rule for an Incorrect Initiator Connection still apply to priority commands. See Section 20.1.3, "Incorrect Initiator Connection" on page 238.

20.3 Command Queuing

When the initiator specifies that the drive shall disable command queuing, the initiator must send only untagged commands. When the initiator specifies that the target shall enable command queuing, the initiator may send either tagged or untagged command, but shall not use both at the same time.

The following commands are never queued and will be immediately executed without Bus disconnection:

- Priority Commands (i.e.: Request Sense and Inquiry)
- Commands linked to previous commands. These are defined to be part of a single I/O process. (Linked commands are always executed immediately following the previous command from the same initiator. No other Initiator's command are allowed to be executed between two linked commands.)
- Commands for which disconnection is not allowed. (These may result in a Busy Status.)
- Commands in which a SCSI bus error occurred between selection and first disconnection following the receipt of the CDB.
- Commands for an invalid LUN.
- Commands which cause an OVERLAPPED COMMANDS ATTEMPTED error (see Section 20.1.3, “Incorrect Initiator Connection” on page 238).

20.3.1 Queue Depth

Any initiator can queue at least one command at any time irrespective of the actions of any other initiators in the system. A single initiator may queue up to 128 commands, if no other initiator has more than one command in the queue, although at times this maximum may be reduced as the drive can reserve command blocks for internal use.

20.3.2 Tagged Queuing

Commands with a tag message are saved in the command queue. Queued commands will be reordered by the target defined rule. See the Section 20.4, “Command Reordering” on page 251 for details.

20.3.3 Untagged Queuing

The target supports queuing one I/O process from each initiator. If the target receives an untagged I/O process while executing an I/O process from a different initiator, the untagged I/O process may be queued.

Untagged I/O processes are treated by the target as though they were received with Simple messages for purposes of queuing.

Note: There is no guarantee that I/O processes are executed in the order they were received in a multiple initiator environment when Untagged Queuing is enabled.

20.3.4 Command Queuing Rule

Commands can be received during an active I/O process if the Bus is free. Read(6), Read (10), Write(6) and Write (10) can be active at the same time. See Section 20.5, “Concurrent I/O Process” on page 251 for details.

20.3.5 Queue Full Status

This status is returned when a Simple, ORDERED or HEAD OF QUEUE message is received and the command queue is full. The I/O process is not placed in the command queue. Since one queue element is reserved for each initiator, any untagged command that does not cause Incorrect Initiator Connection will not cause Queue Full status.

20.3.6 Device Behavior on Command Queuing

1. Initiators must send a Queue tag immediately after the Identify message in Message Out phase just after Selection. Tar-

gets send a Simple message immediately after the Identify message in Message In phase just after Reselection.
2. Each initiator can issue either a tagged command or an untagged command exclusively at the same time. Other initiators can exist which operate mutually exclusively with tagged or untagged commands
3. When DQue (Disable queue) of mode page 0Ah is 1, if an initiator issues a tagged command, the drive returns “Message Reject” message (07h) and receives that command as an untagged command.
4. Queue Tag number does not affect the order of execution.
5. If an initiator issues a command with a queue tag which is the same as the current I/O process or queued I/O process. The target returns Incorrect Initiator connection.
6. A series of linked commands are a single I/O process and are assigned the queue tag established in the initial selection. A command received with a Head-of-Queue tag message shall not suspend a series of linked commands for which the target has begun execution.
7. If DQue is changed to 1 while queued commands exist, all queued commands for the all initiators will be aborted. All future commands received from any initiator with a queue tag will be processed as untagged commands with a message reject message being returned immediately after the qtag is received by the target.

20.4 Command Reordering

Command reordering function is supported under tagged command queuing enabled (DQue = 0). The recorder feature reorders Read/Write commands in order to minimize seek time between commands. This function will improve total throughput of the drive.

20.5 Concurrent I/O Process

Concurrent command are always allowed to execute concurrently with non-priority commands. A second priority command received while a priority command is being executed is put at the head of the command queue.

- WRITE commands when another WRITE command is an active I/O process
- READ commands when another READ command is an active I/O process

When a concurrent command ends in CHECK CONDITION status, the QErr bit on the Mode Page 0Ah will determine how other active I/O processes from the same initiator for that drive will be handled.

20.6 Write Cache

If the WCE (Write cache enable) bit is 1, the drive returns Good Status and Task complete message and goes to Bus Free immediately after receiving the data of the last sector before actually writing the data onto the media.

If the drive detects an error after it returns a Good Status, the drive sets a Deferred Error (Error Code of sense data = 71h) and a following command will be returned with Check Condition and the Contingent allegiance condition is established. Under the Contingent allegiance condition all queued processes including commands from other initiators are suspended.

20.7 Automatic Rewrite/Reallocate

The target supports Auto and Recommended Reallocate for READ, WRITE, WRITE VERIFY, and VERIFY.

Automatic and Recommend Reallocate operate from within the read/write command. When an automatic reallocation occurs, the read or write command takes longer to complete.

This operation is sometimes referred to as auto-reassignment due to its similarity to the operation performed by the reassign command.

Following is a description of the target behavior for each setting of ARRE. ARRE setting affects all data errors. (No Sector Found, Data Sync Byte Errors and Data ECC Errors.)

ARRE=1: An error site determined to need rewriting or reallocation during a read is automatically rewritten or reallocated at the conclusion of the read and prior to the sending of the status. The site will be automatically rewritten or reallocated only if the data has been successfully read.

ARRE=0: An error site determined to need rewriting or reassignment during a read is recommended for rewriting or reassignment at the conclusion of the read.

The setting of the ARRE bit is checked and the target will automatically rewrite/reallocate or recommend rewrite/reassign for the following commands.

- Read
- Write

For all other commands the ARRE setting is ignored and the target will not automatically rewrite/ reallocate or recommend rewrite/reassign.

Following is a description of the target behavior for each setting of AWRE. AWRE setting effects only No Sector Found Errors on writes.

AWRE=1: An error site determined to need reassignment during a write is automatically reallocated at the conclusion of the write and prior to sending the status. The site will be automatically reallocated only if the write recovery succeeded at the conclusion of the write.

AWRE=0: An error site determined to need reassignment during a write is recommended for reassignment at the conclusion of the write.

The setting of the AWRE bit is checked and the target will automatically reallocate or recommend reassign for the following commands.

- Write(6)
- Write(10)
- Write portion of Write and Verify

For all other commands the AWRE setting is ignored and the target will not automatically reallocate or recommend reassign.

Auto/Recommend Reallocate information is communicated via the sense data returned following a command during which a site was determined to need rewriting or reassignment. The LBA returned in the sense data is the LBA that was determined to need rewriting or reassignment.

The sense data combinations with auto/recommend rewrite/reallocate are listed below.

Table 194: Sense data combinations with auto/recommend rewrite/reallocate

Key	Code	Qual	Description
1	17	01	Recovered Data with retries
1	17	06	Recovered Data without ECC - Auto Reallocated
1	17	07	Recovered Data without ECC - Recommend Reassign
1	17	09	Recovered Data without ECC - Data Rewritten
1	18	00	Recovered Data with ECC
1	18	02	Recovered Data with ECC - Auto Reallocated
1	18	05	Recovered Data with ECC - Recommend Reassign
1	18	07	Recovered Data with ECC - Data Rewritten

20.8 Segmented Caching

20.8.1 Overview

Segmented Caching divides the data buffer into several smaller buffers. Each buffer is used as Read/ Write/Read-Ahead buffer.

20.8.2 Read Ahead

The Read Ahead function consists of reading data that the Initiator has not yet requested to the drive buffer. This function is intended to improve performance for an initiator that frequently accesses sequential data with successive SCSI read commands. The Read Ahead function works when RCD (the read cache disable) bit of read cache page (page 08h) is set to zero.

The drive initiates the Read ahead function when the following conditions exist:

- RCD is 0
- Read, Verify and Write and Verify is received.
- The consecutive LBA of the requested LBA is not available in the buffer

If SCSI reset or target reset message is received, all contents of segmented buffer is flushed.

Even if an error occurs during the Read ahead, the error will not be reported to the Initiator. The data read before the error occurred will be stored as valid data by the Read Ahead function.

20.9 Multiple Initiator Systems

This section describes how the target behaves in a multiple initiator system. Up to 32 initiators may be supported at any one time.

20.9.1 Sense Data

A separate sense data area is reserved for each initiator. Each area is maintained independently. This allows a command from one initiator to complete with a CHECK CONDITION status and generate sense data without being affected by a subsequent command from a different initiator. There is no requirement for the first initiator to send a REQUEST SENSE command to retrieve the Sense Data prior to the execution of a command from a different initiator.

20.9.2 Mode Pages

A single set of Mode pages is maintained. This includes both current and saved parameters. If a MODE SELECT command is executed that updates the current parameters, a unit attention condition is generated for all initiators except the one that issued the command. See 20.1.5, “Unit Attention Condition” on page 240 for more information.

20.10 Reselection Time-out

If reselection fails, it will be retried one or more times depending on the drive model. Please see the individual drive specifications for the number of retries allowed.

20.11 Single Initiator Selection

For single initiator systems it is not an error to have only the target ID bit present during selection. Disconnection is not allowed for Single Initiator Selection with only one ID bit present during selection. The initiator must not send an Identify message with the disconnect permission bit(6) on.

20.12 Non-arbitrating systems

The Target cannot detect whether other SCSI devices on the SCSI bus use arbitration prior to selection. As a consequence the

Target allows disconnect permission to be enabled by the Identify message independent of the initiator's use of arbitration prior to selection. A non-arbitrating initiator must ensure that disconnect permission in the Identify message is disabled (bit 6=0) for proper operation.

20.13 Selection without ATN

If the target is selected without ATN signal active, no Identify message is received from the Initiator. In this case the LUN is identified from the CDB and disconnect permission is disabled. The target does not perform any phase retries. The target still responds to a subsequent attention condition. However the LUN is not considered to be known if a fatal error is detected during the Command phase. That is a Command phase parity error or a fatal message error in response to attention condition during Command phase is handled as a Bus Free error with no sense data. The target also knows the use of linked commands if selected without ATN.

Phase retries may be allowed if a subsequent Identify message is received.

20.14 Multiple Initiator Environment

20.14.1 Initiator Sense Data

Separate sense data is reserved for each I-T-L. Each sense data is maintained independent of commands from other initiators.

20.14.2 Initiator Mode Select/Mode Sense Parameters

A single shared copy of the Mode Select/Mode Sense parameters is maintained by the drive. This includes both the current and saved parameters.

20.14.3 Initiator Data Transfer Mode Parameter

A separate data transfer mode parameters area is reserved and maintained for each initiator.

20.15 Contingent Allegiance Condition

The contingent allegiance condition shall exist following the return of Check Condition, except for a Check Condition caused by Invalid LUN. Execution of all queued commands shall be suspended until the contingent allegiance condition is cleared.

The contingent allegiance condition can be cleared by the initiator in one of the following ways:

- By issuing a REQUEST SENSE command to the Target and receiving the sense data. This is the recommended way
- By issuing any other command to the I_T_x nexus that reported the fault
- By issuing an Abort message to the I_T_x nexus that reported the fault. This will also abort the current and queued I/O process from that initiator
- By issuing a Target Reset message to the Target. This will also abort all current and queued I/O processes
- By generating a RESET condition on the bus. This MUST be the last resort

20.16 Reset

The Reset condition is used to clear all SCSI devices from the bus. This condition takes precedence over all other phases and conditions. After a reset condition is detected and the reset actions completed, the target returns to a 'SCSI bus enabled' state that allows the target to accept SCSI commands.

This device uses the Hard reset option as defined in the SCSI-3 standard.

20.16.1 Reset Sources

There are four sources of resets detected by the target:

Reset Name	Reset Source
Power-On Reset	This is the signal generated by the hardware at initial power-on
Self-Initiated reset	This is a software-generated reset that occurs when a catastrophic error is detected by the microcode.
SCSI Bus Reset	This is a reset generated when the SCSI bus control line RST goes active.
Target Reset Message	This is the reset generated by the SCSI Target Reset Message (0Ch).

20.16.2 Reset Actions

The action taken by the drive following a reset is dependent on the source of the reset.

20.16.2.1 Power-On reset and Self-Initiated reset

These two reset conditions cause the following to be performed in the order shown:

- a power-up sequence
- a startup sequence is necessary to put the drive in a ready state

20.16.2.2 SCSI Bus reset and SCSI Target Reset message

These two reset conditions cause the following to be performed.

- If reset goes active while the power-up sequence is in progress, the power-up sequence is started over.
- If the Auto Start pin is grounded and a startup sequence has not yet completed, a startup sequence will be re-attempted from the beginning.

Note: The power-up sequence, having already completed, is not rerun.

- If reset occurs while a physical sector is being written, the write operation is disabled after the current physical sector is written. Data is not lost as long as power stays valid until the physical sector being written is completed.

20.17 Diagnostics

The drive will execute Power on Diagnostics at power on time to assure the correct operation of the drive by validating components (ROM, RAM, Sector Buffer, EEPROM, HDC, Spindle Motor, Actuator), checking stored information in the Reserved Area and EEPROM, and verifying fault detects circuits.

Self-test can be invoked by issuing a SEND DIAGNOSTIC command.

20.17.1 Power on Diagnostics

At power on time the following tests are executed:

1. Validation of ROM and EEPROM
2. RAM test for internal RAM
3. Test and Initialize HDC registers
4. RAM test for Sector Buffer
5. Start Spindle Motor (if Auto spin up enable)
6. Calibration of Actuator
7. Read/Write test for all Heads

8. Validation of RAM code and data table (RDM, Log, Mode Page) from the Reserved Area

If Auto spin up is disabled, steps 5 - 8 will be executed by the first START STOP UNIT command which has the Start bit set.

Faults detected before successful completion of the HDC section could prevent the drive from responding to a selection.

Faults detected after the successful completion of the HDC test section will be reported as CHECK CONDITION status to the Initiator on the first command issued after a fault is detected (except for the INQUIRY, REPORT LUNS and REQUEST SENSE commands). The INQUIRY, REPORT LUNS and REQUEST SENSE commands will always be responded with a GOOD status. Detecting a fault during power on will not terminate execution of the tests nor will it terminate the power on process.

20.17.2 Self-test via SEND DIAGNOSTIC Command

20.17.2.1 Default Self-test

The default self-test is invoked by the SlfTst bit in the SEND DIAGNOSTIC command. The response is simply a GOOD status if the test is successful or a CHECK CONDITION status if the test fails.

The following tests are performed by the default self-test (in the order defined):

1. **Spin check** is to check if the spindle motor is running at the correct speed.
2. **Write, Read and Compare test** is a disk read/write test. It writes data to a predefined location in the reserved area and then reads it back and validates the content. All heads are tested.
3. **ECC circuit test** is a test for ECC circuit to ensure that errors can be corrected by the circuit.
4. **Seek test** is a servo test. It validates seeks to 256 random locations out of the full volume.

20.17.2.2 Short and Extended Self-tests

There are two other types of self-tests that may be invoked using the Function Code field in the SEND DIAGNOSTIC command: a short self-test and an extended self-test. The tests performed in the short and extended self-tests are described later. The time required by a logical unit to complete its extended self- test is specified in the Extended self-test Completion Time field in the Control Mode Page. The results of self-test can be retrieved via the LOG SENSE command for Log Page 10.

20.17.2.3 Self-test Modes

There are two modes for short and extended self-tests: a foreground mode and a background mode. These modes are described in the following clauses.

Foreground mode

When the drive receives a SEND DIAGNOSTIC command specifying a self-test to be performed in the foreground mode, the drive will return status for that command after the self-test has been completed. While performing a self-test in the foreground mode, the drive will respond to all commands except INQUIRY, REPORT LUNS, and REQUEST SENSE with a CHECK CONDITION status, a sense key of NOT READY and an additional sense code of LOGICAL UNIT NOT READY - SELF-TEST IN PROGRESS.

If the drive is performing a self-test in the foreground mode and a test error occurs, the drive will update the self-test results log page and report CHECK CONDITION status with a sense key of HARDWARE ERROR and an additional sense code of LOGICAL UNIT FAILED SELF-TEST. The application client may obtain additional information about the failure by reading the self-test results log page.

An application client may terminate a self-test that is being performed in the foreground mode using an ABORT TASK, ABORT TASK SET, or CLEAR TASK SET task management function. If the drive receives an ABORT TASK, ABORT TASK SET, or CLEAR TASK SET task management function while performing a self-test in the foreground mode, it will abort the self-test and update the self-test results log page.

Background mode

When the drive receives a SEND DIAGNOSTIC command specifying a self-test to be performed in the background mode, the drive will return status for that command as soon as the command descriptor block has been validated. After returning status for the SEND DIAGNOSTIC command specifying a self- test to be performed in the background mode, the drive will initialize the self-test results log page as follows. The Function Code from the SEND DIAGNOSTIC command will be placed in the Function Code field in the log page. The self-test Results field shall be set to 0Fh. After the self-test results log page is initialized, the drive will begin the first self-test segment.

While the device server is performing a self-test in the background mode, it shall terminate with a CHECK CONDITION status any SEND DIAGNOSTIC command it receives that meets one of the following criteria:

a. The SlfTst bit is one
b. The Function Code field contains a value other than 000b or 100b.

When terminating the SEND DIAGNOSTIC command, the sense key shall be set to NOT READY and the additional sense code shall be set to LOGICAL UNIT NOT READY, SELF-TEST in PROGRESS. While performing a self-test in the background mode, the drive will suspend the self- test to service any other command other than SEND DIAGNOSTIC (with Function Code field set to 100b) WRITE BUFFER (with the mode set to any download microcode option), FORMAT UNIT and START UNIT STOP command. Suspension of the self-test to service the command will occur within 2 seconds. If SEND DIAGNOSTIC (with Function Code field set to 100b), WRITE BUFFER (with the mode set to any download microcode option), FORMAT UNIT or START UNIT STOP command is received, the drive will abort the self-test, update the self-test log, and service the command within two seconds after the command descriptor block has been validated.

An application client may terminate a self-test that is being performed in the background mode by issuing a SEND DIAGNOSTIC command with the Function Code field set to 100b (Abort background self-test function).

Elements common to foreground and background self-test modes

The Progress Indication field returned in response to a REQUEST SENSE command may be used by the application client at any time during execution of a self-test to poll the progress of the test. While executing a self-test unless an error has occurred, the drive will respond to a REQUEST SENSE command by returning a sense key of NOT READY and an additional sense code of LOGICAL UNIT NOT READY - SELF-TEST IN PROGRESS with the sense key specific bytes set for progress indication.

The application client may obtain information about the twenty most recently completed self-tests by reading the self-test results log page. This is the only method for an application client to obtain information about self-tests performed in the background mode. The default self-test results are not logged in the log page.

Tests performed in the Short and Extended Self-test

The following table defines the tests performed in the short and extended self test. They are defined by their segment number which is also used to report Self-Test Results, in Log Sense Page 10. Note that the only difference between the Short and the Extended tests, is the sequential verify test in segment 9h. Also note that either of these tests can be run in foreground or background mode as previously described.

Table 195: Short and Extended Self-Test Description

Segment Number	**Short Self-Test**	**Extended Self-Test**	**Test Description**
1h	Drive Ready Test		Internal check to insure drive is “ready”, similar to a Test Unit Ready command.
2h	Drive Diagnostics		This test is comprised of the Default Self Test as defined in Section 20.17.2.1, “Default Self-test” on page 257
3h	SMART		Perform SMART testing and check results to ensure that SMART threshold criteria are not exceeded
4h	Low Level Format check		Check to insure that the media is currently not in the MEDIA FORMAT CORRUPTED state.

5h	Physical Head Check		Write/Read test on each head in a predefined location in the drive's Reserved Area of the disk.
6h	Random Verify		Perform 4000 random verify operations and insure no uncorrectable errors.
7h	- Verify First 300MB - Verify Last 100 MB	Verify all LBA’s	Sequential verify operation. Ensure that no uncorrect-able errors occur within the verify range.
8h	Recheck SMART		Same as segment 3h.

20.18 Idle Time Function

The drive periodically saves data in logs and PFA counters in the reserved area of the disks. The information is used by the drive to support various SCSI commands and for the purpose of failure analysis.

20.19 Command Time out Limits

The 'Command Time-out Limits' are defined as the time period from the SCSI Arbitration phase through the SCSI Task complete message, associated with a particular command.

The following times are for environments where Automatic Reallocation is disabled and there are no queued commands.

20.19.1 Reassignment Time

The drive should be allowed a minimum of 5 seconds to complete a “Reassign Blocks” command.

20.19.2 Format Time

Approximately 90/45/25 minutes should be allowed for 300/147/73 GB capacity drives to complete a "Format Unit" command when certification is disabled. Allow 180/90/50 minutes when certification is enabled. If "Fast Format" is enabled via the FFMT bit in mode page 00h, allow 30 seconds for completion..

20.19.3 Start/Stop Unit Time

The drive should be allowed a minimum of 30 seconds to complete a “Start Stop Unit” command (with Immed bit = 0). Initiators should also use this time to allow startup sequences initiated by auto start ups and “Start Stop Unit” commands (with Immed bit = 1) to complete and place the drive in a “ready for use” state.

Note: A time-out of one minute or more is recommended but NOT required. The larger system time-out limit allows the system to take advantage of the extensive ERP/DRP that the drive may attempt in order to successfully complete the startup sequence.

20.19.4 Medium Access Command Time

The time-out limit for medium access commands that transfer user data or non-user data or both should be a minimum of 30 seconds. These commands are

Pre-Fetch

Read

Read Defect Data

Seek

Send Diagnostic (Function Code = 0)

Read Long

Reassign Blocks

Write

Write and Verify

Write Buffer

Write Same

Verify

Note: The 30-second limit assumes the absence of bus contention and data transfers of 64 blocks or less. This time should be adjusted for anticipated bus contention and if longer user data transfers are requested.

20.19.5 Time-out Limits for Other Commands

The drive should be allowed a minimum of 5 seconds to complete these commands:

Inquiry

Log Select

Log Sense

Mode Select

Mode Sense

Persistent Reserve In/Out

Read Buffer

Read Capacity

Read Long

Release

Request Sense

Reserve

Set/Report Device Identifier

Start/Stop Unit (with Immed bit = 1)

Synchronize Cache

Test Unit Ready

Writer Long

The command time-out for a command that is not located at the head of the command queue should be increased by the sum of command time-outs for all of the commands that are performed before it is.

20.20 Recommended Initiator ERP

The Drive's design points for error reporting to the system assumes certain system action for the error return codes. These assumptions are:

1. SCSI protocol will be the first priority in reporting errors.
2. The system will maintain a log of all reported errors.
3. System architecture should include all error handling recommendations made in this section. Deviations should have mutual agreement between Drive development and system integration.

This section is directed toward documenting the assumptions made by the Drive that the system is expected to implement. The two error classes that the system should be concerned with are DATA and NON-DATA errors.

Data errors are those errors that deal with the handling of data to and from the MEDIA and are identified by the Additional Sense Code contained in the sense data. The Additional Sense Codes for data errors are as follows:

- OC - Write error
- 11 - Unrecovered read error
- 14 - No record found
- 16 - Data Synchronization mark error
- 17 - Recovered read error without ECC correction
- 18 - Recovered read error with ECC correction

Typically, data errors do not include positioning of the heads or the data path though the electronics.

Nondata errors are those errors that do not have a direct relationship with transferring data to and from the media. Nondata errors can include data handling if the media is not associated with the error (that is, interface error).

The system action assumed for each class of error is outlined here.

20.20.1 Drive Service Strategy

The Drive service strategy is defined so the customer will be able to use the system as soon after a failure is detected as possible. The first priority is to replace the entire drive to make the system operational with minimal service time. The service representative should:

1. Back up all the customer data on this drive if possible
2. Replace the complete drive
3. Restore the customer data
4. Return the drive to customer service

20.20.2 Recommendations for System Error Log

The system error log should contain information about the Drive error that will allow recovery actions. The system error logs should contain all the error information returned in the sense data. At a minimum, the following information about each error occurrence should be logged.

- Valid bit and error code (Sense byte 0)
- Sense Key (Sense byte 2)
- Information bytes (Sense bytes 3 through 6)
- Command specific information (Sense bytes 8 through 11)
- Additional Sense Code (Sense byte 12)
- Additional Sense Code Qualifier (Sense byte 13)
- Field Replaceable Unit (Sense byte 14)
- Sense Key Specific (Sense bytes 15, 16, and 17)
- Vender Unique error information (Sense bytes 20 through 23)

20.20.3 Data Recovery Procedure

Statistically, most data error activity is noise related and has nothing to do with defects in the media. It is wrong for the system to assume that every data error reported occurred because of a defect in the media. It is also wrong for the system to assume that every data error that occurred because of a media defect rendered the Drive unusable.

Recurring data error activity at the same physical location is an indication of a problem. The problem can be due to a media defect or magnetic damage. A media defect is physical damage to the recording capability of the media while magnetic damage is a defect in the bit pattern written to the media.

In both cases, the error can be corrected without replacing the unit. The physical sector may require relocation. The Drive determines the need to reassign a sector. The Mode Select Page 1 option bit ARRE (See Section 17.10.3, “Mode Page 01 (Read/Write Error Recovery Parameters)” on page 110) set active allows the Drive to relocate recovered read data errors. Non recovered data errors or the ARRE bit being inactive will have additional sense codes returned to recommend reassignment of sectors.

The need to reassign a sector should be infrequent. Sites not meeting error rate criteria are removed from use during SAT (Surface Analysis Test) in Drive manufacturing. With the exception of some early life SAT escapes (sites that were marginally missed during SAT), reassigning defective sectors should be rare. Frequent sector reassignment may be an (early) indication of another type of failure. Sector reassignments are monitored as part of the predictive failure analysis. When a threshold is exceeded, the Drive will notify the initiator that a scheduled service action is required.

Drive soft error rates are based on extraneous random faults that are not predictable. Media defects discovered after the Drive completes manufacturing final test need to be relocated so that soft error rates are not influenced by predictable known error sites. Failure of the system to properly relocate defective media sites can have a direct influence on system throughput and drive error rates.

20.20.3.1 Reassign a Physical Sector

The Drive determines the need to reassign physical sectors based on error activity. Once a physical sector requires reassignment, the Drive will either reassign the physical sector, or recommend to the initiator that the LBA associated with the physical sector be reassigned.

When the following Sense Key, Additional Sense Code, and Additional Sense Code Qualifier combinations are returned, the initiator should reassign the LBA reported at the next opportunity.

Note: In Table 196, the Key, Code, and Qualifier fields are all hex values (i.e., Sense Key 1 is 1h, Sense Code 17 is 17h, etc.).

Table 196: Recommend Reassign Errors

Key	Code	Qual	Description
1	17	07	Recovered Data without ECC - Recommend Reassignment
1	18	05	Recovered Data with ECC - Recommend Reassignment

To reassign an LBA that has sense data recommending a reassignment, the initiator should:

1. Attempt to recover the data from the sector being reassigned with a Read (08) or Read (28) command.
2. Reassign the LBA using the Reassign Blocks (07) command.
   - If the reassignment completes successfully (Good Status), log the error in the system error log.
   - If the reassignment completes unsuccessfully (Check Condition Status), follow the procedure in Section 20.20.3.3, "Reassign Blocks Recovery" on page 265.
3. Write the LBA that was reassigned.

20.20.3.2 Data Error Logging

The Drive will report data errors to the initiator that do not require immediate action (successful auto reallocation, successful auto rewrite, or no action needed on this occurrence). The initiator should log these errors in the system error log. No other action is required.

Table 197: Log Only Errors

Key	Code	Qual	Description
1	16	00	Data Synchronization Mark Error
1	17	01	Recovered Data with Retries
1	17	06	Recovered Data without ECC - Auto Reallocated
1	17	09	Recovered Data without ECC - Data Rewritten
1	18	00	Recovered Data with ECC
1	18	02	Recovered Data with ECC - Auto Reallocated
1	18	07	Recovered Data with ECC - Data Rewritten

20.20.3.3 Reassign Blocks Recovery

The Drive provides the capability to remove media defects without reducing capacity. If the mode parameter bit ARRE is active, the Drive will automatically reallocate LBA's determined to be defective. For those LBA's where the error is unrecoverable or the initiator elects to not have the Drive automatically reallocate LBA's, the Drive will recommend reassignment of the LBA.

Recovery from a failed reassignment consists of the following actions:

- Updating the defect descriptor to remove the LBA's that have been successfully reassigned and then retry the Reassign Blocks command. The LBA contained in the Command Specific Information field of the Sense Data is the LBA in the first defect descriptor that was not reassigned because of the failure. If the command failed because of an unrecoverable read error other than those specified in the defect descriptor, add this LBA to the defect descriptor and retry the command. Refer to Section 17.25, "REASSIGN BLOCKS (07)" on page 167, for additional information.
- If the retried Reassign Blocks (07) command completes successfully, returning to normal processing.
- If the retried Reassign Blocks (07) command fails, servicing the drive using the service guidelines recommended in Section 20.20.1, "Drive Service Strategy" on page 262.

20.20.4 Nondata Error Recovery Procedure

The Drive will follow a logical recovery procedure for nondata errors. The initiator options for non-data errors are limited to logging the error, retrying the failing command, or replacing the drive.

These recovery procedures assume the initiator practices data back-up and logs errors at the system level for interrogation by service personnel.

20.20.4.1 Drive Busy

The Drive is busy performing an operation. **This is not an error condition.** The initiator can test for completion of the operation by issuing *Test Unit Ready (00)* (or media access) commands.

- If the *Test Unit Ready (00)* (or media access) command completes with *Check Condition Status* then issue a *Request Sense (03)*
  - If the specified recovery procedure for the sense data is for a condition other than drive busy, follow the recovery procedure for the condition reported.
  - If the specified recovery procedure for the sense data is for a drive busy condition, then continue re-issuing the *Test Unit Ready (00)* and *Request Sense* commands for the duration of a media access time-out or until the drive returns *Good Statu*s.
  - If the drive has been busy for longer than the limit specified in Section 20.19, "Command Time out Limits" on page 260, then service the drive using the service guidelines recommended in Section 20.20.1, "Drive Service Strategy" on page 262. Otherwise return to normal processing.
- If the *Test Unit Ready (00)* (or media access) command completes with *Good Statu*s, then return to normal processing.

20.20.4.2 Unrecovered Drive Error

The initiator should retry the failing command.

1. If the retry of the failing command completes with *Good Status* or recovered Sense Key, follow the recovery procedure in Section 20.20.4.3, "Recovered Drive Error" on page 266.
2. If the retry of the failing command completes with hardware error sense, verify there is no outside cause (e.g., power supply) for the failure, then retry the failing command.
   a. If the retry of the failing command completes with *Good Statu*s, follow the recovery procedure in next Section 20.20.4.3, "Recovered Drive Error" on page 266.
   b. If the retry of the failing command completes with Recovered sense or Hardware error sense, then service the drive using the service guideline recommended in Section 20.20.1, "Drive Service Strategy" on page 262.

20.20.4.3 Recovered Drive Error

The Initiator should log the error as soft with the recovery level.

20.20.4.4 Drive Not Ready

The initiator should do the following:

1. Issue a *Start Stop Unit (1B)* command.
2. Verify that the drive comes ready within the time specified in Section 4.5.2, “Drive ready time” on page 12.
3. If the drive fails to come ready within the specified time, service the drive using the service guidelines specified in Section 20.20.1, “Drive Service Strategy” on page 262.
4. Retry the failing command.
   a. If the failing command completes with *Good Status*, log the error as recovered.
   b. If the failing command completes with Not Ready sense, verify there is no outside cause (for example, the power supply). Then service the drive using the service guidelines specified in Section 20.20.1, “Drive Service Strategy” on page 262.

20.20.4.5 No Defect Spare

Three conditions can cause this error:

1. When the *Reassign Blocks (07)* command is issued and there are no spares available for the Drive to use for the relocation requested.
2. When the Glist is full and the sector to be reassigned cannot be added.
3. During a format operation, there was not enough space available to fulfill the spare requirement (Dlist is too large).

Service the Drive following Section 20.20.1, “Drive Service Strategy” on page 262.

20.20.4.6 Degraded Mode

Refer to Section 20.1.9, “Degraded Mode” on page 242, for the definition of this state. There are three causes for entering degraded mode. In all cases the Sense Key is *Not Ready.* The causes are the following:

1. Sense Code/Qualifier of *Logical Unit Not Ready, initializing command required*. The spindle motor not spinning or not at the proper speed. This may not be an error condition. The initiator should issue a *Unit start (1B)* command to start the spindle motor. If the Drive fails to come ready in the time specified in Section 20.19, “Command Time out Limits” on page 260, service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.
2. Sense Code/Qualifier of *Diagnostic Failure*. Failure of a Send Diagnostic self test, a start up sequence, or other internal target failures.

- Failure of a send diagnostic self test or a start up sequence.

  This failure is the result of the diagnostics that are executed during power on or when the *Send Diagnostic (1D)* command is executed detecting a failure. As with the RAM code not loaded and the configuration data not loaded, the recovery is either a power cycle or issuing the *Send Diagnostic (1D)* command with the self test bit set active. Recovery for a failed Send Diagnostic (1D) is achieved in one of the following ways:

  Executing the Send Diagnostic (1D) command

  Power cycling the drive

  If the failure repeats, service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

  Recovery for a failed power up sequence is achieved in one of the following ways:

  Issuing a Unit start (1B) command

  Power cycling the drive.

  If the failure repeats, service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

- Internal target failures

  The drive periodically adjusts the track following for each head to compensate for expansion and contraction of the disks due to temperature changes. If one of these adjustments fails, the drive will enter a degraded mode to prevent writing data off track.

  Recovery of this condition is either a power cycle or successful completion of the Send Diagnostic (1D). Service the drive using the recommended service guidelines specified in Section 20.20.1, “Drive Service Strategy” on page 262, if the power cycle or the Send Diagnostic (1D) command fail to complete successfully.

3. Sense Code/Qualifier of **Format Command Failed** Format Unit (04), Sense Code/Qualifier of **Medium Format Corrupted Reassign Failed** Reassign Blocks (07) command, or an automatic reallocation failed or was abnormally terminated.

   Recovery from a failed Format Unit (04) is achieved by retrying the command. If the command fails a second time, service the drive following the procedure defined in Section 20.20.1, “Drive Service Strategy” on page 262.

If the above defined recovery procedures fail to clear the degraded mode condition, the Drive should be replaced. Follow the procedure in Section 20.20.1, “Drive Service Strategy” on page 262, when replacing the drive.

20.20.4.7 Reserved Area Hard Error

Sectors found defective in the reserved area of the disk cannot be reassigned after the Drive leaves the factory. The data in the reserved area is not directly accessible by the initiator. For this reason, the reserved area has duplicate copies of all data. A data error must occur in both copies of the data record before the Drive considers a reserved area read error. When this happens, the integrity of the drive is questionable.

Service the Drive using Section 20.20.1, “Drive Service Strategy” on page 262.

20.20.4.8 Interface Protocol

For all interface protocol errors, the initiator should complete the following steps:

1. Correct the parameter that caused the Illegal Request
2. Retry the failing command
3. If the first retry of the failing command completes with
   - *Good Status*, log the error as recovered
   - *Check Condition Status* with sense data for an Illegal Request, verify there is no outside cause (for example, the power supply) for the failure
   - *Other*, follow the recommendations for the error condition reported. Retry the failing command. If this retry of the failing command completes with
     - *Good Status*, log the error as recovered
     - *Check Condition Status* with sense data for an Illegal Request, service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.
     - *Other*, follow the recommendations for the error condition reported.

20.20.4.9 Aborted Command

The initiator should determine the cause from the Additional Sense Code (byte 12):

- Sense Key = B (Aborted Command) with Additional Sense Codes of 1B, 25, 43, 49, and 4E are initiator caused abort conditions. The initiator should correct the condition that caused the abort and retry the failing command.
- Sense Key = B (Aborted Command) with Additional Sense Code of 44 or 48 are drive caused abort conditions. The initiator should:
  1. Retry the failing command.
  2. If the retry of the failing command completes with

- *Good Statu*s, log the error as recovered.
- Abort Command Sense, verify there is no outside cause (e.g. power supply) for the failure.

3. Retry the failing command.
4. If the retry of the failing command completes with
   - *Good Statu*s, log the error as recovered.
   - Abort command sense, then service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

- Sense Key = B (Aborted Command) and an Additional Sense Code of 47 can be an initiator or Drive caused abort condition. The initiator should follow the above procedure for initiator caused abort conditions if the Drive detected the SCSI bus parity error. The initiator should follow the above procedure for Drive caused abort conditions if the initiator detected the SCSI bus parity error.

20.20.4.10 Unit Attention Condition

Unit Attention Conditions are not errors. They alert the initiator that the drive had an action that may have changed an initiator controlled state in the drive. These conditions are the following:

Not Ready to Ready Transition

Not ready to ready transition, unit formatted. This *Unit Attention Condition* will not be reported to the initiator that issued the *Format Unit (04*).

Reset

Reset - This means the drive was reset by either a power-on reset, Bus reset, a Target Reset message, Transceiver Mode Changed reset, or an internal reset.

Mode Parameters Changed

A *Mode Select (15)* command successfully completed. This means that the mode parameters that are the current value may have changed. The parameters may or may not have changed but the command to change the parameters successfully completed. The Drive does not actually compare the old current and the new current parameters to determine if the parameters changed. This *Unit Attention Condition* will not be reported to the initiator that issued the *Mode Select (15).*

Microcode Has Changed

Write Buffer (3B) to download microcode has successfully completed. This means that the microcode that controls the Drive has been changed. The code may or may not be the same as the code currently being executed. The Drive does not compare old level code with new code.

Commands Cleared by Another Initiator

Tagged commands cleared by a clear queue message. This means that the command queue has been cleared. The *Unit Attention Condition* is not reported to the initiator that issued the clear queue message. *Unit Attention Condition* is reported to all initiators that had commands active or queued.

Reissue any outstanding command.

Log Select Parameters Changed

A Log Select (4C) command successfully completed. This means that the Log Select command cleared statistical information successfully (See Section 17.6, “LOG SELECT (4C)” on page 72). Unit Attention Condition is reported to all initiators excluding the initiator that issued the Log Select command.

Device Identifier Changed

A Set Device Identifier (A4) command successfully completed. This means that the Set Device Identifier information field has been updated. (See 17.40, “SET DEVICE IDENTIFIER (A4/06)” on page 191) A Unit Attention Condition is reported to all initiators excluding the initiator that issued the Set Device Identifier command.

20.20.4.11 Components Mismatch

The compatibility test is performed at a power cycle. The compatibility test verifies the microcode version of the electronics. When the Drive detects the microcode version mismatch, the most likely cause is the result of incorrect parts used during a service action.

If the error reported is Key/code/qualifier 4/40/80, Diagnostic failure, bring-up fail, the initiator should do the following:

1. Retry Power cycle
2. Check the send diagnostic end status. If the status is
   - GOOD, Return to normal processing
   - *Check Condition Statu*s, issue a *Request Sense (03)* and follow the recommendations for the sense data returned unless the sense data is for a component mismatch. If the sense data is for component mismatch, service the drive using the service guideline recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

20.20.4.12 Self Initiated Reset

The Drive will initiate a self reset when the condition of the Drive cannot be determined. The internal reset will terminate any outstanding commands, release any reserved initiators, and stop the spindle motor. The initiator can recover by

1. Logging the error
2. Retrying the failing command. If the failing command completes with:
   - *Good Statu*s, return to normal processing
   - Self initiated reset sense, service the drive according the guidelines recommended in Section 20.20.1, “Drive Service Strategy” on page 262.
   - Other, follow the recommendations for the error reported.

20.20.4.13 Defect List Recovery

This is not an error condition.

The initiator either requested a defect list in a format (block or vendor specific) that the Drive does not support or the requested defect list(s) exceed the maximum list length that can be returned. If the Sense Key/Code/Qualifier are:

1/1F/00, the requested list(s) exceed the maximum length that can be supported. The initiator should request one list at a time. If a single list exceeds the maximum returnable length, this may be an indication of a marginally operational drive. Service the drive following the service guidelines in Section 20.20.1, “Drive Service Strategy” on page 262.

1/1C/01 or 1/1C/02, the requested defect list is not in the format that the Drive supports. The requested defect list is returned in the physical (cylinder, sector, head) format. This is the default format. There is no initiator action required for this condition.

20.20.4.14 Miscompare Recovery

A miscompare can occur on a *Verify (2F)* command or a *Write and Verify (2E)* with the byte check (BytChk) bit active. Recovery for a miscompare error is different for the two commands.

Verify Command

The initiator should do the following:

1. Verify that the data sent to the drive is the correct data for the byte-by-byte compare.
2. Read the data from the media with a *Read (08)* or *Read (28)* command and verify that the data from the media is the expected data for the byte-by-byte compare.
   - If all data are correct, this is an indication that the data may have been read from the media incorrectly without an error detected. Service the drive using the procedure specified in Section 20.20.1, “Drive Service Strategy” on page 262.
   - If all data are not correct, this is an indication that the data on the media is not the data the initiator expected. Rewrite the correct data to the media.

Write and Verify Command

The drive uses the same data in the data buffer to write then read and compare. A miscompare error on the *Write and Verify (2E)* command is an indication that the drive cannot reliably write or read the media. Service the drive using the procedures specified in Section 20.20.1, “Drive Service Strategy” on page 262.

20.20.4.15 Microcode Error

The microcode from the interface is validated before the device operates using that microcode. When the validation detects incorrect or incomplete data, the Drive enters degraded mode.

If the initiator attempted to load microcode using the *Write Buffer (3B)* retry the *Write Buffer (3B).* If the command completes with

- *Good Status* - return to normal processing
- *Check Condition Status* - service the drive using the service guidelines recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

If the check sum error occurred during normal processing, the initiator may attempt to load microcode before deciding to service the drive using the service guidelines recommended in Section 20.20.1, “Drive Service Strategy” on page 262.

To load new microcode, the initiator should issue a Write Buffer (3B) command with the download and save option. If the Write Buffer (3B) command completes with

- Good Status, return to normal processing. Retry the failing command. If the task complete with
  - Good Status - Continue normal processing.
  - Check Condition Status for check sum error - Service the drive using the service guidelines recommended in Section 20.20.1, “Drive Service Strategy” on page 262.
  - Check Condition Status for any other error - follow the recommended recovery procedure for the error reported.
- Check Condition Status for Check sum error, service the drive using the service guidelines recommended in Section 20.20.1, “Drive Service Strategy” on page 262.
- Check Condition Status for any other error, follow the recommendations for the returned sense data.

20.20.4.16 Predictive Failure Analysis

The Drive performs error log analysis and will alert the initiator of a potential failure. The initiator should determine if this device is the only device with error activity.

If this drive is the only drive attached to the initiator with error activity, service the drive using the procedures specified in Section 20.20.1, “Drive Service Strategy” on page 262.

Note: Service for this drive can be deferred. The longer service is deferred, the more probable a failure can occur that will require immediate service.

If more than this drive is experiencing error activity, the drive is probably not at fault. Locate and service the outside source causing error activity on this drive.

21.0 SCSI Sense Data

21.1 SCSI Sense Data Format

Format of the sense data returned by the drive in response to the REQUEST SENSE command.

Table 198: Format of Sense Data.

Byte	Bit							
	7	6	5	4	3	2	1	0
0	Valid	Error Code (70h or 71h)						
1	RSVD = 0							
2	0		ILI	0	Sense Key			
3-6	(MSB) Information Bytes (LSB)							
7	Additional Sense Length							
8-11	(MSB) Product Specific Information (LSB)							
12	Additional Sense Code							
13	Additional Sense Code Qualifier							
14	FRU = 0							
15	SKSV	Sense-Key Specific Bits						
16-17	Sense-Key Specific Bytes							
18-19	Reserved = 0							
20-23	Vendor unique Error information							
24-29	Product Specific Information							
30-31	Reserved = 0							

21.2 Sense Data Description

21.2.1 Valid (Bit 7 of byte 0)

0 The Information Bytes (byte 3 through 6) are not defined.

1 The Information Bytes (byte 3 through 6) contain a valid logical block address.

21.2.2 Error Code (Bit 6 - 0 of byte 0)

70h Current Error. This indicates an error for the current command.

71h Deferred Error. This indicates that the error is for a previous command that has already returned a good status. Such commands are associated with the immediate bit or write caching. Format unit (04h) command is an example of a command that may return a deferred error.

21.2.3 ILI: Incorrect Length Indicator (Bit 5 of byte 2)

The ILI bit is valid for the Read Long (3Eh) command and Write Long (3Fh) command only. ILI set to one and Valid Bit set to one indicates that the requested logical block length does not match the logical block length of the data on the medium for a Read Long or Write Long command. The Information field contains residue information about the error. ILI set to zero indicates there is no incorrect length condition.

0 No Incorrect Length condition.

1 Incorrect Length Indicated.

Valid	ILI	Command = Read Long or Write Long?	Description
x	0	x	No incorrect length condition
1	1	yes	Requested Logical block Length does not match the logical block length of the data on the disk

21.2.4 Sense Key (Bit 3 - 0 of byte 2)

The sense key provides generic categories in which error and exception conditions can be reported. Initiators would typically use sense keys for high level error recovery procedures.

0h **No Sense**

There is no sense key information to be reported for the logical unit.

1h **Recovered Error**

The last command completed successfully with some recovery action performed by the drive. More detailed information is available in the Additional Sense Code and Additional Sense Code Qualifier.

2h **Not Ready**

The logical unit addressed cannot be addressed. More detailed information is available in the Additional Sense Code and Additional Sense Code Qualifier.

3h **Medium Error**

The command terminated with an unrecoverable error condition caused by a flaw in the media or an error in the recorded data. More detailed information is contained in the Additional Sense Code and Additional Sense Code Qualifier.

4h **Hardware Error**

The drive detected a unrecoverable hardware error while performing a command or during a diagnostic test. More detailed information is contained in the Additional Sense Code and Additional Sense Code Qualifier.

5h **Illegal Request**

There was an illegal parameter in the command descriptor block or additional parameter supplied as data. If an invalid parameter is found in the CDB, then the command is terminated without altering the medium. If an invalid parameter is found in parameters supplied as data, then the drive might have altered the medium.

6h **Unit Attention**

Indicates that the drive entered in the 'Unit Attention Condition'. (See Section 21.6.5, “Unit Attention Condition” on page 288)

7h **Data Protect**

8h **Not used**

9h **Vendor Specific**

Ah **Not used**

Bh **Aborted command**

The drive aborted the command.

Ch-Dh **Not Implemented**

Eh **Miscompare**

Fh **Reserved**

21.2.5 Information Bytes (Byte 3 through 6)

This field is only valid when Valid Bit is one.

- ILI = 0: This field contains the unsigned LBA associated with the sense key. The LBA reported will be within the LBA range of the command as defined in the CDB.

Note: An LBA other than the command LBA may be reported on the Reassign Block (07h) command.

- ILI = 1: This field contains the difference (residue) of the requested length in bytes. Negative values are indicated by two's complement notation.

Valid	ILI	Description
0	x	0x00000000 - (not used/invalid)
1	0	LBA
1	1	Residue of the requested length in bytes

21.2.6 Additional Sense Length (Byte 7)

Indicates the remaining number of bytes in the sense data. (It is always set to 18h.)

21.2.7 Command Specific Information (Byte 8 through 11)

The values in this field vary with products. Please see the individual product specification for more details.

21.2.8 Additional Sense Code/Qualifier (Byte 12 and 13)

The following table shows the description of the combination of Sense Key / Sense Code / Qualifier.

Valid Sense Key, Code, Qualifier Combinations Used by the Drive.

Key	Code	Qual	Description
			Sense Key = No Sense
0	**00**	**00**	**No Additional Sense Information**
			(00 00) No Error.
			Sense Key = Recovered Error
1	**01**	**00**	**No Index/Sector Signal**
			1413 Servo: Soft write no index error
1	**02**	**00**	**No Seek Complete**
			141B Servo: Recovered No seek complete
			141D Servo: Recovered Seek timeout
			141F Servo: Recovered Seek error
			142B Servo: Recovered RRO Calibration timeout
1	**03**	**00**	**Peripheral Device Write Fault**
			1405 Servo: Recovered write inhibit error
			1733 Recovered read/write abort
			1737 Recovered post write abort
			1739 Recovered Post PES check write abort
			17E0 Servo Recovered read/write abort estimator error
			17E2 Servo Recovered read/write abort predictor error
			17E4 Servo Recovered read/write abort PES error
			17E6 Servo Recovered read/write abort seek start error
			17E8 Servo Recovered read/write abort PES reset error
			17EA Servo Recovered read/write abort abort WCS other error
			17EC Servo Recovered read/write abort WCS other error
			17EE Servo Recovered read/write abort hard reset
			17F0 Servo Recovered read/write abort RV sensor error
			17F2 Servo Recovered read/write abort RV sensor error
			17F4 Servo Recovered read/write abort SHARP other error
			17F6 Servo Recovered read/write abort SHARP exception error
			17F8 Servo Recovered read/write abort SVGA limit error
			17FA Servo Recovered read/write abort gray code error
			17FC Servo Recovered read/write abort burst error
			17FE Servo Recovered read/write abort no STM error
1	**09**	**00**	**Track Following Error**
			1421 Servo: Recovered track following error
			1423 Servo: Recovered track follow timeout

1	**0B**	**01**	**Temperature Warning Error** xA02 Temperature Warning
1	**0B**	**03**	**Background Selftest Failure Warning** xA03 Background selftest failure warning
1	**0B**	**04**	**Background Pre-Scan Failure Warning** xA04 Background pre-scan failure warning
0	**0B**	**05**	**Background Media Scan Failure Warning** xA05 Background media scan failure warning
1	**0C**	**01**	**Recovered Write Error with Auto Reallocation - Auto Reallocated** D703 Auto-reallocated due to write error
1	**0C**	**03**	**Recovered Write Error - Recommend Reassignment** 1704 Recommend reassign due to write error
1	**15**	**00**	**Random Positioning Error** 1714 Recovered sector overflow 173D Recovered sector miss 1770 Recovered SID timeout 17B2 Recovered abort window error
1	**16**	**00**	**Data Synchronization Mark Error** 173B Recovered data address mark error
1	**16**	**01**	**Data Sync Error - Data Rewritten** E70E Recovered Data Address Mark error - rewritten
1	**16**	**02**	**Data Sync Error - Recommend Rewrite** E70F Recovered Data Address Mark error - recommend rewrite
1	**16**	**03**	**Data Sync Error - Auto Reallocated** D710 Recovered Data Address Mark error - reassigned
1	**16**	**04**	**Data Sync Error - Recommend Reassignment** E711 Recovered Data Address Mark error - recommend reassign
1	**17**	**01**	**Recovered Data with Retries** 1722 Recovered small thermal asperity 172C Recovered media error 172E Recovered media error (off-line correction discarded)
1	**17**	**06**	**Recovered Data Without ECC - Data Auto-Reallocated** E705 Media error with OTF correction - reassigned D72B Recovered thermal asperity - reassigned
1	**17**	**07**	**Recovered Data Without ECC - Recommend Reassignment** E706 Media error with OTF correction - recommend reassign E72A Recovered thermal asperity - recommend reassign
1	**17**	**08**	**Recovered Data Without ECC - Recommend Rewrite** E707 Media error with OTF correction - recommend rewrite E729 Recovered thermal asperity - recommend rewrite

1	**17**	**09**	**Recovered Data Without ECC - Data Rewritten** D708 Media error with OTF correction - rewritten E728 Recovered thermal asperity - rewritten
1	**18**	**00**	**Recovered Data With ECC** 1709 Media error with offline correction
1	**18**	**02**	**Recovered Data - Data Auto-Reallocated** D70A Media error with offline correction and reassign D724 Recovered thermal asperity with offline correction - reassigned
1	**18**	**05**	**Recovered Data - Recommend Reassignment** E70B Media error with offline correction and recommend reassign E725 Recovered thermal asperity with offline correction - recommend reassign
1	**18**	**06**	**Recovered Data With ECC - Recommend Rewrite** E70C Media error with offline correction - recommend rewrite E726 Recovered thermal asperity with offline correction - recommend rewrite
1	**18**	**07**	**Recovered Data With ECC - Data Rewritten** E70D Media error with offline correction - rewritten E727 Recovered thermal asperity with offline correction - rewritten
1	**1C**	**00**	**Defect List Format Not Supported** 1746 Defect list format not supported
1	**1C**	**01**	**Primary Defect List Not Found. Requested Format Not Supported** 1747 Primary defect list not found (Read Defect Data only)
1	**1C**	**02**	**Grown Defect List Not Found. Requested Format Not Supported** 1748 Grown defect list not found (Read Defect Data only)
1	**1F**	**00**	**Partial Defect List Transfer** 1749 Partial defect list transferred (Defect list longer than 64KB, 64 KB of data returned - Read Defect Data only)
1	**44**	**00**	**Internal Target Failure** F123 Invalid request to enter sleep mode F128 DRAM test in progress F129 DRAM test complete F12A DRAM test error F132 GEM FH track read error 1201 Error in UEC class 1202 Error in UEC cause 1301 Motor: Recovered internal error 1303 Motor: Recovered Open Loop Commutation failure 1305 Motor: Recovered No feedback detected 1307 Motor: Recovered Settle timeout 1309 Motor: Recovered Gross speed error 130B Motor: Recovered 12V OK error

130D Motor: Recovered Speed error

1311 Motor: Recovered Internal 12V not OK timeout

1313 Motor: Recovered Iductive Sense measurement timeout

1315 Motor: Recovered Spin Sense speed error

1319 Motor: Recovered Target speed error

131B Motor: Recovered Power driver version error

131D Motor: Recovered Over current error

1321 Motor: Recovered Negative regulator fault

1323 Motor: Recovered Module overtemp error

1325 Motor: Recovered 12V or 5V OK error

1327 Motor: Recovered unknown error

1401 Servo: Recovered Requested rezero head does not exist

1403 Servo: Recovered Back EMF movement in progress

1405 Servo: Recovered Back EMF timeout error

1407 Servo: Recovered ADC conversion timeout

1409 Servo: Recovered Load/unload calibration error

140B Servo: Recovered Invalid 5 volts

140D Servo: Recovered Invalid 12 volts

140F Servo: Recovered Invalid harmonic requested

1411 Servo: Recovered Gain BEMF Calibration error

1413 Servo: Recovered VOFF BEMF calibration error

1415 Servo: Recovered Invalid temperature

1417 Servo: Recovered Truncated rezero

1419 Servo: Recovered Heads not loaded

1425 Servo: Recovered KT Seek out of range

1427 Servo: Recovered DAC Offset calibration error

1429 Servo: Recovered Load speed error

142D Servo: Recovered ADC Calibration error

142F Servo: Recovered ADC Offset error

1431 Servo: Recovered ADC Limit error

1433 Servo: Recovered Balancer Resistance error

1435 Servo: Recovered Balancer Resistance Limit error

1437 Servo: Recovered First Cylinder error

1439 Servo: Recovered Valid Cylinder error

143B Servo: Recovered ADC Saturation error

143D Servo: Recovered Latch Break timeout

143F Servo: Recovered MR Resistance out of range error

1441 Servo: Recovered VCM Retract error

1443 Servo: Recovered Load Retry error

1445 Servo: Recovered DFT Sharp error

1447 Servo: Recovered Load/Unload state error

1606 Recovered data with PPM or precomp load

1608 Recovered data with TA (Thermal Asperity) detection

160A Recovered data with SMM or VM or DDF

160C Recovered data with pre-PPM or FH (Fly Height) detection

160E Recovered data with write or pre-TA detection

1610 Recovered data with pre-SMM or VM or STM or DDF

1612 Recovered data with NRZ parity error

1614 Recovered parity PP correction or STW

1616 Recovered channel error

1618 Recovered AE thermal asperity

161A Recovered AE open MR element error

161C Recovered AE IC over temperature error

161E Recovered AE IP clock count error

1620 Recovered AE write data BLS error

1624 Recovered AE invalid head address error

1626 Recovered AE power supply error

162A Recovered AE write transition error

162C Recovered AE no write head current error

162E Recovered Channel Pre-TA error

1630 Recovered Channel write or NRZ parity error

1632 Recovered Channel write synth unlock error

1634 Recovered AE Short write read head error

1636 Recovered AE Short write head error

1638 Recovered AE Non-selected write head error

163C Recovered AE IH open short error

163E Recovered AE IH delay error

1640 Recovered Channel coarse tune timeout error

1642 Recovered AE Readback error

1712 Recovered ECC error

1716 Recovered overrun

171A Recovered ECC overrun

171C Recovered DRAM CRC error

171E Recovered ID Parity error

1731 Recovered write fault

1759 Unknown recovered error

1792 Recovered Current pointer error

1798 Recovered ID not found error

179C Recovered Channel write or NRZ parity error

17B4 Recovered Shock sensor error

			17B8 Recovered Reference tag error 17BA Recovered Application tag error 17BC Recovered Guard check error 17BE Recovered Channel write synthesis unlock error 17C0 Recovered End sector check error 17C2 Recovered Read CRC error 17C4 Recovered DRAM ECC error 17C6 Recovered DRAM ECC LBA error 17C8 Recovered DRAM Write error
1	**44**	**0B**	**Vendor Unique - Internal Target Failure** 130F Motor: Recovered Spindle Current error 1317 Motor: Recovered Spin Sense timeout 131F Motor: Recovered System clock watchdog error 1329 Motor: Recovered VCM DAC watchdog error
1	**5D**	**10**	**Predictive Failure Analysis Threshold Reached** xAFE SMART: Extreme over-temperature warning
1	**5D**	**50**	**Predictive Failure Analysis Threshold Reached** xA50 SMART: Load/unload cycle count warning
1	**5D**	**5B**	**Predictive Failure Analysis Threshold Reached** xA5B SMART: Spin-up retry count warning
1	**5D**	**62**	**Predictive Failure Analysis Threshold Reached** xA32 SMART: Read error rate warning xA4A SMART: Write error rate warning
1	**5D**	**63**	**Predictive Failure Analysis Threshold Reached** xA43 SMART: Seek error rate warning
1	**5D**	**64**	**Predictive Failure Analysis Threshold Reached** xA14 SMART: Spare sector availability warning
1	**5D**	**66**	**Predictive Failure Analysis Threshold Reached** xA56 SMART: Spin-up time warning
1	**5D**	**FF**	**Predictive Failure Analysis Threshold Reached** xAFF SMART: Test warning
1	**81**	**00**	**Vendor Unique - Internal Logic Error** 1601 Channel/AE internal logic error 1718 Recovered Abort immediate error
Sense Key = Not Ready			
2	**04**	**00**	**Logical Unit Not Ready - Start Spindle Motor Fail** 1501 Logical unit not ready
2	**04**	**01**	**Logical Unit Is In The Process of Becoming Ready** 1502 Logical unit becoming ready
2	**04**	**02**	**Logical Unit Not Ready, initializing command required**

			F124 Bring-up error
			1503 Logical unit not ready - initializing command required
2	**04**	**03**	Logical Unit Not Ready, Manual Intervention Required
			1572 Not ready - manual intervention required
			1573 Commands cleared due to power failure event (SAS)
2	**04**	**04**	**Logical Unit Not Ready, Format In Progress**
			1504 Not ready - format in progress
2	**04**	**09**	**Not Ready - Self-test In Progress**
			1505 Not ready - self-test in progress
2	**04**	**11**	**Not Ready - Notify (Enable Spin-up) Required**
			1553 Not ready - Notify (Enable Spin-up) required (SAS only)
2	**04**	**F0**	**Vendor Unique - Logical Unit Not Ready**
			F133 BATS error: Vendor ID mismatch
2	**31**	**00**	**Medium Format Corrupted - Reassign Failed**
			1506 Reassign failed
2	**31**	**01**	**Format Command Failed**
			1507 Format failed
			Sense Key = Medium Error
3	**03**	**00**	**Medium Error - Write Fault**
			F734 Unrecovered read/write abort
			F738 Unrecovered post write abort
			F73A Unrecovered Post PES check write abort
			F7E1 Servo Unrecovered read/write abort estimator error
			F7E3 Servo Unrecovered read/write abort predictor error
			F7E5 Servo Unrecovered read/write abort PES error
			F7E7 Servo Unrecovered read/write abort seek start error
			F7E9 Servo Unrecovered read/write abort PES reset error
			F7EB Servo Unrecovered read/write abort SID unlock error
			F7ED Servo Unrecovered read/write abort WCS other error
			F7EF Servo Unrecovered read/write abort hard reset
			F7F1 Servo Unrecovered read/write abort RV sensor error
			F7F3 Servo Unrecovered read/write abort RV sensor error
			F7F5 Servo Unrecovered read/write abort SHARP other error
			F7F7 Servo Unrecovered read/write abort SHARP exception error
			F7F9 Servo Unrecovered read/write abort SVGA limit error
			F7FB Servo Unrecovered read/write abort gray code error
			F7FD Servo Unrecovered read/write abort burst error
			F7FF Servo Unrecovered read/write abort no STM error
3	**11**	**00**	**Unrecovered Read Error**
			F702 Too many notches

			F723 Unrecovered small thermal asperity F702 Too many notches F72D Unrecovered media error F72F Unrecovered media error (off-line correction discarded) F753 G-list full (Format command) F754 G-list full (2) (Format command) F755 Pointer repeat size error F756 DST slot size error F757 P-list full F758 Spare list full
3	**15**	**00**	**Random Positioning Error** F715 Unrecovered sector overflow F73E Unrecovered sector miss F771 Unrecovered SID timeout F7B3 Unrecovered abort window error
3	**16**	**00**	**Data Synchronization Mark Error** F73C Unrecovered data address mark error
3	**19**	**02**	**Defect List Error in Primary List** F74B Primary defect list error
3	**19**	**03**	**Defect List Error in Grown List** F74C Grown defect list error
3	**31**	**00**	**Medium Format Corrupted Reassign Failed** F701 Format corrupted
3	**81**	**00**	**Vendor Unique - Internal Logic Error** F719 Recovered Abort immediate error F75B Too many sectors
			Sense Key = Hardware Error
4	**02**	**00**	**No Seek Complete** F41C Servo: Unrecovered No seek complete F41E Servo: Unrecovered Seek timeout F420 Servo: Unrecovered Seek error F42C Servo: Unrecovered RRO Calibration timeout
4	**09**	**00**	**Track Following Error** F422 Servo: Unrecovered track following error F424 Servo: Unrecovered track follow timeout
4	**31**	**00**	**Medium Format Corrupted - Reassign Failed** F204 Reassign reserved area media error
4	**32**	**00**	**No Defect Spare Location Available** F205 G-list full - can't reassign any more sectors F206 No spares available

4	**35**	**00**	**Enclosure Services Failure**
			1539 ESI: unspecified failure (FC-AL only)
4	**35**	**01**	**Enclosure Services Failure - Unsupported Enclosure Function**
			153A ESI: unsupported function (FC-AL only)
4	**35**	**02**	**Enclosure Services Failure - Enclosure Services Unavailable**
			153B ESI: enclosure unavailable (FC-AL only)
4	**35**	**03**	**Enclosure Services Failure - Enclosure Services Transfer Failure**
			153C ESI: transfer failure
			1556 ESI: transfer failed - write ack
			1557 ESI: transfer failed - read ack
			1558 ESI: transfer failed - write ready
			1559 ESI: transfer failed - read ready
			155E ESI: transfer failed - EDV
4	**35**	**04**	**Enclosure Services Failure - Enclosure Services Refused**
			153D ESI: transfer refused
			155A ESI: transfer refused - write ack
			155B ESI: transfer refused - read ack
			155C ESI: transfer refused - write ready
			155D ESI: transfer refused - read ready
4	**3E**	**03**	**Self-test Failed**
			F75D Self-test failed
4	**40**	**80**	**Diagnostic Failure**
			F101 BATS error: Reserved Area - Invalid request
			F102 BATS error: Reserved Area - Broken
			F103 BATS error: Reserved Area - Invalid version
			F104 BATS error: Reserved Area - Invalid checksum
			F105 BATS error: Reserved Area - Invalid eyecatcher
			F106 BATS error: Reserved Area - Invalid main header checksum
			F107 BATS error: Reserved Area - Invalid read length
			F108 BATS error: Reserved Area - Address boundary error
			1109 BATS error: Reserved Area - Error reading first copy
			110A BATS error: Reserved Area - Error reading second copy
			F10B BATS error: Reserved Area - Read block error
			110C BATS error: Reserved Area - Write fix soft error
			F10D BATS error: Reserved Area - Write fix hard error
			F10E BATS error: Directory broken
			F10F BATS error: Overlay code load error
			F110 BATS error: Overlay code check
			F111 BATS error: RAM code load error
			F112 BATS error: RAM code check

			F113 BATS error: Config invalid
			F114 BATS error: Log manager invalid
			F115 BATS error: Media tables invalid
			F116 BATS error: Logical-to-physical invalid
			F117 BATS error: Defect manager invalid
			F11D Incorrect Disk Code
			F11F RPO SID invalid
			F120 BATS error: Code compatibility failure
			F121 BATS error: Code download in progress
			F122 BATS error: Performance data read error
			F125 BATS error: Invalid RID/FID
			F126 BATS error: Code checksum error
			F127 BATS error: Invalid header
			F12B BATS error: Reserved area - invalid model
			F12C BATS error: Invalid code size
			F12D Format Reserved: FAT size exceeded
			F12E Format Reserved: Insufficient DIRS good
			F12F Format Reserved: Insufficient FATS good
			F130 Incorrect Customer code
			F131 Flash timeout
4	**40**	**90**	**Diagnostic Failure**
			F118 BATS#2 error: Seek test error
4	**40**	**A0**	**Diagnostic Failure**
			F119 BATS#2 error: Read/write test error
			F11A BATS#2 error: ECC test error
			F11B BATS#2 error: CRC test error
			F11C BATS#2 error: XOR test error
4	**44**	**00**	**Internal Target Failure**
			F203 Internal target failure
			F207 AHB Access Error
			F302 Motor: Unrecovered internal error
			F304 Motor: Unrecovered Open Loop Commutation error
			F306 Motor: Unrecovered No feedback detected error
			F308 Motor: Unrecovered Settle timeout
			F30A Motor: Unrecovered Gross speed error
			F30C Motor: Unrecovered 12V OK error
			F30E Motor: Unrecovered Speed error
			F312 Motor: Unrecovered internal 12V not OK timeout
			F446 Servo: Unrecovered DFT Sharp error
			F448 Servo: Unrecovered Load/Unload state error

1449 Servo: Recovered TFCR out-of-range error

F44A Servo: Unrecovered TFCR out-of-range error

F314 Motor: Unrecovered Inductive Sense measurment timeout

F316 Motor: Unrecovered Spin Sense speed error

F31A Motor: Unrecovered Target speed error

F31C Motor: Unrecovered Power driver version error

F31E Motor: Unrecovered Over current error

F322 Motor Unrecovered Negative regulator fault

F324 Motor Unrecovered Module overtemp error

F326 Motor Unrecovered 12V or 5V OK error

F328 Motor: Unrecovered unkown error

F402 Servo: Unrecovered Requested re zero head does not exist

F404 Servo: Unrecovered Back EMF movement in progress

F406 Servo: Unrecovered Back EMF timeout error

F408 Servo: Unrecovered ADC conversion timeout

F40A Servo: Unrecovered Load/unload calibration error

F40C Servo: Unrecovered Invalid 5 volts

F40E Servo: Unrecovered Invalid 12 volts

F410 Servo: Unrecovered Invalid harmonic requested

F412 Servo: Unrecovered Gain BEMF Calibration error

F414 Servo: Unrecovered VOFF BEMF calibration error

F416 Servo: Unrecovered Invalid temperature

F418 Servo: Unrecovered Truncated rezero

F41A Servo: Unrecovered Heads not loaded

F426 Servo: Unrecovered KT Seek out of range

F428 Servo: Unrecovered DAC Offset calibration error

F42A Servo: Unrecovered Load speed error

F42E Servo: Unrecovered ADC Calibration error

F430 Servo: Unrecovered ADC Offset error

F432 Servo: Unrecovered ADC Limit error

F434 Servo: Unrecovered Balancer Resistance error

F436 Servo: Unrecovered Balancer Resistance Limit error

F438 Servo: Unrecovered First Cylinder error

F43A Servo: Unrecovered Valid Cylinder error

F43C Servo: Unrecovered ADC Saturation error

F43E Servo: Unrecovered Latch Break timeout

F440 Servo: Unrecovered MR Resistance out of range error

F442 Servo: Servo: Unrecovered VCM Retract error

F444 Servo: Unrecovered Load Retry error

F446 Servo Unrecovered DFT Sharp error

F448 Servo Unrecovered Load/Unload state error

F44A Servo Unrecovered TCFR out-of-range error

F603 Channel/AE target failure

F604 Channel/AE calibration error

F607 Unrecovered data with PPM or precomp load

F609 Unrecovered data with TA detection

F60B Unrecovered with SMM or VM or DDF

F60D Unrecovered data pre-PPM or FH detection

F60F Unrecovered data write or pre-TA detection

F611 Unrecovered data with pre-SMM or VM or STM or DDF

F613 Unrecovered data with NRZ parity error

F615 Unrecovered parity PP correction or STW

F617 Unrecovered channel error

F619 Unrecovered AE thermal asperity found

F61B Unrecovered AE open MR element error

F61D Unrecovered AE IC over temperature error

F61F Unrecovered AE IP clock count error

F621 Unrecovered AE high MR current error

F623 Unrecovered AE write data BLS error

F625 Unrecovered AE invalud head address error

F627 Unrecovered AE power supply error

F629 Unrecovered AE open write head error

F62B Unrecovered AE write transition error

F62D Unrecovered AE no write head current error

F62F Unrecovered Channel Pre-TA error

F631 Unrecovered Channel write or NRZ parity error

F633 Unrecovered Channel write synch unlock error

F635 Unrecovered AE Short write read head error

F637 Unrecovered AE Short write head error

F639 Unrecovered AE Non-selected write head error

F63B Unrecovered AE Write current in read error

F63D Unrecovered AE IH open short error

F63F Unrecovered AE IH delay error

F641 Unrecovered Channel coarse tune timeout error

F643 Unrecovered AE Readback error

F645 Unrecovered Channel coarse tune timeout error

F713 Unrecovered ECC error

F717 Unrecovered overrun

F71B Unrecovered ECC overrun

F71D Unrecovered DRAM CRC error

			F71F Unrecovered ID Parity error F732 Unrecovered Write Fault F75A Unknown unrecovered error F799 Unrecovered ID not found error F7B5 Unrecovered Shock sensor error F7B9 Unrecovered Reference tag error F7BB Unrecovered Application tag error F7BD Unrecovered Channel write synthesis unlock error F7C1 Unrecovered End sector check error F7C3 Unrecovered Read CRC error F7C5 Unrecovered DRAM ECC error F7C7 Unrecovered DRAM ECC LBA error F7C9 Unrecovered DRAM ECC Write error FCxx Unable to read RID or FID number xx
4	**44**	**0B**	**Vendor Unique - Internal Target Failure** F310 Motor: Unrecovered Spindle Current error F318 Motor: Unrecovered Spin Sense timeout F320 Motor: Unrecovered System clock watchdog error F32A Motor: Unrecovered VCM DAC watchdog error
4	**44**	**F2**	**Vendor Unique - Internal Target Failure** F134 Head Health Check data compare error
4	**44**	**F6**	**Vendor Unique - Internal Target Failure** F135 Head Health Check unrecovered media error
4	**81**	**00**	**Vendor Unique - Internal Logic Error** F602 Channel/AE hard logic error F56E Log dump data corrupt F56F Log dump data memory error
			Sense Key = Illegal Request
5	**1A**	**00**	**Parameter List Length Error** 1509 Parameter list length error
5	**20**	**00**	**Invalid Command Operation Code** 150A Invalid opcode in CDB
5	**21**	**00**	**Logical Block Address out of Range** 150B LBA out of range
5	**24**	**00**	**Invalid Field in CDB** 150C Illegal request - invalid field in CDB 1542 SPC buffer not allocated
5	**24**	**F3**	**Vendor Unique - Illegal Request** 1545 Formatted without P-List
5	**25**	**00**	**Logical Unit Not Supported**

			150D Invalid LUN
5	**26**	**00**	**Invalid Field in Parameter List**
			150E Illegal request - invalid field in parameter list
			150F Saved parameter not supported
			1510 Unsupported log page
5	**26**	**04**	**Invalid Release of Active Persistent Reservation**
			1540 Invalid release of persistent reservation
5	**35**	**01**	**Unsupported Enclosure Function**
			1511 Unsupported enclosure services function (FC-AL only)
5	**49**	**00**	**Invalid Message Error**
			1512 Invalid message (SCSI only)
5	**55**	**00**	**System Buffer Full**
			1513 System buffer full
5	**55**	**04**	**Insufficient Registration Resources**
			1567 Insufficient registration resources
			Sense Key = Unit Attention
6	**28**	**00**	**Not Ready To Ready Transition (Format completed)**
			1514 Not ready to read transition
6	**29**	**00**	**Unit Attention - Login Reset**
			1515 Login reset (FC-AL only)
6	**29**	**01**	**Unit Attention - POR Occurred**
			1516 Power on reset
6	**29**	**02**	**Unit Attention - SCSI Bus Reset Occurred**
			1517 SCSI bus reset (SCSI), LIP Reset (FC-AL), SAS Hard Reset (SAS)
6	**29**	**03**	**Unit Attention - Bus Device Reset Occurred**
			1518 Bus device reset (SCSI only), Target Reset (FC-AL), LUN Reset (SAS)
6	**29**	**04**	**Unit Attention - Self Initiated Reset Occurred**
			1519 Self initiated reset
6	**29**	**05**	**Transceiver Changed to SE**
			151A Tranceiver changed to single-ended (SCSI only)
			1548 Hard Reset received
6	**29**	**06**	**Transceiver Changed to LVD**
			151B Tranceiver changed to LVD (SCSI only)
6	**29**	**07**	**I_T Nexus Loss Occurred**
			1554 I_T Nexus Loss Occurred (SAS only)
6	**2A**	**01**	**Mode Parameters Changed**
			151C Mode parameters changed
6	**2A**	**02**	**Log Parameters Changed**
			151D Log parameters changed
6	**2A**	**03**	**Reservations Preempted**

			151E Reservations pre-empted
6	**2A**	**04**	**Reservations Released**
			151F Reservations released
6	**2A**	**05**	**Registrations Released**
			1520 Registrations pre-empted
6	**2F**	**00**	**Commands Cleared by Another Initiator**
			1521 Commands cleared by another initiator
6	**2F**	**01**	Commands Cleared by Power Loss Notification
			1573 Commands cleared due to power failure event (SAS)
6	**3F**	**01**	**Microcode has been changed**
			1522 Microcode changed
6	**3F**	**03**	**Inquiry Parameters Changed**
			1523 Inquiry parameters changed
6	**3F**	**05**	**Device Identifier Changed**
			1537 Device identifier changed
6	**5D**	**00**	**Predictive Failure Analysis Threshold Reached**
			1524 PFA Threshold reached
6	**5D**	**FF**	**Predictive Failure Analysis Threshold Reached**
			1525 PFA Test warning
			Sense Key = Aborted Command
B	**1B**	**00**	**Synchronous Data Transfer Error**
			1527 Synchronous data transfer error (SCSI only)
			1528 ACK synchronization error (SCSI only)
B	**0E**	**00**	**Information Unit Too Long**
			1562 Information unit too long.
B	**25**	**00**	**Logical Unit Not Supported**
			1529 Unsupported LUN (SCSI only)
B	**3F**	**0F**	**Aborted Command - Echo Buffer Overwritten**
			1544 Echo buffer overwritten
B	**43**	**00**	**Message Error**
			152A Message reject error (SCSI only)
			152C Message parity error rcvd when no message sent by target (SCSI only)
B	**44**	**00**	**Internal Target Failure**
			152D Buffer CRC error on read
			152E Internal target failure - Host Interface
			154A Xfer Ready credit exceeded (FC-AL only)
			154B Xfer length error (FC-AL only)
			1568 End-to-End Data Protection Guard check
			1569 End-to-End Data Protection Application Tag check
			156A End-to-End Data Reference Tag check

156B ECC error in DRAM customer data area

156C Uncorrectable DRAM ECC error

1570 Host interface CRC error

F645 Unrecovered Channel coarse tune timeout

F75C   Internal media access timeout

F761   Read/write command timeout

F772   DASH starting timeout

F773   ID table timeout

F774   Servo timeout

F775   Buffers timeout

F776   DASH done timeout

F777   DASH unknown timeout

F77A   Unrecovered Channel SBW timeout

F77D   Recovery timeout

F7B1   Overall command timeout

F7D0 Pre-load timeout

17E0   Servo: Recovered read/write abort estimator error

F7E1 Servo: Unrecovered read/write abort estimator error

17E2 Servo: Recovered read/write abort predictor error

F7E3 Servo: Unrecovered read/write abort predictor error

17E4 Servo: Recovered read/write abort PES error

F7E5 Servo: Unrecovered read/write abort PES error

17E6 Servoe: Recovered read/write abort seek start error

F7E7 Servo: Unrecovered read/write abort seek start error

17E8 Servo: Recovered read/write abort PES reset error

F7E9 Servo: Recovered read/write abort PES reset error

17EA Servo: Recovered read/write abort SID unlock error

F7EB Servo: Unrecovered read/write abort SID unlock error

17EC Servo: Recovered read/write abort WCS other error

F7ED Servo: Unrecovered read/write abort WCS other error

17EE   Servo: Recovered read/write abort hard reset

F7EF Servo: Unrecovered read/write abort hard reset

17F0 Servo: Recovered read/write abort RV sensor error

F7F1 Servo: Unrecovered read/write abort RV sensor error

17F2 Servo: Recovered read/write abort RV sensor error

F7F3 Servo: Unrecovered read/write abort RV sensor error

17F4 Servo: Recovered read/write abort SHARP other error

F7F5 Servo: Unrecovered read/write abort SHARP other error

17F6 Servo: Recovered read/write abort SHARP exception error

F7F7 Servo: Unrecovered read/write abort SHARP exception error

			17F8 Servo: Recovered read/write abort SVGA limit error
			F7F9 Servo: Unrecovered read/write abort SVGA limit error
			17FA Servo: Recovered read/write abort gray code error
			F7FB Servo: Unrecovered read/write abort gray code error
			17FC Servo: Recovered read/write abort burst error
			F7FD Servo: Unrecovered read/write abort burst error
			17FE Servo: Recovered read/write abort no STM error
			F7FF Servo: Unrecovered read/write abort no STM error
B	**45**	**00**	**Select or Reselect Failure**
			152F Selection reselection error (SCSI only)
B	**47**	**00**	**SCSI Parity Error**
			1530 Message parity error - initiator (SCSI only)
			1531 Message parity error - target (SCSI only)
B	**47**	**03**	**Information Unit iuCRC Error Detected**
			1543 IU SCSI CRC error
B	**48**	**00**	**Initiator Detected Error Message Received**
			1532 Initiator detected error message received (SCSI only)
B	**49**	**00**	**Invalid Message Error**
			152B Attention dropped too late (SCSI only)
			1533 Inappropriate or illegal message (SCSI only)
B	**4B**	**00**	**Data Phase Error**
			153E Data phase error
B	**4B**	**01**	**Invaled Target Port Transfer Tag Received**
			1561 Information unit too short (SAS only)
B	**4B**	**02**	**Too Much Write Data**
			1560 Too much write data (SAS only)
B	**4B**	**03**	**ACK/NAK Timeout**
			1551 ACK/NAK Timeout (SAS only)
B	**4B**	**04**	**NAK Received**
			1550 NAK Received (SAS only)
B	**4B**	**05**	**Data Offset Error**
			1552 Bad parameter offset (SAS only)
B	**4B**	**06**	**Initiator Response Timeout**
			1555 Initiator response timeout (SAS only)
B	**0E**	**00**	**Information Unit Too Long**
			1562 Information unit too long.
B	**4E**	**00**	**Overlapped Commands Attempted**
			1534 Overlapped command attempted
B	**4F**	**00**	**Command Aborted Due To Loop Initialization**
			153F Abort by LIP (FC-AL only), Abort by OOB (SAS)

Sense Key = Miscompare			
E	**1D**	**00**	**Miscompare During Verify Operation**
			1535 Miscompare during verify

21.2.9 RU: Field Replaceable Unit (Byte 14)

The FRU (Field Replaceable Unit) field value will always be zero.

21.2.10 Sense Key Specific (Byte 15 through 17)

The definition of this field is determined by the value of the sense key field.

21.2.10.1 Sense Key Specific - Illegal Request (Sense Key = 5h)

Error field pointer is returned.

Table 199: Field Pointer Bytes

Byte	Bit							
	7	6	5	4	3	2	1	0
15	SKSV	C/D	Reserved		BPV	Bit Pointer		
16	(MSB)			Field Pointer				
17								(LSB)

SKSV Sense-key specific valid

- **0** Sense-key specific field is not valid.
- **1** Sense-key specific field is valid.

C/D Command/Data

- **0** Indicates that the illegal parameter was in the data parameters sent by the initiator during DATA OUT phase
- **1** Indicates that the illegal parameter was in the command descriptor block.

BPV Bit Pointer Valid

- **0** Bit pointer field is not valid.
- **1** Bit pointer field is significant.

Bit Pointer Indicates which bit of the byte number reported in Field Pointer is the bit in error. When a multiple bit field is in error, the pointer points to the most significant bit of the field.

Field Pointer Indicates which bytes of the command descriptor block or of the parameter data were in error. Bytes are numbered starting from zero, as shown in the tables describing the commands and parameters. When a multiple byte field id is in error, the pointer points to the most significant byte of that field.

21.2.10.2 Sense Key Specific - Recovered (Sense Key = 1h) or Medium (Sense Key = 3h) or Hardware (Sense Key =4h)

Hardware (Sense Key = 4h) or Medium Error (Sense Key = 3h)

Actual Retry Count is reported.

Table 200: Actual Retry Count

Byte	Bit							
	7	6	5	4	3	2	1	0
15	SKSV	Reserved						
16	Secondary Step				ERP Type			
17	Actual Retry Count							

SKSV Sense-key specific valid

0 Actual Retry Count is not valid.

1 Actual Retry Count is valid.

Actual Retry Count Number of retry steps used in attempting to recover from the error condition.

The content of these two bytes indicates:

0x04nn - nn recovery steps were used in the 'Minus Mod' branch of the recovery process,

0x02nn - nn recovery steps were used in the 'TA' branch of the recovery process,

0x01nn - nn recovery steps were used in the 'Sync Byte' branch of the recovery process.

Secondary Step Secondary error recovery step (valid for servo errors only).

ERP Type Error recovery table branch for this error. Valid values are shown in the table below.

Recovery Type	**ERP Type**
Read	0x00
Verify	0x01
Write	0x02
Seek	0x03
Read, Sync Byte branch	0x04
Read, Thermal Asperity branch	0x05
Read, Minus Mod branch	0x06
Verify, Sync Byte branch	0x07
Verify, Thermal Asperity branch	0x08
Verify, Minus Mod branch	0x09

21.2.10.3 Sense Key Specific - Not Ready (Sense key = 2h)

These fields are defined for the Format unit (04h) command with the Immediate bit set to one and the Send Diagnostic (1Dh) command with Background self-test function.

Progress indication is returned.

Table 201: Progress Indication

Byte	Bit							
	7	6	5	4	3	2	1	0
15	SKSV	Reserved						
16 17	(MSB) Progress Indication (LSB)							

SKSV Sense-key specific valid

0 Progress Indication is not valid.

1 Progress Indication is valid.

Progress Indication Indicates a percent complete in which the returned value is the numerator that has 10000h as its denominator.

21.2.11 Reserved (Byte 18 through 19)

Reserved fields are filled with zero.

21.2.12 Vendor unique error information (Byte 20 through 23)

This field gives detailed information about the error. It contains a unique code which describes where the error was detected and which piece of hardware or microcode detected the error depending on current operation.

21.2.13 Physical Error Record (Byte 24 thru 29)

- ILI = 1 - This field contains zeros.
- ILI = 0 - These bytes contain the physical location of the error in cylinder, head, and sector. Bytes 24, 25, and 26 are cylinder high, middle and low bytes respectively, of the cylinder number. Byte 27 is the head number. Bytes 28 and 29 are the high and low bytes, respectively of the sector number.

  If the head is undetermined, bytes 24, 25, and 26 are set to 0FFFFFFh. If the head number is undetermined, byte 27 is set to 0FFh. If cylinder, head, and sector have no relevance the error, bytes 24 through 29 will all be set to 0FFFFFFFFFFFFh for Valid = 0 and ILI = 0. This Physical Error Record field is valid for Sense Key 1, 3, and 4 only.

Valid	ILI	Description
1	0	Cylinder Number (bytes 24-26) Head number (byte 27) Sector Number (bytes 28-29)
1	1	0x000000000000
0	x	0x000000000000 - (not used/invalid)

21.2.14 Reserved (Byte 30 through 31)

Reserved fields are filled with zero.

22.0 Appendix. UEC list

Following is the list of Unit Error Codes and associated descriptions. The Unit Error Codes are returned by the target in sense data bytes 20-21.

The list of Unit Error Codes and descriptions does not have a direct correlation to the error descriptions and Sense Key/Code/Qualifier descriptions in Section 21.0, “SCSI Sense Data” on page 271. These codes are used internally by Hitachi and may change without notice.

How to find a specific UEC

The first hex digit of the UEC indicates the error severity, e.g. Fxxx codes are for hard/unrecoverable errors, 1xxx codes are for soft/recoverable errors, etc. The second hex digit indicates the grouping, e.g. interface, media, servo, etc. types of errors. The table is sorted without regard to the first hex digit; instead, sorting is by the least significant three hex digits. Unit Error Codes list.

Table 202: Unit Error Codes list

UEC	Description
----	--
0000	No error
F101	BATS error: Reserved Area - Invalid request
F102	BATS error: Reserved Area - Broken
F103	BATS error: Reserved Area - Invalid version
F104	BATS error: Reserved Area - Invalid checksum
F105	BATS error: Reserved Area - Invalid eyecatcher
F106	BATS error: Reserved Area - Invalid main header checksum
F107	BATS error: Reserved Area - Invalid read length
F108	BATS error: Reserved Area - Address boundary error
1109	BATS error: Reserved Area - Error reading first copy
110A	BATS error: Reserved Area - Error reading second copy
F10B	BATS error: Reserved Area - Read block error
110C	BATS error: Reserved Area - Write fix soft error
F10D	BATS error: Reserved Area - Write fix hard error
F10E	BATS error: Directory broken
F10F	BATS error: Overlay code load error
F110	BATS error: Overlay code check
F111	BATS error: RAM code load error
F112	BATS error: RAM code check
F113	BATS error: Config invalid
F114	BATS error: Log manager invalid
F115	BATS error: Media tables invalid
F116	BATS error: Logical-to-physical invalid
F117	BATS error: Defect manager invalid
F118	BATS#2 error: Seek test error
F119	BATS#2 error: Read/write test error

UEC	Description
F11A	BATS#2 error: ECC test error
F11B	BATS#2 error: CRC test error
F11C	BATS#2 error: XOR test error
F11D	Incorrect Disk Code
F11F	RPO SID invalid
F120	BATS error: Code Compatibility Failure
F121	BATS error: Code download in progress
F122	BATS error: Performance data read error
F123	Invalid request to enter sleep mode
F124	Bring-up error
F125	BATS error: Invalid RID/FID
F126	BATS error: Code checksum error
F127	BATS error: Invalid header
F128	DRAM test in progress
F129	DRAM test complete
F12A	DRAM test error
F12B	BATS error: Reserved area - invalid model
F12C	BATS error: Invalid code size
F130	Incorrect Customer Code
F131	Flash timeout
F132	GEM FH track read error
F133	BATS error: Vendor ID mismatch
F134	Head Health Check data compare error
F135	Head Health Check unrecovered media error
F136	BATS#2 error: End-To-End Data Protection error
1201	Error in UEC class
1202	Error in UEC cause
F203	Internal target failure
F204	Reassign reserved area media error
F205	G-list full - can't reassign any more sectors
F206	No spares available
F207	AHB Access Error
1301	Motor: Recovered internal error
F302	Motor: Unrecovered internal error
1303	Motor: Recovered Open Loop Commutation failure
F304	Motor: Unrecovered Open Loop Commutation failure
1305	Motor: Recovered No feedback detected error
F306	Motor: Unrecovered No feedback detected error
1307	Motor: Recovered Settle timeout

UEC	Description
F308	Motor: Unrecovered Settle timeout
1309	Motor: Recovered Gross speed error
F30A	Motor: Unrecovered Gross speed error
130B	Motor: Recovered 12V OK error
F30C	Motor: Unrecovered 12V OK error
130D	Motor: Recovered Speed error
F30E	Motor: Unrecovered Speed error
130F	Motor: Recovered Spindle Current error
F310	Motor: Unrecovered Spindle Current error
1311	Moto: Recovered Internal 12V not OK timeout
F312	Motor: Unrecovered Internal 12V not OK timeout
1313	Motor: Recovered Inductive Sense measurement timeout
F314	Motor: Unrecovered Inductive Sense speed error
F315	Motor: Recovered Spin Sense speed error
F316	Motor: Unrecovered Spin Sense speed error
1317	Motor: Recovered Spin Sense timeout
F318	Motor: Unrecovered Spin Sense timeout
1319	Motor: Recovered Target speed error
F31A	Motor: Unrecovered Target speed error
131B	Motor: Recovered Power driver version error
F31C	Motor: Unrecovered Power driver version error
131D	Motor: Recovered Over current error
F31E	Motor: Unrecovered Over current error
131F	Motor: Recovered System clock watchdog error
F320	Motor: Unrecovered System clock watchdog error
1321	Motor: Recovered Negative regulator fault
F322	Motor: Unrecovered Negative regulator fault
1323	Motor: Recovered Module overtemp error
F324	Motor: Unrecovered Module overtemp error
1325	Motor: Recovered 12V or 5V OK error
F326	Motor: Unrecovered 12V or 5V OK error
1327	Motor: Recovered unknown error
F328	Motor: Unrecovered unknown error
1329	Motor: Recovered VCM DAC watchdog error
F32A	Motor: Unrecovered VCM DAC watchdog error
1401	Servo: Recovered Requested rezero head does not exist
F402	Servo: Unrecovered Requested rezero head does not exist
1403	Servo: Recovered Back EMF movement in progress
F404	Servo: Unrecovered Back EMF movement in progress
1405	Servo: Recovered Back EMF timeout error

UEC	Description
F406	Servo: Unrecovered Back EMF timeout error
1407	Servo: Recovered ADC conversion timeout
F408	Servo: Unrecovered ADC conversion timeout
1409	Servo: Recovered Load/unload calibration error
F40A	Servo: Unrecovered Load/unload calibration error
140B	Servo: Recovered Invalid 5 volts
F40C	Servo: Unrecovered Invalid 5 volts
140D	Servo: Recovered Invalid 12 volts
F40E	Servo: Unrecovered Invalid 12 volts
140F	Servo: Recovered Invalid harmonic requested
F410	Servo: Unrecovered Invalid harmonic requested
1411	Servo: Recovered Gain BEMF Calibration error
F412	Servo: Unrecovered Gain BEMF Calibration error
1413	Servo: Recovered VOFF BEMF calibration error
F414	Servo: Unrecovered VOFF BEMF calibration error
1415	Servo: Recovered Invalid temperature
F416	Servo: Unrecovered Invalid temperature
1417	Servo: Recovered Truncated rezero
F418	Servo: Unrecovered Truncated rezero
1419	Servo: Recovered Heads not loaded
F41A	Servo: Unrecovered Heads not loaded
141B	Servo: Recovered No seek complete
F41C	Servo: Unrecovered No seek complete
141D	Servo: Recovered Seek timeout
F41E	Servo: Unrecovered Seek timeout
141F	Servo: Recovered Seek error
F420	Servo: Unrecovered Seek error
1421	Servo: Recovered Track following error
F422	Servo: Unrecovered Track following error
1423	Servo: Recovered Track follow timeout
F424	Servo: Unrecovered Track follow timeout
1425	Servo: Recovered KT Seek out of range
F426	Servo: Unrecovered KT Seek out of range
1427	Servo: Recovered DAC Offset calibration error
F428	Servo: Unrecovered DAC Offset calibration error
1429	Servo: Recovered Load speed error
F42A	Servo: Unrecovered Load speed error
142B	Servo: Recovered RRO Calibration timeout
F42C	Servo: Unrecovered RRO Calibration timeout
142D	Servo: Recovered ADC Calibration error
F42E	Servo: Unrecovered ADC Calibration error

UEC	Description
142F	Servo: Recovered ADC Offset error
F430	Servo: Unrecovered ADC Offset error
1431	Servo: Recovered ADC Limit error
F432	Servo: Unrecovered ADC Limit error
1433	Servo: Recovered Balancer Resistance error
F434	Servo: Unrecovered Balancer Resistance error
1435	Servo: Recovered Balancer Resistance Limit error
F436	Servo: Unrecovered Balancer Resistance Limit error
1437	Servo: Recovered First Cylinder error
F438	Servo: Unrecovered First Cylinder error
1439	Servo: Recovered Valid Cylinder error
F43A	Servo: Unrecovered Valid Cylinder error
143B	Servo: Recovered ADC Saturation error
F43C	Servo: Unrecovered ADC Saturation error
143D	Servo: Recovered Latch Break timeout
F43E	Servo: Unrecovered Latch Break timeout
143F	Servo: Recovered MR Resistance out of range error
F440	Servo: Unrecovered MR Resistance out of range error
1441	Servo: Recovered VCM Retract error
F442	Servo: Unrecovered VCM Retract error
1443	Servo: Recovered Load Retry error
F444	Servo: Unrecovered Load Retry error
1445	Servo: Recovered DFT Sharp error
F446	Servo: Unrecovered DFT Sharp error
1447	Servo: Recovered Load/Unload state error
F448	Servo: Unrecovered Load/Unload state error
1449	Servo: Recovered TFCR out-of-range error
F44A	Servo: Unrecovered TFCR out-of-range error
1501	Logical unit not ready
1502	Logical unit becoming ready
1503	Logical unit not ready - initializing command required
1504	Not ready - format in progress
1505	Not ready - self-test in progress
1506	Reassign failed
1507	Format failed
1509	Parameter list length error
150A	Invalid opcode in CDB
150B	LBA out of range
150C	Illegal request - invalid field in CDB
150D	Invalid LUN

UEC	Description
150E	Illegal request - invalid field in parameter list
150F	Saved parameter not supported
1510	Unsupported log page
1511	Unsupported enclosure services function (FC-AL only)
1512	Invalid message (SCSI only)
1513	System buffer full
1514	Not ready to ready transition
1515	Login reset (FC-AL only)
1516	Power on reset
1517	SCSI bus reset (SCSI), LIP reset (FC-AL), SAS Hard Reset (SAS)
1518	Bus device reset (SCSI), Target Reset (FC-AL), LUN Reset (SAS)
1519	Self initiated reset
151A	Tranceiver changed to single-ended (SCSI only)
151B	Tranceiver changed to LVD (SCSI only)
151C	Mode parameters changed
151D	Log parameters changed
151E	Reservations pre-empted
151F	Reservations released
1520	Registrations pre-empted
1521	Commands cleared by another initiator
1522	Microcode changed
1523	Inquiry parameters changed
1524	PFA threshold reached
1525	PFA test warning
1527	Synchronous data transfer error (SCSI only)
1528	ACK synchronization error (SCSI only)
1529	Unsupported LUN (SCSI only)
152A	Message reject error (SCSI only)
152B	Attention dropped too late (SCSI only)
152C	Message parity error rcvd when no message sent by target (SCSI only)
152D	Buffer CRC error on read
152E	Internal target failure
152F	Selection reselection error (SCSI only)
1530	Message parity error - initiator (SCSI only)
1531	Message parity error - target (SCSI only)
1532	Initiator detected error message received (SCSI only)
1533	Inappropriate or illegal message (SCSI only)
1534	Overlapped command attempted
1535	Miscompare during verify
1536	Reservation conflict
1537	Device identifier changed

UEC	Description
1539	ESI: unspecified failure (FC-AL only)
153A	ESI: unsupported function (FC-AL only)
153B	ESI: enclosure unavailable (FC-AL only)
153C	ESI: transfer failure (FC-AL only)
153D	ESI: tranfer refused (FC-AL only)
153E	Data phase error
153F	Abort by LIP (FC-AL), Abort by OOB (SAS)
1540	Invalid release of persistent reservation
1541	Low power condition on
1542	SPC buffer not allocated
1543	IU SCSI CRC error
1544	Echo buffer overwritten
1545	Formatted with No P-List
1548	Hard Reset received
154A	Xfer Ready credit exceeded (FC-AL only)
154B	Transfer length error (FC-AL only)
1550	NAK rcvd (SAS)
1551	ACK NAK Timeout (SAS)
1552	Bad parameter offset (SAS)
1553	LUN Not ready, Notify (Enable Spinup) required (SAS)
1554	I_T_Nexus Loss Occurred (SAS)
1555	Initiator Response Timeout (SAS)
1556	ESI transfer failed - write ack (FC-AL)
1557	ESI transfer failed - read ack (FC-AL)
1558	ESI transfer failed - write ready (FC-AL)
1559	ESI transfer failed - read ready (FC-AL)
155A	ESI transfer refused - write ack (FC-AL)
155B	ESI transfer refused - read ack (FC-AL)
155C	ESI transfer refused - write ready (FC-AL)
155D	ESI transfer refused - read ready (FC-AL)
155E	ESI transfer failed - EDV (FC-AL)
1560	Too much write data (SAS)
1561	Information unit too short (SAS)
1562	Information unit too long (SAS)
1567	Insufficient registration resources
1568	End-to-End Data Protection Guard check
1569	End-to-End Data Protection Application Tag check
156A	End-to-End Data Protection Reference Tag check
156B	ECC error in DRAM customer data area
156C	Uncorrectable DRAM ECC error
F56C	Log dump data corrupt

UEC	Description
F56F	Log dump data memory error
1570	Host interface CRC error
1572	LUN not ready; manual intervention required
1573	Commands cleared due to power failure event (SAS)
1601	Channel/AE internal logic error
F602	Channel/AE hard logic error
F603	Channel/AE target failure
F604	Channel/AE calibration error
1606	Recovered data with PPM or precomp load
F607	Unrecovered data with PPM or precomp load
1608	Recovered data with TA (Thermal Asperity) detection
F609	Unrecovered data with TA detection
160A	Recovered data with SMM or VM or DDF
F60B	Unrecovered data with SMM or VM or DDF
160C	Recovered data with pre-PPM or FH (Fly Height) detection
F60D	Unrecovered data with pre-PPM or FH detection
160E	Recovered data with write or pre-TA detection
F60F	Unrecovered data with write or pre-TA detection
1610	Recovered data with pre-SMM or VM or STM or DDF
F611	Unrecovered data with pre-SMM or VM or STM or DDF
1612	Recovered data with NRZ parity error
F613	Unrecovered data with NRZ parity error
1614	Recovered parity PP correction or STW
F615	Unrecovered parity PP correction or STW
1616	Recovered channel error
F617	Unrecovered channel error
1618	Recovered AE thermal asperity found
F619	Unrecovered AE thermal asperity found
161A	Recovered AE open MR element error
F61B	Unrecovered AE open MR element error
161C	Recovered AE IC over temperature error
F61D	Unrecovered AE IC over temperature error
161E	Recovered AE IP clock count error
F61F	Unrecovered AE IP clock count error
1620	Recovered AE high MR current error
F621	Unrecovered AE high MR current error
1622	Recovered AE write data BLS error
F623	Unrecovered AE write data BLS error
1624	Recovered AE invalid head address error
F625	Unrecovered AE invalid head address error

UEC	Description
1626	Recovered AE power supply error
F627	Unrecovered AE power supply error
F629	Unrecovered AE open write head error
162A	Recovered AE write transition error
F62B	Unrecovered AE write transition error
162C	Recovered AE no write head current error
F62D	Unrecovered AE no write head current error
162E	Recovered Channel Pre-TA error
F62F	Unrecovered Channel Pre-TA error
1630	Recovered Channel write or NRZ parity error
F631	Unrecovered Channel write or NRZ parity error
1632	Recovered Channel Write Synth Unlock error
F633	Unrecovered Channel Write Synth Unlock error
1634	Recovered AE Short write read head error
F635	Unrecovered AE Short write read head error
1636	Recovered AE Short write head error
F637	Unrecovered AE Short write head error
1638	Recovered AE Non-selected write head error
F639	Unrecovered AE Non-selected write head error
163A	Recovered AE Write current in read error
F63B	Unrecovered AE Write current in read error
163C	Recovered AE IH Open short error
F63D	Unrecovered AE IH Open short error
163E	Recovered AE IH Deleay error
F63F	Unrecovered AE IH Deleay error
1640	Recovered AE Write head encode error
F641	Unrecovered AE Write head encode error
1642	Recovered AE Readback error
F643	Unrecovered AE Readback error
1644	Recovered Channel coarse tune timeout
F645	Unrecovered Channel coarse tune timeout
F701	Format corrupted
F702	Too many notches
D703	Auto-reallocated due to write error
1704	Recommend reassign due to write error
E705	Media error with OTF correction - reassigned
E706	Media error with OTF correction - recommend reassign
E707	Media error with OTF correction - recommend rewrite
D708	Media error with OTF correction - rewritten
1709	Media error with offline correction

UEC	Description
D70A	Media error with offline correction - reassigned
E70B	Media error with offline correction - recommend reassign
E70C	Media error with offline correction - recommend rewrite
E70D	Media error with offline correction - rewritten
E70E	Recovered Data Address Mark error - rewritten
E70F	Recovered Data Address Mark error - recommend rewrite
D710	Recovered Data Address Mark error - reassigned
E711	Recovered Data Address Mark error - recommend reassign
1712	Recovered ECC error
F713	Unrecovered ECC error
1714	Recovered sector overflow
F715	Unrecovered sector overflow
1716	Recovered overrun
F717	Unrecovered overrun
1718	Recovered abort immediate
F719	Unrecovered abort immediate
171A	Recovered ECC overrun
F71B	Unrecovered ECC overrun
17B8	Recovered Reference tag error
171C	Recovered DRAM CRC error
F71D	Unrecovered DRAM CRC error
171E	Recovered ID Parity error
F71F	Unrecovered ID Parity error
1720	Recovered sudden death
F721	Unrecovered sudden death
1722	Recovered small thermal asperity
F723	Unrecovered small thermal asperity
D724	Recovered thermal asperity w/offline correction - reassigned
E725	Recovered thermal asperity w/offline correction - recommend reassign
E726	Recovered thermal asperity w/offline correction - recommend rewrite
E727	Recovered thermal asperity w/offline correction - rewritten
E728	Recovered thermal asperity - rewritten
E729	Recovered thermal asperity - recommend rewrite
E72A	Recovered thermal asperity - recommend reassign
D72B	Recovered thermal asperity - reassigned
172C	Recovered media error
F72D	Unrecovered media error
172E	Recovered media error (off-line correction discarded)
F72F	Unrecovered media error (off-line correction discarded)
1731	Recovered Write Fault
F732	Unrecovered Write Fault

UEC	Description
1733	Recovered read/write abort
F734	Unrecovered read/write abort
1737	Recovered post write abort
F738	Unrecovered post write abort
1739	Recovered Post PES check write abort
F73A	Unrecovered Post PES check write abort
173B	Recovered data address mark error
F73C	Unrecovered data address mark error
173D	Recovered sector miss
F73E	Unrecovered sector miss
1746	Defect list format not supported
1747	Primary defect list not found
1748	Grown defect list not found
1749	Partial defect list transferred
F74B	Primary defect list error
F74C	Grown defect list error
F74D	Too many heads
F74E	Skew table size error
F74F	Too many zones
F750	Too many SIDs
F751	Alternate track table full
F752	Drive capacity too small
F753	G-list full (Format command)
F754	G-list full (2) (Format command)
F755	Pointer repeat size error
F756	DST slot size error
F757	P-list full
F758	Spare list full
1759	Unknown recovered error
F75A	Unknown unrecovered error
F75B	Too many sectors
F75C	Internal media access timeout
F75D	Selftest failed
F75E	Max servo cylinder too small
F761	Read/write command timeout
1770	Recovered SID timeout
F771	Unrecovered SID timeout
F772	DASH starting timeout
F773	ID table timeout
F774	Servo timeout
F775	Buffers timeout

UEC	Description
F776	DASH done timeout
F777	DASH unknown timeout
F77A	Unrecovered channel SBW timeout
F77D	Recovery timeout
1792	Recovered Current pointer error
1794	Recovered Drive DMA timeout error
F795	Unrecovered Drive DMA timeout error
1798	Recovered ID not found error
F799	Unrecovered ID not found error
F7B1	Overall Command Timeout
17B2	Recovered abort window error
F7B3	Unrecovered abort window error
17B4	Recovered shock sensor error
F7B5	Unrecovered shock sensor error
F7B9	Unrecovered Reference tag error
17B8	Recovered Reference tag error
17BA	Recovered Application tag error
F7BB	Unrecovered Application tag error
17BC	Recovered Guard check error
F7BD	Unrecovered Guard check error
17BE	Recovered Channel write synthesis unlock error
F7BF	Unrecovered Channel write synthesis unlock error
17C0	Recovered End sector check error
F7C1	Unrecovered End sector check error
17C2	Recovered Read CRC error
F7C3	Unrecovered Read CRC error
17C4	Recovered DRAM ECC error
F7C5	Unrecovered DRAM ECC error
17C6	Recovered DRAM ECC LBA error
F7C7	Unrecovered DRAM ECC LBA error
17C8	Recovered DRAM ECC Write error
F7C9	Unrecovered DRAM ECC Write error
F7D0	Pre-load timeout
17E0	Servo Recovered read/write abort estimator error
F7E1	Servo Unrecovered read/write abort estimator error
17E2	Servo Recovered read/write abort predictor error
F7E3	Servo Unrecovered read/write abort predictor error
17E4	Servo Recovered read/write abort PES error
F7E5	Servo Unrecovered read/write abort PES error
17E6	Servo Recovered read/write abort seek start error
F7E7	Servo Unrecovered read/write abort seek start error

UEC	Description
17E8	Servo Recovered read/write abort PES reset error
F7E9	Servo Recovered read/write abort PES reset error
17EA	Servo Recovered read/write abort SID unlock error
F7EB	Servo Unrecovered read/write abort SID unlock error
17EC	Servo Recovered read/write abort WCS other error
F7ED	Servo Unrecovered read/write abort WCS other error
17EE	Servo Recovered read/write abort hard reset
F7EF	Servo Unrecovered read/write abort hard reset
17F0	Servo Recovered read/write abort RV sensor error
F7F1	Servo Unrecovered read/write abort RV sensor error
17F2	Servo Recovered read/write abort RV sensor error
F7F3	Servo Unrecovered read/write abort RV sensor error
17F4	Servo Recovered read/write abort SHARP other error
F7F5	Servo Unrecovered read/write abort SHARP other error
17F6	Servo Recovered read/write abort SHARP exception error
F7F7	Servo Unrecovered read/write abort SHARP exception error
17F8	Servo Recovered read/write abort SVGA limit error
F7F9	Servo Unrecovered read/write abort SVGA limit error
17FA	Servo Recovered read/write abort gray code error
F7FB	Servo Unrecovered read/write abort gray code error
17FC	Servo Recovered read/write abort burst error
F7FD	Servo Unrecovered read/write abort burst error
17FE	Servo Recovered read/write abort no STM error
F7FF	Servo Unrecovered read/write abort no STM error
1A02	SMART: Temperature warning (no sense)
2A02	SMART: Temperature warning (recovered sense)
3A02	SMART: Temperature warning (unit attn sense)
1A03	SMART: Background selftest warning (no sense)
2A03	SMART: Background selftest warning (recovered sense)

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